



## Internship Modules for Undergraduate / Postgraduate Students

### Stream 1 : Analog Design

This module familiarises typical analog design flow. The fundamental concepts will be covered including Differential Amplifiers, Operational Amplifiers, Voltage References, and Filters. A simulation based design project will be executed by the student starting from specifications.

### Stream 2 : Digital Design

This module familiarises typical digital design flow. The fundamental concepts will be covered including simulation and FPGA implementation of digital logics including some architectures for digital processors. A simulation based design project will be executed by the student starting from specifications.

### Stream 3 : Mixed Signal Design

This module familiarises analog to digital converters and digital to analog converters with concepts including sampling, comparator design, opamp design, and various ADC/ DAC architectures. A simulation based design project will be executed by the student starting from specifications.

### Stream 4 : RF Design

This module familiarises RF concepts including nonlinearity, noise, wireless transceiver architectures, Low Noise Amplifiers Mixers, Oscillators. A simulation based design project will be executed by the student starting from specifications.

### Contact Person

**Dr Nithin M,**  
**Assistant Professor,**  
**Department of ECE, RVCE**  
**Ph +91 88612 25913**