

RV COLLEGE OF ENGINEERING[®] (Autonomous Institution Affiliated to VTU, Belagavi) R.V. Vidyaniketan Post, Mysore Road Bengaluru – 560 059



Scheme and Syllabus for I to VI Semesters

2018 SCHEME

MASTER OF COMPUTER APPLICATIONS



(Autonomous Institution affiliated to VTU, Belagavi)



Department of Master of Computer Applications

Scheme and Syllabus of Autonomous System w.e.f 2018

VISION

Pioneering in ICT Enabled Quality Education and Research with a focus on Sustainable and Inclusive Applications

MISSION

- **M1** To adapt novel methodologies for quality education through experiential learning
- M2 To empower students with continuous, holistic education, emphasizing on discipline,

ethics and social commitment

- M3 To become a vibrant knowledge center for research and software development
- **M4** To continuously build capacity steering towards industry- institute collaborative research and entrepreneurial competencies
- **M5** To utilize and develop free and open source software tools for sustainable and inclusive growth

Program Outcomes (PO)

MCA graduates will be able to

- **PO1 Computational Knowledge:** Acquire in-depth computational knowledge and mathematics with an ability to abstract and conceptualize models from defined problems and requirements
- **PO2 Problem Analysis:** Identify, formulate, conduct literature survey and solve complex computing problems through analysis as well as provide optimal solutions
- **PO3 Design** / **Development of Solutions:** Design and evaluate solutions for complex problems, components or processes that meet specified needs after considering public health and safety, cultural, societal, and environmental factors
- **PO4 Conduct investigations of complex Computing problems:** Conduct literature survey to analyze and extract information relevant to unfamiliar problems and synthesize information to provide valid conclusions and interpret data by applying appropriate research methods, tools and design experiments
- **PO5 Use of Modern Tool**: Create, select, adapt and apply appropriate techniques, resources, and modern IT tools to complex computing system activities, with an understanding of the limitations
- **PO6 Professional Ethics:** Understand and commit to professional ethics and cyber regulations, responsibilities, and norms of professional computing practices
- **PO7 Life-long Learning:** Engage in lifelong learning independently for continual development to improve knowledge and competence as a computing professional
- **PO8 Project management and finance:** Demonstrate knowledge and understanding of management principles and apply these to multidisciplinary software development as a team member and manage projects efficiently as a leader considering economical and financial factors
- **PO9 Communication Efficacy:** Understand and communicate effectively with the computing community and with society at large, regarding complex computing systems activities confidently and effectively by writing effective reports and design documentations by adhering to appropriate standards, make effective presentations and give / receive clear instructions
- **PO10** Societal and Environmental Concern: Understand responsibilities and consequences based on societal, environmental, health, safety, legal and cultural issues within local and global contexts relevant to professional computing practices
- **PO11 Individual and Team Work:** Function effectively as an individual, as a member or leader in diverse teams in multidisciplinary environments
- **PO12 Innovation and Entrepreneurship:** Identify a timely opportunity for entrepreneurship and use innovation to pursue and create value addition for the betterment of the individual and society at large

R. V. College of Engineering, Bengaluru – 59. (An Autonomous Institution affiliated to VTU, Belagavi) **Department of Master of Computer Applications**

	I Semester MCA											
SI.	Course	Course Title	BoS	Cre	dit Alloca	tion	Total					
No	Code			Lecture Tutorial Practice		credits						
1.	18MAT11	Discrete Mathematics	MAT	3	1	0	4					
2.	18MCA12	Computer Organization	MCA	3	0	0	3					
		and Architecture										
3.	18MCA13	Data Structures using C	MCA	3	1	1	5					
4.	18MCA14	Object Oriented	MCA	3	1	1	5					
		Programming										
5.	18MCA15	Web Programming MCA		3	1	1	5					
		Total		15	4	3	22					
		Contact (Hrs/week)+		15	8	6	30					
		Counseling										

	II Semester MCA											
SI.	Course	Course Title	BoS	Cre	edit Alloca	tion	Total					
No	Code			Lecture	Tutorial	Practice	credits					
1.	18MCA21	Software Engineering	MCA	3	0	0	3					
2.	18MCA22	E-Commerce	MCA	3	1	0	4					
3.	18MCA23	Data Base Systems	MCA	3	1	1	5					
4.	18MCA24	Operating Systems	MCA	3	1	1	5					
5.	18MCA25	Java Programming	MCA	3	1	1	5					
		Total		15	4	3	22					
		Contact (Hrs/week)		15	8	6	30					
		+Counseling										

SI.	Course	Course Title	BoS	Cr	edit Alloca	tion	Total
No	Code	le		Lecture	Tutorial	Practice	credits
1.	18MCA31	Research Methodology	MCA	3	0	0	3
2.	18MCA32	Analysis and Design of Algorithms	MCA	3	1	1	5
3.	18MCA33	Computer Networks	MCA	3	1	1	5
4.	18MCA34X	Elective I	MCA	3	1	0	4
5.	18MCA35X	Elective – II (With Practice)	MCA	4	0	1	5
		Total		16	3	3	22
		Contact (Hrs./week) +		16	6	6	30
		Counseling Placement Activity					(28+2)

III Semester MCA

III Semester Electives

	Elective – I	Elective – II (With Practice)			
Course	Course Title	Course Code	Course		
Code					
18MCA341	Non-Relational Databases	18MCA351	Content Management System		
	(NOSQL)				
18MCA342	Operations Research	18MCA352	Advanced OOPS		
18MCA343	Machine Learning	18MCA353	Model View Controller		
			Programming		

	IV Semester MCA										
SI.	Course	Course Title	BoS	Cre	ation	Total					
No	Code			Lecture	Tutorial	Practice	credits				
1.	18MCA41	Software Testing	MCA	3	0	1	4				
2.	18MCA42	Mobile Application Development	MCA	3	0	1	4				
3.	18MCA43X	Elective III	MCA	3	0	0	3				
4.	18MCA44X	Elective – IV	MCA	3	0	0	3				
5.	18MCA45X	Elective – V (with Practice)	MCA	3	1	1	5				
6.	18MCA46	Minor Project– I	MCA	0	0	3	3				
		Total		15	1	6	22				
		Contact (Hrs./week)		15	2	12	31				
		+Counseling+					(29+2)				
		Placement Activities									

		IV Sem	ester Electives				
Ele	ctive – III	Elec	tive – IV	Elective – V (With Practice)			
Course	Course Title	Course	Course Title	Course	Course Title		
Code		Code		Code			
18MCA43	Information	18MCA441	Cyber Security	18MCA451	Computer Graphics		
1	Retrieval						
18MCA43	Pattern	18MCA442	Soft	18MCA452	Advanced		
2	Recognition		Computing		Computer		
					Networks		
18MCA43	Software	18MCA443	Unified	18MCA453	Enterprise		
3	Architecture		Modeling		Application		
			Language		Programming		

V Semester MCA										
SI.	Course	Course Title	BoS	Cr	edit Alloca	tion	Total			
No	Code			Lecture	Tutorial	Practice	credits			
1.	18MCA51	Software Project Management	MCA	3	0	0	3			
2.	18MCA52	Big Data Analytics	MCA	3	1	1	5			
3.	18MCA53X	Elective – VI	MCA	3	0	0	3			
4.	18MCA54X	Elective – VII (with Practice)	MCA	3	1	1	5			
5.	18MCA55	Seminar-1	MCA	0	0	2	2			
6.	18MCA56	Minor Project – II	MCA	0	0	4	4			
7.		Total		12	2	8	22			
		Contact (Hrs./week) + Counseling		12	4	16	32			

	Electives V Semester								
	Elective – VI	Elective VII (With Practice)							
Course Code	Course Title	Course Code	Course Title						
18MCA531	Wireless Mobile Networks	18MCA541	Cloud Computing						
18MCA532	Software Performance Engineering	18MCA542	Internet of Things						
18MCA533	Principles of UI/UX Design	18MCA543	Virtual Reality						

	VI Semester MCA									
Sl.	Course	Course Title	BoS	Cı	redit Allocati	ion	Total			
No	Code			Lecture	Tutorial	Practice	credits			
1	18MCA61	Project & Viva Voce	MCA	-	-	20	20			
2	18MCA62	Seminar-2	MCA	-	-	2	2			
		Total		-	-	22	22			

	Proposed Credits for the MCA Programme										
	Total Credits										
Semeste	Core	Elective	Project /	Seminar	Total Credits						
r			Industry								
			internship								
Ι	22	00	00	00	22						
II	22	00	00	00	22						
III	13	09	00	00	22						
IV	08	11	03	00	22						
V	08	08	04	02	22						
VI	00	00	20	02	22						
Total	73	28	27	04	132						

I - SEMESTER								
DISCRETE MATHEMATICS (Theory)								
Course Code	:	18MAT		CIE Mai	rks	:	100	
Hrs/Week	:		3:1:0	SEE Ma		:	100	
Credits	:			SEE Dur		:	3 Hrs	
Unit – I							10 Hrs	
		-		et theory, Principle inclusion and exclus		nd	exclusion	
			Unit – II				10 Hrs	
Cartesian Produ Hasse diagram,	Relations and Functions Cartesian Product and relations, Properties of Relations, Zero-one matrices and directed graphs, Hasse diagram, Equivalence relations and partitions, Functions-types of functions, Special Functions, Function composition and Inverse function.							
			Unit – II				10 Hrs	
The rules of su	Principle of counting The rules of sum and product, Permutations and Combinations-Combinations with repetitions, Derangements, Rook polynomials, Arrangements with Forbidden positions.							
			Unit – IV				08 Hrs	
Fundamentals Basic connectiv Definitions and	rity ai	nd Truth	m.	uivalence, logical in	mplications, (Qua		
			Unit – V				10 Hrs	
Planar Graphs, Adjacency matri	Ham x.	-	• • •	ents and Graph Iso Matrix representat	-		0	
Course Outcon								
	-		the student will be concepts of sets, co	able to: unting, logic, relatio	ns, functions a	and	graph	
for differe	ent do	mains in c	omputer science	g, logic, relations, fu inting, reasoning, rel	C		5	
theory to CO4 : Implement	solve t over	the proble all mather	ms and optimize th natical knowledge	0	0		0 1	
		g in practi	cal situation					
References Boo	ks:							
Introductio	1 Ralph P Grimaldi, B.V.Ramana, "Discrete and Combinatorial Mathematics. An applied Introduction", 5 th Edition, Pearson Education, 2007, ISBN-10:8177584243, ISBN-13:9788177584240.							
Hill, 3 rd Eo	lition,	2007, ISE	3N: 978-0-161587-					
2010, ISB	3 Kenneth H Rosen, " Discrete Mathematics & its Applications", 7 th Edition, McGraw-Hill, 2010, ISBN-10: 0073383090, ISBN-13: 978-0-073383095.							
U U				"Graph Theory-Mo - 978-81-317-1728-	0 11	lica	tions and	

Introduction to Digital Electronics Logic gates: Basic Gates, Universal Gate , the exclusive OR gate, Equivalence Gates Combinational circuits: Adders, Subtracters, Binary Parallel Adder, Decimal Adder, Decoders Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip-Flops. Unit – III 07Hrs Basic Structure of Computer and Machine Instructions. 07Hrs Computer Types, Functional Units, Basic Operational Concepts Bus structures, Performance Memory Location and Addresses , Machine Instruction and Program Memory Operations Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. Unit – IV 07Hrs IA-32 Architecture 07Hrs IA-32 Register Structure, IA-32 Addressing modes, Machine Instructions format, IA-3 instructions Init – V 07 Hrs IA-32 Programming Unit – V 07 Hrs IA-32 Programming Program Flow Control: Conditional and Unconditional Jumps, Shift and Rotate Instructions Programming examples. Course Outcomes Course Outcomes After going through this course, the student will be able to: CO1: Illustrate the concepts of digital system and assembly language in solving problems CO2: Apply basic concepts of digital system and assembly language programs CO4: Justify the solutions selected for a problem Reference Books:<	I – SEMESTER									
Course Code 18MCA12 CIE Marks 100 Hrs/Week 1.T.P. 3:0:0 SEE Marks 100 Credits 3 3 3 Hr Introduction to Boolean Algebra Number systems and Boolean Algebra: Number systems, binary codes, Axiomatic Definition of Boolean Algebra, Basic Theorems and Properties of Boolean Algebra, Boolean Functions Canonical and Standard Forms, Expression simplification using Boolean theorem and K-map. 07 Hrs Introduction to Digital Electronics 07 Hrs Logic gates: Basic Gates, Universal Gate , the exclusive OR gate, Equivalence Gates Combinational circuits: Rip – Flops, Triggering of Flip - Flops. Wultiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip - Flops. 07 Hrs Basic Structure of Computer and Machine Instructions. 07 Hrs Gomputer Types, Functional Units, Basic Operational Concepts Bus structures, Performance Memory Location and Addresses , Machine Instruction and Program Memory Operations: Introductions Unit – IV 07 Hrs IA-32 Architecture IA-32 Addressing modes, Machine Instructions format, IA-33 Instructions Unit – IV 07 Hrs IA-32 Register Structure, IA-32 Addressing modes, Machine Instructions format, IA-32 Instructions Program Flow Control: Conditional and Unconditional Jumps, Shift and Rota	COMPUTER ORGANIZATION AND ARCHITECTURE									
Hrs/Week : L:T:P 3:0:0 SEE Marks : 100 Credits : 3 Int SEE Duration : 3 Hr Introduction to Boolean Algebra Number systems and Boolean Algebra: Number systems, binary codes, Axiomatic Definition of Boolean Algebra, Basic Theorems and Properties of Boolean Algebra, Boolean Functions Canonical and Standard Forms, Expression simplification using Boolean theorem and K-map. Unit – II 07 Hrs Introduction to Digital Electronics Logic gates: Basic Gates, Universal Gate , the exclusive OR gate, Equivalence Gates: Combinational circuits: Adders, Subtracters, Binary Parallel Adder, Decimal Adder, Decoders: Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip-Flops. 07 Hrs Basic Structure of Computer and Machine Instructions. 07Hrs Basic Structure of Computer and Machine Instructions. 07Hrs Memory Location and Addresses , Machine Instruction and Program Memory Operations 107Hrs Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. 07Hrs IA-32 Register Structure, IA-32 Addressing modes, Machine Instructions format, IA-33 instructions 101 – V 07Hrs IA-32 Programming Program Flow Control: Conditional and Unconditional Jumps, Shift and Rotate Instructions Program SO3: Analyse the working of digital system, its organization and architecture			(Theory)							
Credits : 3 Introduction : 3 Hr Unit – I 08 Hrs Introduction to Boolean Algebra: Number systems and Boolean Algebra: Number systems, binary codes, Axiomatic Definition of Boolean Algebra, Basic Theorems and Properties of Boolean Algebra, Boolean Functions: Canonical and Standard Forms, Expression simplification using Boolean theorem and K-map. 07 Hrs Introduction to Digital Electronics 07 Hrs Logic gates: Basic Gates, Universal Gate , the exclusive OR gate, Equivalence Gates Gates Combinational circuits: Adders, Subtracters, Binary Parallel Adder, Decimal Adder, Decoders Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip- Flops. 07 Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip- Flops. 07 Mrs Basic Structure of Computer and Machine Instructions. 07 Gommarco Computer Types, Functional Units, Basic Operational Concepts Bus structures, Performance Memory Operations Introduction t Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. 07 IA-32 Register Structure, IA-32 Addressing modes, Machine Instructions format, IA-33 instructions Instructions	Course Code	:	18MCA12	CIE Marks	:	100				
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Introduction to Boolean Algebra Number systems and Boolean Algebra: Number systems, binary codes, Axiomatic Definition of Boolean Algebra, Basic Theorems and Properties of Boolean Algebra, Boolean Functions: Canonical and Standard Forms, Expression simplification using Boolean theorem and K-map. Unit – II 07 Hrs Introduction to Digital Electronics 07 Hrs Logic gates: Basic Gates, Universal Gate , the exclusive OR gate, Equivalence Gates: Combinational circuits: Adders, Subtracters, Binary Parallel Adder, Decimal Adder, Decoders: Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip- Flops. Unit – III 07Hrs Basic Structure of Computer and Machine Instructions. 07Hrs Computer Types, Functional Units, Basic Operational Concepts Bus structures, Performance Memory Location and Addresses , Machine Instruction and Program Memory Operations; Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. 07Hrs IA-32 Architecture IA-32 Addressing modes, Machine Instructions format, IA-3 instructions Instructions Unit – IV 07Hrs IA-32 Programming Orgram Flow Control: Conditional and Unconditional Jumps, Shift and Rotate Instructions Programming examples. CO3: Apply basic concepts of digital system, its organization and architecture CO3: Apply basic concepts of digital system, its organization and architecture CO3: Apply basic concepts of digital logic c	Credits	:		SEE Duration	:					
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Boolean Algebra, Basic Theorems and Properties of Boolean Algebra, Boolean Functions Canonical and Standard Forms, Expression simplification using Boolean theorem and K-map. Unit – II 07 Hrs Introduction to Digital Electronics Ogg gates: Basic Gates, Universal Gate , the exclusive OR gate, Equivalence Gates Combinational circuits: Adders, Subtracters, Binary Parallel Adder, Decimal Adder, Decoders Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip- Flops. Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip- Flops. 07Hrs Basic Structure of Computer and Machine Instructions. 07Hrs Computer Types, Functional Units, Basic Operational Concepts Bus structures, Performance Memory Location and Addresses , Machine Instruction and Program Memory Operations Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. Unit – IV 07Hrs IA-32 Architecture IA-32 Architecture IA-32 Register Structure, IA-32 Addressing modes, Machine Instructions format, IA-33 instructions Unit – V 07 Hrs IA-32 Programming Program Flow Control: Conditional and Unconditional Jumps, Shift and Rotate Instructions Program Flow Control: Conditional and Unconditional Jumps, Shift and Rotate Instructions CO1: Illustrate the concepts of digital system and assembly language in solving problems <t< td=""><td></td><th></th><td>0</td><th></th><th></th><th></th></t<>			0							
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Unit – II 07 Hrs Introduction to Digital Electronics Logic gates: Basic Gates, Universal Gate , the exclusive OR gate, Equivalence Gates Combinational circuits: Adders, Subtracters, Binary Parallel Adder, Decimal Adder, Decoders Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip-Flops. Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip-Flops. 07Hrs Basic Structure of Computer and Machine Instructions. 07Hrs Computer Types, Functional Units, Basic Operational Concepts Bus structures, Performance 07Hrs Memory Location and Addresses , Machine Instruction and Program Memory Operations Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. 07Hrs IA-32 Architecture IA-32 Register Structure, IA-32 Addressing modes, Machine Instructions format, IA-3 instructions Init – V 07Hrs IA-32 Programming Unit – V 07 Hrs IA-32 Programming Course Outcomes After going through this course, the student will be able to: CO1: Illustrate the concepts of digital system, its organization and architecture CO2: Apply basic concepts of digital system, and assembly language in solving problems CO2: Apply basic concepts of digital logic circuits and assembly language programs CO4: Justify the solutions selected for a problem Meference Books:	Boolean Algebr	a,	Basic Theorems and Properties of Bo	olean Algebra, Boolea	n	Functions,				
Introduction to Digital Electronics Logic gates: Basic Gates, Universal Gate , the exclusive OR gate, Equivalence Gates Combinational circuits: Adders, Subtracters, Binary Parallel Adder, Decimal Adder, Decoders Multiplexers. Sequential Circuits: Flip – Flops, Triggering of Flip- Flops. Unit – III 07Hrs Basic Structure of Computer and Machine Instructions. 07Hrs Computer Types, Functional Units, Basic Operational Concepts Bus structures, Performance Memory Operations Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. Voit – IV 07Hrs IA-32 Register Structure, IA-32 Addressing modes, Machine Instructions format, IA-3 instructions Instructions Program Flow Control: Conditional and Unconditional Jumps, Shift and Rotate Instructions Programming examples. 07 Hrs Course Outcomes After going through this course, the student will be able to: CO1: Illustrate the concepts of digital system, its organization and architecture CO2: Apply basic concepts of digital system and assembly language in solving problems CO4: Justify the solutions selected for a problem Reference Books: 1 M. Morris Mano, " Digital Logic and Computer design", Pearson Education Ltd., 2016 ISBN: 13 9789332542525 Carl Hamacher, Z Varnesic and S Zaky, " Computer Organization", Tata McGraw-Hill, 5 Edition 2012, ISBN: 13 978125900527	Canonical and S	tan		ng Boolean theorem and	d F	-				
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Unit – III 07Hrs Basic Structure of Computer and Machine Instructions. Computer Types, Functional Units, Basic Operational Concepts Bus structures, Performance Memory Location and Addresses , Machine Instruction and Program Memory Operations Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. IA-32 Architecture Unit – IV 07Hrs IA-32 Register Structure, IA-32 Addressing modes, Machine Instructions format, IA-3 instructions Instructions Instructions Voit – V 07 Hrs IA-32 Programming Program Flow Control: Conditional and Unconditional Jumps, Shift and Rotate Instructions Programming examples. Course Outcomes After going through this course, the student will be able to: CO1: Illustrate the concepts of digital system, its organization and architecture CO2: Apply basic concepts of digital system and assembly language in solving problems CO3: Analyse the working of digital logic circuits and assembly language programs CO4: Justify the solutions selected for a problem Efference Books: I 1 M. Morris Mano, " Digital Logic and Computer design", Pearson Education Ltd., 2016 ISBN: 13 978032542525 Carl Hamacher, Z Varnesic and S Zaky, " Computer Organization", Tata McGraw-Hi	Combinational of	irc	cuits: Adders, Subtracters, Binary Paralle	l Adder, Decimal Adde	er,	Decoders,				
Basic Structure of Computer and Machine Instructions. Computer Types, Functional Units, Basic Operational Concepts Bus structures, Performance Memory Location and Addresses , Machine Instruction and Program Memory Operations Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction t Microprocessor based computer system. Unit – IV O7Hrs IA-32 Architecture IA-32 Architecture IA-32 Programming Unit – V O7 Hrs IA-32 Programming Programming Unit – V O7 Hrs IA-32 Programming Programming Unit – V O7 Hrs IA-32 Programming Programming Programming Course Outcomes After going through this course, the student will be able to: CO1: Illustrate the concepts of digital system, its organization and architecture CO2: Apply basic concepts of digital logic circuits and assembly language programs CO4: Justify the solutions selec	Multiplexers. Se	qu	· · · · · · · · · · · · · · · · · · ·	Flip- Flops.						
Computer Types, Functional Units, Basic Operational Concepts Bus structures, Performance Memory Location and Addresses , Machine Instruction and Program Memory Operations Instructions, Instruction Sequencing, Basic Input/output Operations, Introduction to Microprocessor based computer system. Init – IV 07Hrs IA-32 Architecture IA-32 Architecture IA-32 Register Structure, IA-32 Addressing modes, Machine Instructions format, IA-3 instructions INIT – V IA-32 Programming 07 Hrs Program Flow Control: Conditional and Unconditional Jumps, Shift and Rotate Instructions Programming examples. Course Outcomes After going through this course, the student will be able to: CO1: Illustrate the concepts of digital system, its organization and architecture CO2: Apply basic concepts of digital system and assembly language in solving problems CO3: Analyse the working of digital logic circuits and assembly language programs CO4: Justify the solutions selected for a problem Reference Books: 1 M. Morris Mano, " Digital Logic and Computer design", Pearson Education Ltd., 2016 ISBN: 13 978032542525 2 Carl Hamacher, Z Varnesic and S Zaky, "Computer Organization", Tata McGraw-Hill, 5 Edition 2012, ISBN: 13 9781259005275 3 Charles H. Roth, Jr. and Larry L. Kinney, "Fundamentals of Logic Design", Cengag Learning 7th Edition 2014, ISBN: 10:1-133-62847-8						07Hrs				
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Edition 2012, ISBN: 13 97812590052753Charles H. Roth, Jr. and Larry L. Kinney, "Fundamentals of Logic Design", Cengag Learning 7th Edition 2014, ISBN: 10:1-133-62847-8				ign", Pearson Education	ı I	Ltd., 2016,				
3 Charles H. Roth, Jr. and Larry L. Kinney, "Fundamentals of Logic Design", Cengag Learning 7th Edition 2014, ISBN: 10:1-133-62847-8				Prganization" , Tata McC	Gra	w-Hill, 5 th				
	3 Charles H	. F	Roth, Jr. and Larry L. Kinney, "Fundar	nentals of Logic Desig	'n"	, Cengage				
4 Bob Colwell and Tom Shanley, "The Unabridged Pentium 4 IA32 Processor Genealogy" Addison-Wesley Professional, 2004, ISBN: 032124656X	4 Bob Colw	ell	and Tom Shanley, "The Unabridged Per		G	enealogy",				

		I – SEMESTER			
DATA STRUCTURES USING C (Theory & Practice)					
Course Code	:	18MCA13	CIE Marks	:	100+50
Hrs/Week	:	L:T:P 3:1:2	SEE Marks	:	100+50
Credits	:	5	SEE Duration	:	3 Hrs
Graduates shal 1. Unders 2. Implem 3. Identify	Course Objectives Graduates shall be able to: 1. Understand the fundamentals of Data Structures 2. Implement different data structures like stacks, queues, linked lists, trees and graphs 3. Identify different data structures and its applications 4. Solve problems by using data structures for different applications				
		Unit – I			06Hrs
Types of Data Structures –Primitive , Composite and Abstract Data, Linear and Non-Linear Data Structures , Applications of Data Structures, Pointers and Dynamic Memory Allocation Linear Data Structures – Arrays: Static and Dynamic, Multi-Dimensional Arrays, Basic Operations on Linear Data Structures Unit – II 08Hrs Stacks and Queues Stacks, Applications of Stacks: Evaluations of Expressions, Queues, Circular Queues , Priority Queues				08Hrs	
、		Unit – III			08Hrs
Linked Lists Singly Linked	Linked Lists Singly Linked lists, Linked Stacks and Queues, Doubly Linked Lists, Circular Linked Lists				
		Unit – IV			08 Hrs
	Trees & Hashing Binary Trees, Binary Tree Traversals, Binary Search Trees ,Heaps, Hashing: Static Hashing				
		Unit – V			06Hrs
Depth First Sea	et] arc]	ing Data Type: Definitions, and Representation h, Breadth First Search ort, Insertion Sort, Selection Sort	ons, Elementary Graph	С	perations:

Unit – VI (Lab Component)

Students are required to implement the following using C

- 1. Searching techniques using arrays (Linear and Binary)
- 2. Operations for a String based Stack
- 3. Evaluate postfix expression in a compiler
- 4. Basic queue operations
- 5. Task Scheduling using Priority Queues
- 6. Working of a singly linked list
- 7. Binary Search Tree traversal techniques
- 8. Sorting techniques (Bubble Sort, Insertion Sort, Selection Sort)
- 9. Depth First Search Traversal to identify the connectivity of a graph
- 10. Breadth First Search to display the reachable nodes

Note :

- 1. Experiment No.6, students are required to demonstrate any two operations specified by the examiners during CIE and SEE with display operation as a mandatory
- 2. Experiment No.8, students are required to demonstrate any two sorting technique specified by the examiners during CIE and SEE

Course Outcomes

After going through this course the student will be able to:

CO1: Understand data abstraction and data structures

CO2: Identify relevant data structures to develop solutions for a particular problem

CO3: Apply relevant data structures for different applications

CO4: Examine the use of different data structures in various applications

Reference Books

- 1. Horowitz, Sahni, Anderson-Freed: "Fundamentals of Data Structures in C", 2nd Edition, University Press,2012 ISBN: 978-81-7371-605-8
- 2. Michael T. Goodrich, Roberto Tamassia, Michael H. Goldwasser, "Data Structures and Alogorithms in Python"2013, Wiely Publications *ISBN*-10: 1118290275
- 3. YedidyahLangsam, Moshe J. Augenstein, Aaron M. Tannenbaum, "Data Structures Using C and C++", 2006, PHI publications, ISBN 10: 0130369977
- 4. Brian W. Kernighan , Dennis M. Ritchie , "The C Programming Language" , 2nd Edition, PHI Publications, ISBN-10: 0131103628 ISBN-13: 978-0131103627

		I - SEME	STER		
		Object Oriented (Theory &P			
Course Code	:	18MCA14	CIE Marks	:	100 + 50
Hrs/Week	:	L: T: P 3:1:2	SEE Marks	:	100 + 50
Credits	:	5	SEE Duration	:	3 Hrs
		Unit – I			8 Hrs
	-	ogramming: Introductions, OC ation, Basics of polymorphism:			
		Unit – II			8 Hrs
Introduction to	• Pvt	hon Programming Language			
numbers, if state Data Types, I	emer F unc	non, program output, input, co at, while loop, for loop, and the r tions and Modules: Lists ar ional programming,	range()		
		Unit – III			8 Hrs
		e modules, modules and files, I action, types of inheritance, sub Unit – IV			ods 6 Hrs
Frror and Fy	cont	ions : Lists and tuples, Workin	ng with Strings Dictionaria	oc Intro	
exceptions in]	oytho	on, detecting and handling ex otions, assertions, standard excep	ceptions, context managem		
		Unit – V			6Hrs
Files, Writing F	iles.	ng Files- Introduction to File op Iagic method syntax, Available I	Methods	iniques f	for Reading
		Unit – VI(Lab (- <i>'</i>		
		Student should implement	using Python Language		
 Impleme Impleme Impleme Design a Impleme Impleme Impleme Impleme 	ent 1 ent 1 and d ent H ent N ent o	nporting of modules using OOP 0 operations on string and Tuple 0 operations on sets and dictiona levelop pay band scale for N em lierarchical Inheritance fultilevel Inheritance verriding concept the magic methods usage: i)g	ary ployees using dictionary		

Course Outcomes

After going through this course, the student will be able to:

CO1: Relate and recall object oriented and structured programming concepts

CO2: Demonstrate competency in object-oriented concepts

CO3: Utilize object-oriented concepts for real world problem

CO4: Analyze solutions using OOPs concepts for real world applications

Refe	erence Books
1	Hetland, Magnus Lie. "Beginning Python: from novice to professional". 3 rd Edition, Apress,
	ISBN 978-1-4842-0029-2, 2017
2	Sneeringer, Luke," Professional Python", John Wiley & Sons, ISBN -978-1-119-07085-6,
	2016.
3	Paul Gries, Jennifer Campbell, Jason Montojo, "Practical Programming", 2nd Edition
	An Introduction to Computer Science Using Python 3, SHROFF Publishers and Distributors
	Pvt Ltd, ISBN: 13:978-93-5110-469-8, 2014
4	Wesley J Chun, "Core Python Programming", second edition, Pearson Education, ISBN 81-
	317-1188-9, 2007

		I – SEMESTER			
Web Programming (Theory and Practice)					
Course Code	: 18MCA15	(Theory and Tract	CIE Marks	:	100 + 50
Hrs/Week	: L: T: P 3:1:2		SEE Marks	:	100 + 50
Credits	: 5		SEE Duration	:	3 Hrs
		Unit – I		-	07 Hrs
HTTP, Security Scripting versu Markup Langu	r, the Web Programm s Server-Side Scripti	ners Toolbox. Multi-tier ng. - Formatting, Commenti	Browsers, Web Servers, UI Application Architecture, (ng, Code, Anchors, Backg	Clie	nt-Side
		Unit – II			07 Hrs
In-line Styles, text, Margins a XML: Introdu schema, displa Basics of Ja	Embedding Style Sl nd Padding, Position ction, syntax, Docur ying raw XML docur vaScript: Overview	heets, Linking External ing using CSS. ment structure, Docume nents, Displaying XML Unit – III of JavaScript, Object	action to CSS – Basic synta Style Sheets, Background ent Type Definitions, Nar documents with CSS, XSL et orientation and JavaS creen output and keyboard	ls, n nes <u>p</u> <u>T st</u> crip	nanipulating baces, XML yle sheets 07 Hrs t, Syntactic
	ject creation and mod		ions, Constructors, Pattern		
	ss in Java Script, Ev	waScript Execution Env	vironment, The Document ag, The DOM 2 Event Mo		ject Model,
		Unit – V			08 Hrs
Characteristics	Primitives, Opera	ations and Expression Handling, Files, Cookies			
		Unit – VI : Lab Comp			
a. b. c. d.	Text Formatting tags Links Images Tables				
 Design affiliate Year of and use Develop 	an XML document d to VTU. The infor Joining, and e-mail it to display the docu and demonstrate a d	rmation must include US id. Make up sample dat ument. calculator using HTML5	bout a student in an engi SN, Name, Name of the C a for 3 students. Create a file that includes JavaScrij	Colle CSS pt.	ege, Branch, 5 style sheet
includii	ng the given name su	bmitted in the html form	the page and display a we ck which displays the curr		
(the val	id format is: A digit f	from 1 to 4 followed by t	HTML5 document that c two upper-case characters f wed by three digits; no er	follc	wed by two

allowed) of the user. Event handler must be included for the form element that collects this information to validate the input. Messages in the alert windows must be produced when errors are detected.

- 8. Write a PHP Program to demonstrate all the file operations
- 9. Demonstrate a login page using HTML5 and validate the username and password using PhP
- 10. Write a PHP program to store current date-time in a COOKIE and display the 'Last visited on' date-time on the web page upon reopening of the same page. Also, store page views count in SESSION, to increment the count on each refresh, and show the count on web page.

Course Outcomes

After going through this course the student will be able to:

CO1: Illustrate the fundamentals of web programming

CO2: Apply the mark-up and scripting language concepts to build web applications

CO3: Analyze appropriate content layout design and event handling techniques

CO4: Implement server side utilities for dynamic real world environment

Reference Books

1 Robert W. Sebesta," Programming the World Wide Web", Pearson Education, 8th Edition, 2015.

ISBN: 9780133775983

- M. Srinivasan, "Web Technology Theory and Practice", Pearson Education, 1st Edition, 2012, ISBN: 9788131774199
- Harvey & Paul Deitel & Associates, Harvey Deitel and Abbey Deitel, "Internet and World Wide
 Web How to Program", Pearson Education, 5th Edition, 2011, ISBN: 9780132151009
- Chris Bates, "Web Programming Building Internet Applications", Wiley India, 3rd Edition, 2006, ISBN: 9780470017753

			TER		
		SOFTWARE ENG			
Course Code	:	(Theory 18MCA21	7) CIE Marks	:	100
Hrs/Week	• •	L:T:P 3:0:0	SEE Marks	: 100	
Credits	•	3	SEE Duration	•	3 Hrs
Cicuits	•	Unit – I		•	10 Hrs
Introduction	0_	Software Process Models			10 1113
			Software engineering and	tho M	
		ofessional Software Development, oftware engineering ethics, Softw			
		ocess activities: Coping with cha			
		nming, Scrum; Case study- Develop			
0		ation (IEEE format)	Software Requirement Sp	cuncau	
icui world upp	<u></u>	Unit – II			08 Hrs
System Mode	lin	g, Design and implementation			UU III 3
•		g: Context models, Interaction mo	dels Structural models P	ehavior	al model
-		sign: Architectural design decisions			
		nodels for real world application	, Architectural patterns, Ca	ise Stud	y-Develu
Object offente	<u>a 11</u>	וטעבוא וטו וכמו שטווע מתחונמנוטוו			
		**			06 Hrs
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Ref	erence Books
1	Ian Summerville, "Software Engineering", Pearson Education Ltd, 10 th Edition, 2015, ISBN : 9780133943030
2	Roger S Pressman, Bruce R. Maxim, "Software Engineering- A Practitioner's Approach", McGraw-Hill, 8 th Edition, 2015, ISBN: 9780078022128
3	E-Book: Guide to the Software Engineering body of Knowledge version 3.0 SWEBOK,2014,ISBN:9780769551661
4	Pankaj Jalote, "An Integrated Approach to Software Engineering", Narosa Publishing House, 3rd Edition, 2013, ISBN: 81-7319-702-4

			II - Sl	MESTER		
			E-CO	MMERCE		
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	irse Code					
	/Week			SEE Marks	:	100
Cre	dits	:	4	SEE Duration	:	3 Hrs.
			Unit	1		8 Hrs
Eigl	1	tur		gy, Types of E-Commerce, E-Cor lodel, Case studies	nmerc	e Busines
			Unit	II		7 Hrs
Bui	lding an E-	Со	e web site and building mo	re and Hardware requirement, ( ile applications, Case studies	Case s	
			Unit	III		7 Hrs
Sec	urity threats	in		nt, Management policies, busines l Local marketing, Case studies	s proc	<b>7Hrs</b> redures, F
	1 5		Unit –			7 Hrs
Und Inte	lerstanding llectual Prop	Eth erty	y rights, Case studies	e-Commerce, Privacy and inf	ormati	on right
Afte CO2 CO2 CO3	1: Understan 2: Apply e-co 3: Analyze cl	ugh d th omi hall	Dutcomes a this course the student wil be fundamentals of e-comm merce initiatives in various enges for e-commerce mod ommerce business application	rce in business Business applications els		
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1			Laudon, Carol Guercio T dition,2016,ISBN 978-93-3	aver, "E-commerce BusinessTech 25-5673-7.	inolog	y Societ

	II - SEN	IESTER		
		e Systems		
Course Code		Id Practice) CIE Marks	•	100 + 50
Hrs/Week	: L:T:P 3:1:2	SEE Marks	:	100 + 50
Credits	: 5	SEE Duration	:	3 Hrs
	Unit – I			07 Hrs
database app Database Ap	to data, information, databases, d roach, Actors on the Scene, Advanta	ages of using DBMS approach and instances, Three schema	, Cla a arcl	ssification c
	Unit – II			08 Hrs
Introduction Relational Da	s and Basic SQL to Data Models, Relational Mode atabase Schemas, Keys, Dealing with of SQL, Data Definition Language and	Constraint Violations, E-R to R l Data Types		nal Mapping
	Unit – III Query Language			07 Hrs
Duta Mainp	ulation language, Data Control La	nguage, Data Query Languag	ge an	d all relate
commands. C Commit, Roll <b>Database De</b> Informal Des on Primary I Form, Multi-	Queries using Group by and Order b <u>lback, Save point. Views: Introduction</u> <u>Unit – IV</u> sign Guidelines for Relation Schemas Keys, General Definitions of Second- valued Dependency and Fourth Nor	y clause & Join, Operators, Ag n s, Functional Dependencies, No l and Third Normal Forms, Bo	ormal	te Functions 07 Hrs Forms Base Codd Norma
commands. C Commit, Roll <b>Database De</b> Informal Des on Primary I	Queries using Group by and Order b <u>lback, Save point. Views: Introduction</u> <b>Unit – IV</b> <b>sign Theory and Normalization</b> sign Guidelines for Relation Schemas Keys, General Definitions of Second -valued Dependency and Fourth Nor	y clause & Join, Operators, Ag n s, Functional Dependencies, No l and Third Normal Forms, Bo	ormal	te Functions 07 Hrs Forms Base Codd Norma Fifth Norma
commands. C Commit, Roll Database De Informal Des on Primary I Form, Multi- Form Transaction Introduction-	Queries using Group by and Order b lback, Save point. Views: Introduction <b>Unit – IV</b> <b>esign Theory and Normalization</b> sign Guidelines for Relation Schemas Keys, General Definitions of Second -valued Dependency and Fourth Nor <b>Unit – V</b> <b>Processing and Concurrency Contr</b> Properties of Transaction, Serializab commit Protocol, Dead lock	y clause & Join, Operators, Ag s, Functional Dependencies, No l and Third Normal Forms, Be rmal Form, Join Dependencies rol ility, Concurrency Control, Loo	ormal oyce-( and	te Functions 07 Hrs Forms Base Codd Norma Fifth Norma 07 Hrs
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commands. C Commit, Roll Database De Informal Des on Primary I Form, Multi- Form Transaction Introduction- Two Phase C	Queries using Group by and Order b lback, Save point. Views: Introduction Unit – IV sign Theory and Normalization sign Guidelines for Relation Schemas Keys, General Definitions of Second -valued Dependency and Fourth Nor Unit – V Processing and Concurrency Contre Properties of Transaction, Serializab commit Protocol, Dead lock Unit – VI: La ider a scenario on Employee Managen Identify the Entities, attributes, relat Design the ER diagram and map it t Create the tables and populated ther Design the solution for the followin a. List the employee name and Dollars.	y clause & Join, Operators, Ag s, Functional Dependencies, No l and Third Normal Forms, Be rmal Form, Join Dependencies rol ility, Concurrency Control, Loc <b>b Component</b> nent Database and perform the fr ionships, cardinality and participo o schema diagram n with appropriate data g	ggrega ormal oyce-( and cking ollowi pation expres	te Functions 07 Hrs Forms Base Codd Norma Fifth Norma 07 Hrs Mechanisms ng ssed as

- d. List the details of the departments where maximum number of employees are working
- e. List the name, salary, and commission for those employees whose net pay are greater than or equal to the salary of any other employee in the company.
- f. List the department where there are no employees (use Join)

### Additional queries

- 1. List the managers who are senior to ALEX and who are junior to ANDREW.
- 2. Display the unique department of the employees
- 3. List the details of the senior employees as on year 1991.
- 4. List the name and average salary of employees in department wise.
- 5. list the name and salary of ANEESA if her salary is equal to maximum salary of her grade
- 6. List the department number, name, designation, salary, and net salary (salary + commission) of the SALESMAN who are earning maximum net salary
- 7. Find the most recently hired employees in each department order by joining date
- 8. List the name of the employees for their manager LENIN and also the name of the manager of LENIN
- 9. List the name of the managers who is having maximum number of employees working under him
- 10. list the name of the departments where more than average number of employees are working
- 2. Consider a scenario on Movie Database and perform the following
  - i. Identify the Entities, attributes, relationships, cardinality and participation
  - ii. Design the ER diagram and map it to schema diagram
  - iii. Create the tables and populated them with appropriate data
  - iv. Design the solution for the following
    - a. Find the movies which was released before 1998
    - b. Find the name of the director (first and last names) who directed a movie that casted a role for 'Eyes Wide Shut'
    - c. Find the reviewer's name and the title of the movie for those reviewers who rated more than one movie
    - d. Find all the years which produced at least one movie and that received a rating of more than 3 stars
    - e. List all the actors who acted in a movie before 1990 and also in a movie after 2000 (use Join)
    - f. Compute a report which contains the genres of those movies with their average time and number of movies for each genre (use Join)

### Additional Queries

- 1. Find the titles of all movies directed by the director whose first and last name are Ronnie William
- 2. Find the titles of all movies that have no ratings
- 3. Find the titles of all movies directed by carl Hamacher.
- 4. Find the names of all reviewers who have ratings with a NULL value

- 5. Retrieve the reviewer name, movie title, and stars for those movies which reviewed by a reviewer and must be rated. Sort the result by reviewer name
- 6. Find the movie title, and the highest number of stars that movie received and arranged the result according to the group of a movie and the movie title appear alphabetically in ascending order
- 7. Find the titles of all movies which have been reviewed by anybody except by Allen Timpson
- 8. list all the information of the actors who played a role in the movie 'SILVER LINE'
- 9. Find the movie title, actor first and last name, and the role for those movies where one or more actors acted in two or more movies

Retrieve the reviewer name, movie title, and stars in an order that reviewer name will come first, then by movie title, and lastly by number of stars (use join)

- 3. Consider a scenario on Hospital Database and perform the following
  - 1. Identify the Entities, attributes, relationships, cardinality and participation
  - 2. Design the ER diagram and map it to schema diagram
  - 3. Create the tables and populated them with appropriate data
  - 4. Design the solution for the following
    - a. Find the name and medication for those patients who did not take any appointment
    - b. Find out the floor where the minimum no of rooms are available
    - c. Obtain the nurses and the block where they are booked for attending the patients on call (use Join)
    - d. Find the name of the patients who taken an advanced appointment, and also display their physicians and medication
    - e. Find the name of the patients, their treating physicians and medication
    - f. Generate a report which will show name of the patient, name of the physician who is treating him or her, name of the nurse who is attending him or her, which treatment is going on to the patient, the date of release, In which room the patient has admitted and which floor and block the room belongs to respectively

#### Additional Queries

- 1. Find the floor and block where the room number 104 belongs to
- 2. Count the number of unavailable rooms
- 3. Find the name of the nurse who are the head of their department
- 4. Find all the information of the nurses who are yet to be registered
- 5. Obtain the name of the physicians who are trained for a special treatment
- 6. Count the number of patients who taken appointment with at least one physician
- 7. Obtain the name of the physicians who are not a specialized physician
- 8. Obtain the name of the physicians with department who are yet to be affiliated
- 9. Count number of unique patients who got an appointment for examination room C
- 10. Find the name of the patients and the number of physicians they have taken appointment

#### **Course Outcomes**

After going through this course the student will be able to:

- **CO1:** Explain the basic concepts of data models, database design for transaction processing and Query Language
- **CO2**: Analyze an information storage problem and derive an information model expressed in the form of an entity relation diagram
- **CO3**: Transform high-level conceptual model to relational data model, populate database and formulate queries based on principles of normalization
- **CO4:** Design and Implement a relational database for any given problem

#### **Reference Books**

- 1 Ramez Elmasri, Shamkant B. Navathe, "Fundamentals of Database Systems", Pearson Addison Wesley, 6th Edition, 2011, ISBN 13: 978-0-136-08620-8
- 2 Abraham Silberschatz, Henry F. Korth, S. Sudarshan, "Database Systems Concepts", McGraw-Hill Education, 6th Edition, 2010, ISBN 0-07-352332-1
- Raghu Ramakrishnan, Johannes Gehrke, "Database Management System", Mc Graw-Hill, 3rd
   Edition, 2003, ISBN-10: 0072465638
- 4 Jeffrey A. Hoffer, Mary B. Prescott, Fred R. McFadden, "Modern Database Management", Prentice Hall, 8th Edition, ISBN-13: 978-0-13-033969-0

	II SEMESTER						
			OPERA	FING SYSTEMS			
				y and Practice)			
Course Code:18MCA24CIE Marks:100 +50							
Hrs/Week	:	L-T-P:3:	1:2	SEE Marks	:	100 +50	)
Credits	:	05		SEE Duration	:	3 HOU	RS
			Unit – I				7 Hrs
	n o	bjectives and	l functions, U	Jnix – architecture, feat s: head, tail, cut, paste, f			
The system, Das		iic attributes,	Unit – II		UIK,	301t, ii, g	7 Hrs
Process Manag	eme	ent					
-			s Description	, Process Control, Sys	em	call – fo	rk, exec Process
			duling Criter	ia and Algorithms			1
			Unit – II	[			8 Hrs
-	Conc	currency, Se	- · ·	essage Passing, Princi	-		
	dlo	ck Avoidanc	e, Deadlock	Detection and Dining	Phi	losopher'	s problem using
semaphores			Unit – IV	7			7 Hrs
Memory Mana	aem	ent	Unit – 1 v	·			/ 115
5			rv Allocatio	n, Paging, Segmentat	ion.	Demano	d Paging, Page
Replacement an	_		-		1011,	Deman	u 1 u <u>5</u> <u>6</u> , 1 u <u>5</u> c
•			Unit – V				7 Hrs
	File econ	systems:	e Structure: D	mplementation, Alloca isk Structure, Disk Scho			_
4 147 1 1 11				(Lab Component)			
1. Write a shell							
				n for all users. rmatted output			
,				with replacement of curr	ent o	date to '*'	,
				oggle case and store it in			
2. Write a prog							
				nat generates Fibonacci s			
				l non preemptive SJF ar	ld m	ake the co	omparison
<ol> <li>5. Implement FCFS and Round Robin algorithms</li> <li>6. Implement producer-consumer problem using semaphore</li> </ol>							
			1	0 1	ina	with Fi	xed number
<ol><li>Write a program to simulate the MFT (Multiprogramming with Fixed number of tasks) memory management technique</li></ol>							
8. Write a pro	gra	am to imple	ement FIFO	, LRU and Optimal P string with three fra	-	•	ement
9. Implement L	inke	ed List File A	llocation tech	niques			
•			duling Algo		-		
			8 and 10	Implement Any Tv	/0 i	n CIE/S	EE)
<b>Expected Cour</b> After going thro			ho studont wi	11 h h 1 - 4 - 1			

CO1: Define the structure of an operating system components and its responsibilities

CO2: Analyze the structure and design decisions involved in the implementation of an operating system

CO3: Design and Demonstrate general solutions for the issues involved in the management of an operating system

CO4: Implement and manage the computer resources like memory, storage devices, processes, file systems and users

Ref	Reference Books:			
1.	William Stallings, "Operating Systems – Internals and Design Principles", Pearson, Seventh Edition, 2012, ISBN:978-93-325-1880-3			
2.	Abraham Silberschatz, <u>Peter Baer Galvin</u> , <u>Greg Gagne</u> , "Operating System Concepts", John Wiley, Eighth Edition, 2010, ISBN :978-81-265-205-0			
3.	Sumitabha Das, "Unix Concepts and Applications", Mc Graw Hill, Fourth Edition, 2012,ISBN:978-0-07-063546-3			
4.	Jonathan W Valvano, Real –Time Operating Systems For ARM Cortex-M Microcontrollers - Embedded Systems, Volume 3, 4 th Edition, 2017, ISBN: 978-1463590154			

		II Semester					
		VA Programn eory and Prac	-				
Course Code	: 18MCA25		CIE Marks	:	100	+50	
Hrs/Week : L:T:P 3:1:2 SEE Marks : 100+50							
Credits	: 5		SEE Duration	:	3 H	[ <b>rs</b>	
	Uni	it — I					6 Hrs
Programming, T Scanners, The Ja operators, Type	ning Fundamentals: In he Java Development Kit ava Class Libraries. Java Casting, Operator Prece	t, The Java Keyv 's Primitive Typ	vords, Identifiers i bes, The Scope an	in Ja d Li	va, U fetim	ser inpu e of Va	ut using ariables,
Arrays, String Ha		it – II					6 Hr
Classes, Ohiects	and Methods: Class Fu		v Objects are Crea	ted.	Refe	rence V	
	r, Methods, Returning ne new operator Revisi rstanding Static.		•			0	
	Unit	i – III					10 Hr
<b>Exception Hand</b> subclass Excepti using throws, Ja and Runnable In	<b>lling and Multithreading</b> ons, try blocks can be r va's Built-in Exceptions. nterface, Creating Threa Thread Communicatio	nested, Throwing Multithreaded I Id, Creating Mu	g an Exception, T Programming: fun ltiple Threads, T	Throv dam hrea	wable entals d Pri	, using , Threa orities,	finally, d Class Thread
itesuining and st		t – V					8 Hr
stream.Applet: fi	The Java I/O Classes a undamentals, Architecture ainting, using the status	nd Interfaces, I e, Applet Skelet window, Passin	on, Applet Initial g parameters to A	izatio	on an	d Term	Output ination,
1 Minito a IAV		-VI: Lab Comp		hod	ovorl	onding	
2. Write a Java a. Write a JA	A Program to demonstrate program to demonstrate th VA Program to demonstrate va program to demonstrate	he methods in St ate Inheritance	ring and StringBut	ffer	overl	uaung	
geometrical f	the usage of packages igures and calculating the A program to demonstrate throws.)	eir area. Import a	nd compile classes	s in c	other p	program	1
concept 6. Write a JAV	A program using Synch A program to demonstrat priority in another program	e the Thread pri-					

- 7. Write a JAVA Program to demonstrate read, write and append operations on files
- 8. Write a JAVA applet program, which handles keyboard event.
- 9. Write a Java program that works as a simple calculator. Use a grid layout to arrange buttons for the digits and for the + * % operations. Add a text field to display the result.

#### **Course Outcomes**

At the end of the course the student will be able to

CO1: Understand the fundamentals of Java Programming

CO2: Design a Java program efficiently by using its features

CO3: Implement and Develop software using Java programming

CO4: Demonstrate various Java features in building applications

#### **Reference Books**

1.	Herbert Schildt, Dale Skrien, "Java Fundamentals, A Comprehensive Introduction ", Tata
	McGraw Hill Edition, 2013, McGraw Hill Publication, ISBN-13:9781249006593
2.	Schildt, Herbert. Java: The Complete Reference 9th . McGraw-Hill Education Group, 2014,
	McGraw-Hill Education Group, ISBN:0071808558 9780071808552
3.	Danny Poo, Derek Kiong, Swarnalatha Ashok, "Object-Oriented Programming and Java",
	assend adition Chringer science and husiness madia 2000 ISDN 070 01 0400 225 2

second edition Springer science and business media, 2009, ISBN: 978-81-8489-235-2

4. Krishna, P. Radha "Object oriented programming through Java" CRC Press, Inc., 2007,ISBN: 8173715726