

International Journal Paper Published

Sl.No	Name of the Authors	Title of the Paper	Journals Name/Volume/Year	Scopus	Web of Science	Quartile
				Citations/Indexed	Citations/Indexed	h index
2022-2023						
1.	MahendraB. M.Savita Sonoli, Abhishek Ameta	Improved Deep Multi-Patch Hierarchical Network For Handling Saturation In Image Deblurring	Array Open access (Elsevier) Volume 15, September, 2022 SCI Journal	0/ Yes	0/Yes	Q1/7
2.	Shinde, A.S., Mahendra, B.M., Nejakar, S., Herur, S.M., Bhat, N.	Performance analysis of machine learning algorithm of detection and classification of brain tumor using computer vision	Advances in Engineering Software,2022, 173, 103221 SCI Journal	0/ Yes	0/ Yes	Q1/94
3.	Shaik Mahammad Ameer Afridi , Nagaraja Shylashree , Satish Tunga , Latha Bavikatte Nanjundappa	An effective way to generate the shift timing constraints and sanity checks	Indonesian Journal of Electrical Engineering and Computer Science, June 2023	0/ Yes	0/ NO	Q3/32

4.	K A Mohamed Junaid , Y Sukhi , N Anjum , Y Jeyashree , A Fayaz Ahamed , S Debbarma , G. Chaudhary , S. Priyadarshini , N Shylashree , S Garg , M Kumar , V Nath	PV-based DC-DC buck-boost converter for LED drive	e-Prime-Advances in electrical Engineering, Electronics and Energy, Elsevier, 2023	0/ Yes	0/ NO	Q4/33
5.	N. Shylashree, M. S. Amulya, Gulur R. Disha, N. Praveena, Vijay Kumar Verma, S. Muthumanickam, V. Kannagi, K. Sivachandar & Vijay Nath	A Novel Design of Low Power & High Speed FinFET Based Binary and Ternary SRAM and 4*4 SRAM Array,	IETE Journal of Research (Taylor & Francis) May 2023 SCI Journal	0/ Yes	0/ Yes	Q3/31

6.	Sheetal SingrihalliHemaraj , Shylashree Nagaraja , Sunitha YariyurNarasimhaiah , Madhu Patil,	Design and development of frameworks for CPU verification efficiency improvement	Indonesian Journal of Electrical Engineering and Computer Science, Vol. 32, No. 3, December 2023, pp. 1361~1369	0/ Yes	0/ NO	Q3/32
7.	Arun Kumar Chavan, Ravish Aradhya	Design of 5.1 GHz ultra-low power and wide tuning range hybrid oscillator	International Journal of Electrical and Computer Engineering, 2023.	0/ Yes	0/ NO	Q3/32
8.	Sushanth, G., Ravish Aradhya, H.V.	Hardware in loop network simulators-An insight overview	International Journal of Modeling, Simulation, and Scientific Computing, 2023.	0/ Yes	0/ NO	Q4/20
9.	Malhotra, K., Revathi, M.S., Uma, B.V., Ajay, K.M.	Architectural framework and register-transfer level design synthesis for cost-effective smart eyewear	Indonesian Journal of Electrical Engineering and Computer Science, 2023.	0/ Yes	0/ NO	Q3/32
10.	Vasudeva, G., Uma, B.V.	Design and Implementation of High Speed and Low Power 12-Bit SAR ADC Using 22nm FinFET	International Journal of Electrical and Computer Engineering, 2022.	0/ Yes	0/ NO	Q3/32

11.	Prapulla, S.B., Patra, S.M., Subramanya, K.N., Uma, B.V.	Techniques for Strengthening 21st Century Learners' Critical Thinking Skills	Journal of Engineering Education Transformations, 2022	0/Yes	0/No	Q3/9
12.	Vasudeva, G., Uma, B.V.	Design and Implementation of High Speed and Low Power 12-bit SAR ADC using 22nm FinFET	WSEAS Transactions on Systems and Control, 2022	0/Yes	0/No	Q4/18
13.	Kedar, A., Vangol, P., Mahesh, A.	Hybridization of Bayesian Compressive Sensing and Array Dilation Technique for Synthesis of Linear Isophoric Sparse Antenna Arrays	IEEE Transactions on Antennas and Propagation ,2023. SCI Journal	0/Yes	0/Yes	Q1/221
14.	Anu, K.S., Vishnumurthy, K.A., Mahesh, A., Natarajan, K.	Effect of carbon fiber reinforcement on the mechanical and electromagnetic shielding properties of the Fe ₃ O ₄ -carbonaceous/epoxy composites	Polymers for Advanced Technologies, 2023. SCI Journal	0/Yes	0/Yes	Q2/100

15.	Shashidhar, R., Shashank, M.P., Sahana, B.	Enhancing Visual Speech Recognition for Deaf Individuals: A Hybrid LSTM and CNN 3D Model for Improved Accuracy	Arabian Journal for Science and Engineering ,2023 SCI Journal	0/Yes	0/Yes	Q1/60
16.	Shenoy, P.S.P., Vishnu, S.S., Kumar, R.P., Bailuguttu, Sahana.	Enhancement of observability using Kubernetes operator	Indonesian Journal of Electrical Engineering and Computer Science, 2022	0/ Yes	0/ NO	Q3/32
17.	Lakshmi Narayanan, S., Ignatia, K.M.J., Alfurhood, B.S., Bhat, N.	Image classification using gaussian curvature based local tetra pattern descriptor with enhanced feature selection technique	Journal of Intelligent and Fuzzy Systems., 2023. SCI Journal	0/ Yes	0/ NO	Q2/73
18.	Gayathri, D.S., Rani, K.R.Usha.	Adaptive impedance matching system for broadband power line communication using RC-filters	Journal of Ambient Intelligence and Humanized Computing,, 2023	0/ Yes	0/ NO	Q1/64
19.	Kumar, P., Usha Rani, K.R.	Precoder Based Transceivers Design for Non-Orthogonal Multiple Access Mixed Numerologies	Journal of Communications., 2023	0/ Yes	0/ NO	Q3/39

20.	Kumar, P., Usha Rani, K.R.	Inter Numerology Interference Reduction Using Windowing Technique For OFDM Based Multi- Numerologies	Journal of Applied Science and Engineering (Taiwan), 2023	0/ Yes	0/ NO	No/0
21	Kasliwal, K., Jayanthi, P.N., Jain, A., Bahl, R.K.	Enhancing satellite-to-ground communication using quantum key distribution	IET Quantum Communication, 2023 SCI Journal	0/ Yes	0/ Yes	Q1/6
22	Ashok Kumar nanda, Neelakadan, Sachi gupta, Angel Latha mary, ramya S, Siripuri Kiran	Multi layer Perceptron's neural network with optimaztion algorithm for green house gas forecasting system	Environmental Challenges, 2023	0/Yes	0/No	Q2/19
2021-2022						
1	Hemanth Kumar C S and Kariyappa B S	Node Voltage and KCL Model-Based Low Leakage Volatile and Non- Volatile 7T SRAM Cells	IETE Journal of Research (Taylor & Francis)/0377-2063/ February , 2022 SCI Journal	0/ Yes	0/ Yes	Q3/30
2	N Shylashree, Varchas S Bharadwaj, D Yashas, Vinayak Kulkarni, Ajay Bharadwaj,	Comprehensive Design and Timing Analysis for High speed Master Slave D Flip-Flops using 18 nm FinFET Technology	IETE Journal of Research, Taylors and Francis 0377-2063 / 0974-780X / August, 2021 SCI Journal	2/ Yes	0/ Yes	Q3/28

	Vijay Nath					
3	Javalkar Vinay Kumar, N Shylashree, Spoorthi G Gojanur, G Raju, Vinay Varma Bhupathiraju, Manjunatha Channegowd a	Design and Analysis of a Biosensor for the Detection of Estrogen Hormonal Levels	Bionanoscience/2191- 1630 / 2191-1649/ Feb, 2022 SCI Journal	0/ Yes	0/ Yes	Q3/27
4	AS Anand Swamy, AS Mamatha, N Shylashree, Vijay Nath	Lossless Compression of Hyperspectral Imagery by Assimilating Decorrelation and Pre-processing with Efficient Displaying Using Multiscale HDR Approach	IETE Journal of Research, Taylors and Fransis 0377-2063 / 0974-780X/ Feb, 2022 SCI Journal	0/ Yes	0/ Yes	Q3/28
5	Satish Tunga, Sadashiva V. Chakrasali, N. Shylashree, Latha B. N., Mamatha A. S.	Optimal path discovery for two moving sinks with a common junction in a wireless sensor network	Indonesian Journal of Electrical Engineering and Computer Science /2502-4752/ August, 2021	0/ Yes	0/ Yes	Q3/23
6	Chethana G., Padmaja K. V.	A novel secure biomedical data aggregation using fully	Indonesian Journal of Electrical Engineering and Computer Science /2502-4752/ october,	0/ Yes	0/ Yes	Q3/23

		homomorphic encryption in WSN	2021			
7	Rajani Katiyar, K R Usha Rani, TS Sindhu, HD Sneha Jain, Vidhyashree V1, S Ashoka and Manjunatha Channegowda	Design and development of electrochemical potentiostat circuit for the sensing of toxic cadmium and lead ions in soil	Engineering Research Express /45026/ November, 2021	0/ Yes	0/ Yes	No/0
8	G. S. Ananth, N. Shylashree, Satish Tunga, Latha B. N.	A novel design for hardware interface board with reduced resource utilization	Indonesian journal of Electrical Engineering and computer science 2502-4752/ December, 2021	0/ Yes	0/ Yes	Q3/23
9	Rohit Ghatikar, Nithin M	Design of high speed 2-stage OTA for high capacitive load of 120pF and 2.96V	Engineering Research Express /2631-8695/ December, 2021	0/ Yes	0/ Yes	No/0
10	N. Shylashree , M Anil Naik , A. S. Mamatha, V. Sridhar	Design and Implementation of Image Edge Detection Algorithm on FPGA	INTERNATIONAL JOURNAL OF CIRCUITS, SYSTEMS AND SIGNAL PROCESSING E-ISSN: 1998-4464/ January, 2022	0/ Yes	0/ Yes	Q4/15
11	Prerana,Sai	Enhancement of	Indonesian Journal of	0/ Yes	0/ No	Q3/23

	Vishnu,Rama kanth,sahana	observability using Kubernetes operator	electrical engineering and computer science /2502-4752/ Jan, 2022			
12.	Vasudeva, G., Uma, B.V.	Low Voltage Low Power And High Speed Opamp Design Using High-K Finfet Device	WSEAS Transactions on Circuits and Systems ,2021	0/ Yes	0/ No	Q4/18
13	Vasudeva, G., Uma, B.V.	22nm finfet based high gain wide band differential amplifier	International Journal of Circuits, Systems and Signal Processing, 2021	0/ Yes	0/ No	No/16
14	Katiyar, R., Rani, K.R.U., Sindhu, T.S., ...Ashoka, S., Channegowd a, M	Design and development of electrochemical potentiostat circuit for the sensing of toxic cadmium and lead ions in soil	Engineering Research Express, 2021	0/ Yes	0/ No	Q3/14
2020-2021						
1	Binu Dennis, Kariyappa B S	Rider-deep-lstm network for hybrid distance score- based fault prediction in analog circuits	IEEE Transactions on Industrial Electronics,2021 SCI Journal	10/ Yes	10/ Yes	Q1/304
2	Binu Dennis, Kariyappa B S	Multi-Rider Optimization- Based NeuralNetwork for	Journal of Circuits, Systems and Computers, 2021	0/ Yes	0/ Yes	Q3/33

		Fault Isolation in Analog Circuits	SCI Journal			
3	N. Shylashree, Anandswamy	HDR Image Compression by Multi-Scale down Sampling of Intensity Levels	International Journal of Image and Graphics SCI journal	0/Yes	0/Yes	Q3/22
4	N. Shylashree, Yatish D. Vahvale, N. Praveena, A. S. Mamatha	Design and Implementation of 64-bit SRAM and CAM on Cadence and Open-source environment	International journal of circuits, systems and signal processing/ July, 2021/ E-ISSN: 1998-4464	0/Yes	0/No	Q4/15
5	Juhie Fadnavis and Kariyappa B S	PNR Flow Methodology for Congestion Optimization using Different Macro Placement Strategies of DDR Memories	International Journal of Advanced Technology and Engineering Exploration (IJATEE)/ July, 2021/2394-5443	0/Yes	0/No	No/10
6	Praveen Kumar Y G, Kariyappa B S, Shashank S. M, Bharath C. N,	Performance Analysis of Multipliers Using Modified Gate Diffused Input Technology	IETE Journal of Research SCI Journal	0/Yes	0/Yes	Q3/28
7	Sowmya K B, Gagana P	A system verilog approach for verification of memory	International Journal of Reconfigurable and Embedded / July, 2020 Systems/2089-	0/Yes	0/Yes	Q4/17

		controller	4864			
8	Mrs. Chethana G, Dr. Padmaja K.V.	Integer Matrix Keys for Secure Data Aggregation in Clustered Wireless Sensor Networks	INTL JOURNAL OF ELECTRONICS AND TELECOMMUNICATIONS./20818491, 23001933, Scopus and web of science/ October-November, 2020	0/Yes	0/Yes	Q3/20
9	A V Skandashree, P Narashima Raja, Ankush Raina	Design and Development of Battery Management System for Fuel Cells	Journal of Information and Computational Science/ June, 2020/1548-7741	0/Yes	0/Yes	No/26
10	Sowmya K B, Gagana	A system verilog approach for verification of memory controller	International Journal of Reconfigurable and Embedded Systems (IJRES)/ July, 2020/2089-4864 Scopus	0/Yes	0/Yes	No/0
11	Nagaraj Bhat,Santosh Herur, Madhusudan	An Internet Of Things To Optimize Human Task Using Artificial Intelligence	International Journal of Future Generation Communication and Networking/ May, 2021/ ISSN: 2233-7857/IJFGCN	0/Yes	0/Yes	NO/0
12	Sindhu Rajendran a, Swathi	Technological advancements in occupational	Measurement: Sensors/ June,	0/Yes	0/Yes	No/0

	Giridhar , Surabhi Chaudhari , Praveen Kumar Gupta	health and safety	2021/100045 Scoups			
13	Sowmya K. B, Thejaswini A	Systematising Troubleshooting of Disputes in Network	International Journal of Reconfigurable and Embedded Systems/ March, 2021/2089- 4864	0/Yes	0/Yes	Q4/17
14	Sowmya K. B, Thanushree M	Characterization and Hierarchical Static Timing Analysis of Mixed-Signal Design	International Journal of Reconfigurable and Embedded Systems/ March, 2021/ ISSN: 2089-4864	0/Yes	0/Yes	Q4/17
15	Shylashree. N., Vahvale, Y.D., Praveena, N., Mamatha, A.S	Design and implementation of 64-bit SRAM and cam on cadence and open-source environment	International Journal of Circuits, Systems and Signal Processing, 2021	2/Yes	0/NO	Q4/16
16	Prapulla, S.B., Shetty, J., Kulkarni, Uma B V., Ramakant h Kumar, P., Shobha, G.	Design and analysis of an online course using moodleplatform	Journal of Engineering Education Transformations., 2020	0/Yes	0/No	Q3/9

17	Sandhya, S., Koppad, S.H., Anupama Kumar, S., ...Uma, B.V., Subramanya, K.N.	Adoption of google forms for enhancing collaborative stakeholder engagement in higher education	Journal of Engineering Education Transformations., 2020	0/Yes	0/No	Q3/9
18	R Priyadarshi, P Rawat, V Nath, B Acharya, N Shylashree	Three level heterogeneous clustering protocol for wireless sensor network	Microsystem Technologies, 2020 SCI journal	53/Yes	20/Yes	Q2/65
19	Padma, U., Kumaraswa my, H.V., Ravishankar, S.	Development of relay selection method for audio transmission in cooperative ad hoc networks	International Journal of Advanced Computer Science and Applications., 2020	0/Yes	0/No	Q3/35
20	Nithin, M., Kittur, H.M.	Design of on chip spiral inductors for millimeter wave frequency synthesizers	International Journal of Circuits, Systems and Signal Processing, 2020	2/Yes	0/NO	Q4/16
2019-2020						
1	N Shylashree, B Venkatesh, TM Saurab, Tarun Srinivasan, Vijay Nath	Design and analysis of high- speed 8-bit ALU using 18 nm FinFET technology	Microsystem Technologies, springer/ ISSN:1432-1858 SCI Journal	6/Yes	6/Yes	Q2/65

2	Shaik Qadeer, Mohammed Yousuf Khan, Shylashree N and Vijay Nath	High resolution fuel indicating and tracking system	Microsystem Technologies, springer/ ISSN:1432-1858, Volume 25, Issue 6/ June 2019 SCI Journal	2/Yes	2/ Yes	Q2/65
3	Shylashree N, Sangeetha B G, Adithya Thonse and Vijay Nath	Ge4Sb1Te5 device case study for NVRAM Applications	Microsystem Technologies, Springer/ ISSN:1432-1858/ SCI journal	0/Yes	0/Yes	Q2/65
4	YG Praveen Kumar, BS Kariyappa, SM Shashank, CN Bharath	Performance Analysis of Multipliers Using Modified Gate Diffused Input Technology	IETE Journal of Research (Taylor & Francis)/0377-2063/ 2020 SCI Journal	0/ Yes	0/ Yes	Q3/28
5	G. Smitha, H. V. Ravish Aradhya	mGDI based parallel adder for low power applications	Microsystem Technologies, Springer/ ISSN:1432-1858/ 2019 SCI journal	0/Yes	0/Yes	Q2/65
6	Bhagyashree Atharga, Anurag Chandan, Sujatha Hiremath	Verification of Accessibility and Connectivity of Multi-Die Module Through TAP Interface	International Journal of Recent Technology and Engineering (IJRTE)/ ISSN: 2277-3878, Volume-8 Issue-1/ May 2019	0/ Not Indexed	0/ Indexed	No/23
7	Anitha N,	Balanced	International Journal of	0/Yes	0/ No	No/40

	Srividya P	XOR/XNOR Circuits using CNTFET	Innovative Technology and Exploring Engineering (IJITEE)/ ISSN 2278-3075, Volume-8, Issue-10/ August 2019			
8	Sujata D Badiger, Dr. Uttrakumari M	Tree Copula Theory Based Fusion And Compressive Sensing For Activity Detection using Multi-Modal Data	International Journal of Innovative Technology and Exploring Engineering (IJITEE)/ ISSN: 2278-3075, Volume-8 Issue-11/ September 2019,	0/Yes	0/ No	No/40
9	Nethravathi K.A, Dr.Ravishankar	Signal Superposition Model with Mineralogy Based Spectroscopic Dielectric Model in Wireless Underground Sensor Networks	International Journal of electronics and telecommunications/ DOI: 10.24425/ijet.2019.130251, VOL. 65,NO. 4/ September 2019,	1/Yes	0/ No	Q3/20
10	Manjunath Managuli, Abhay Deshpande	Description and Identification of Soil Quality Measuring Development using Uavs and E-Nose System	International Journal of Recent Technology and Engineering (IJRTE)/ ISSN: 2277-3878, Volume-8 Issue-3/ September 2019,	0/Yes	0/ No	No/23
11	Mohana, H. V. Ravish Aradhya	Object Detection and Classification Algorithms using Deep Learning for Video	International Journal of Innovative Technology and Exploring Engineering (IJITEE)	2/Yes	0/ No	No/40

		Surveillance Applications				
12	Roopa, J., Rahul, N., Pragna, Geetha K.S., Satyanarayana, B.S., Govinda Raju, M.	Bio potential signal conditioning using MATLAB on electromyography signals	International Journal of Recent Technology and Engineering,	0/Yes	0/NO	No/27

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