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# Call for the Post of Junior Research Fellow under the ISRO Project

Applications are invited from the interested candidates for the position of Junior Research Fellow (JRF) to work on the R&D project under ISRO, sponsored project bearing the sanction number ISRO/RES/3/1054/24-25 undertaken in the Department of Electrical and Electronics Engineering RVCE.

## Project Details:

- **Project Title:** Field Oriented Control (FOC) based BLDC/Stepper Motor Drive Electronics
- **Sponsoring Agency:** Indian Space Research Organisation (ISRO)
- **Tenure:** 24 Months (Initial appointment for one year, extendable based on performance)
- **Principal Investigator:** Dr. S G Srivani
- **Co-Investigators:** Dr. Abhilash Krishna D G, Dr. Pandry Narendra Rao

## Fellowship Details:

- **Monthly Fellowship:** Rs. 37,000/- plus House Rent Allowance (HRA-24%) as per Institute norms

## Eligibility Criteria:

- **Essential Qualifications:**
  - B.E./B.Tech in Electrical Engineering / Electrical and Electronics Engineering / Instrumentation and M.E./M.Tech in relevant specializations with 60% aggregate marks (or CGPA of 6.5) for UR/EWS/OBC-NCL category.
  - For SC/ST/PwD candidates, 55% aggregate marks (or CGPA of 6.0).
  - **GATE/CSIR - UGC - NET Qualified is preferable.**
- **Desirable Qualifications/Experience:**
  - Hands-on experience in Power Electronics and Drives.
  - Proficiency in simulation tools (MATLAB/Simulink, PSIM, ANSYS, etc.).
  - Experience with dSPACE/FPGA/Micro/DSP controllers for control implementation.
  - Strong analytical and research skills with publications in relevant areas.
  - The candidate should be able to join immediately upon selection.

### Job Responsibilities:

- Research project execution, documentation, and related activities to ensure successful project completion.
- Development and implementation of control techniques for BLDC and Stepper motor drives.

### Additional Benefits:

- The selected candidate is eligible to apply for a **Ph.D. degree**.
- Hostel accommodation and mess facilities **may be provided** based on availability.

### Application Process:

- Interested candidates should submit their application by accessing the below link.

[Click Here To Apply](#)

**Deadline for Application Submission:** 15.03.2025 (11:59 PM IST)

**Interview Date and particulars will be communicated through mail after initial screening.**

**Interview Venue:** HoD Office, Department of Electrical and Electronics Engineering, RVCE, Bengaluru

### For any other queries contact:

#### Dr. S G Srivani (Principal Investigator)

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#### Dr. D G Abhilash Krishna (Co-PI)

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#### Dr. P Narendra Rao (Co-PI)

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