Objective of the Workshop

On-chip interconnects play a major role in shaping the power and performance profiles of multicore processors. As a scalable substitute for time shared on-chip bus, Network-on-Chip (NoC) is proposed as the communication infrastructure in modern multi/manycore System-on-Chips (SoC). This workshop is intended to provide good opportunity for our UG and PG students in the field of **VLSI & Embedded Systems, Computer Networking** to have a complete knowledge of bridging the fundamentals which they have studied in the programs to the outside industrial world. The workshop will be conducted IEEE student chapter, RVCE.

Resource Person:

Dr. John Jose is as an Assistant Professor in the Department of Computer Science and Engineering, IIT Guwahati, Assam. He is an alumnus of IIT Madras. He is the principal investigator of DST sponsored R&D projects. He has active research collaboration with University of Catania-Italy, ITRI Taiwan, and BITS Pilani-Dubai Campus. He is the recipient of ACM-SIGDA, IEEE-CEDA, IARCS and DRDO research grants. He has many peer-reviewed journals and conference papers to his credit. He coordinates the Ishan Vikas program of MHRD at IIT Guwahati. He has offered MHRD sponsored GIAN courses and NPTEL courses in computer architecture. He had conducted a similar three day workshop in 2017-18, which kindled many students to take up summer internship and projects which has culminated in joint publications with RVCE and IITG. More about him can be found at http://www.iitg.ernet.in/johnjose/

Mrs. Manju R. is currently a Ph.D. scholar in the Department of Computer Science and Engineering, IIT Guwahati. She has over 12 years of teaching experience in various Government Engineering Colleges under Directorate of Technical Education, Government of Kerala. Her area of interests is in compiler design and interconnection networks.

Outcome of the workshop:

- Understand and analyse multicore and many core architecture on-chip interconnect design.
- Design NoC using booksim simulator.
- The course will provide an opportunity to the participants to have fruitful association / collaboration with MARS Research Group.

ADVISORY COMMITTEE

Dr. K.N Subramanya, Principal, RVCE
Dr. M. Uttakumari, Prof. , ECE
Dr. K. S Geetha, Prof. & HoD, ECE
Dr. Ramakanthkumar P, Prof. & HoD, CSE
Dr. Rajashree Shettar, Associate Dean, CSE

ORGANIZING COMMITTEE

Prof. Pratibha K, Asst. Prof, ECE, RVCE
Prof. Sindhu Rajendran, Asst. Prof, ECE, RVCE
Prof. Sandhya S, Asst. Prof, CSE, RVCE
Prof. Smriti Shrivastav, Asst. Prof, CSE, RVCE
Mr. Aravind Varier, Student, RVCE
Mr. Allbright D'Souza, Student, RVCE
Mr. Shiva Sagar Shetty, Student, RVCE
Mr. Sunil K., Student, RVCE

For any queries, please contact:

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R. V. College of Engineering, Bengaluru-59

Five-Day Workshop on Emerging Trends in Multi-core Processors & Network on Chip Architecture

21st - 25th January, 2019
under
IEEE – RVCE Student Chapter

Organized by

Department of
Electronics and Communication Engineering,
Computer Science and Engineering
R V College of Engineering

In association with
MARS Research Lab, IIT-Guwahati

About RVCE

Established in 1963 with three engineering branches namely Civil, Mechanical and Electrical, today RVCE offers 12 Under Graduate Engineering programmes, 21 Master Degree programmes and Doctoral Studies. Located 13 km from the heart of Bangalore City – the Silicon Valley of India, on Mysore Road. Sprawling campus spread over an area of 52 acres set in sylvan surroundings. Provides an ideal ambience to stimulate the teaching-learning process, helping in bringing out skilled and disciplined Engineers. Rated one amongst the top ten self-financing Engineering Institutions in the country. Current annual student intake for Undergraduate Programmes & Post Graduate Programmes in Engineering is in excess of 1200. Highly qualified and dedicated faculty. Utilizes its expertise in various disciplines to conduct Research and Development for Industry and Defense establishments in the country.

About the Department

Electronics and Communication Engineering

The Electronics & Communication Engineering Department was started in the year 1972 and now conducts one undergraduate program, two postgraduate programs (M. Tech in VLSI Design & Embedded Systems and M. Tech in Communication Systems) with academic autonomy, and Ph.D program with affiliation to VTU. A total of 57 experienced faculty members, support four core specializations (VLSI Design, Wireless & Wireline Communications, Signal processing and Embedded Systems) in the department. UG & PG students are encouraged to take up interdisciplinary research in various domains and students are placed in reputed companies like Intel, NXP Semiconductors, SanDisk, Cisco IT, Qualcomm, Robert Bosch, Deloitte.

Department of Computer Science and Engineering

The Department of Computer Science and Engineering was established in year 1984. The Department offers B.E (CSE) undergraduate program, two postgraduate programs M.Tech(CSE) and M.Tech(CNE). Department also offers M.Sc by research and Ph.D. degree in various specializations of Computer Science and Engineering. Department of Computer Science and Engineering has MoUs for Research, Consultancy and training with multinational companies and government agencies such as CISCO, Citrix, Nihon, Gnostice, Sapient ,Infosys Technologies and Karnataka Forest department. Department has industry sponsored labs from CISCO, IBM and Nvidia GPU research centre.

About Mars Research Lab

MARS Lab, IIT Guwahati was established in 2016 and the research work in the lab focuses on on-chip memory and communication aspects of multi/many-core processors. Design issues in fault tolerant router architectures, multicasting techniques and architectural support for real-time image processing hardware are also explored Research findings from the lab group are published in premier VLSI and computer architecture conferences and journals. The research group also organizes short-term courses, workshops and assists students and faculty through summer /winter internship programs. For more details refer: http://www.iitg.ac.in/johnjose/MARS/index.html

AGENDA

The course is organized as (a) regular lecture sessions (b) hands-on sessions on NoC simulator-booksim (c) problem solving & tutorial sessions for deeper understanding of advanced concepts. The course will throw light on few emerging research problems in NoC domain upon which the participants can work on, once they go back to their parent institutions with necessary support from the course coordinator.

The course also provides an opportunity to the participants to have fruitful association/collaboration with MARS Research Group.

REGISTRATION DETAILS

INR 6000 – Industry
INR 2500 – Faculty
INR 2000 – Research Scholars
INR 1000 – Students
INR 750 – IEEE Student Member **(including GST)**

PAYMENT DETAILS

The registration must be completed before 16th of January.

ACCOMMODATION

Delegates are requested to make their own arrangements for **accommodation.**

Website:
www.rvce.edu.in

Registration Link:
https://goo.gl/forms/WZIXJkvSmHZhDtDM2

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under
IEEE – RVCE Student Chapter

Name: _____

Designation: _____

Email Address: _____

Organization: _____

Postal Address: _____

_____

City: _____

Postal Code: _____

Contact No: _____

IEEE Member No: _____

Signature of the Applicant

Signature & Seal of the Sponsoring Authority