

Electronics and Communication Engineering

List of Research Scholars pursuing Ph.D in Electronics and Communication Engineering Research Center(2013-14 Onwards)

S.No	USN	Name of the Research Scholar	Research Topic	Date of Registration	Name of the Supervisor	Status		
						Course Work	Comprehensive Viva	Thesis Submission Status
1	1RV10PEN01	Sekhar Babu	Coarsening hierarchical mixed-size placement algorithm with timing constraints estimation	02.08.10	Dr. Sumitra Devi	Y	Y	N
2	1RV11PEM05	P N Jayanthi	Novel detection algorithms with dynamic spectrum management in MIMO	24.05.11	Dr.S.Ravishankar	Y	Y	Y
3	1RV11PEN06	K.A Nethravathi	Design of an intruder alarm by wireless underground sensor networks	03.10.11	Dr.S.Ravishankar	Y	Y	Y
4	1RV11PEN22	Rajani Katiyar	A new method of equalization for broadband dynamic spectrum management over twisted pairs	22.02.12	Dr. K.V. Padmaja	Y	Y	Y
5	1RV12PEN06	Chethana G	Tone loading algorithms with space time spreading in MIMO performance analysis	04.10.12	Dr. K.V. Padmaja	Y	Y	N
6	1RV12PEN16	Sujata D Badiger	Fusion of seismic and acoustic signals for classification and detection of multi target using signal processing	17.11.12	Dr.M.Uttara Kumari	Y	Y	Y
7	1RV13PEN11	Vasudeva G	Design and characterization of ADC using TFT's for energy efficient systems	24.10.13	Dr.B.V.Uma	Y	Y	N
8	1RV13PEN10	Ravishankar Hol	Thin film transistor sensor based wireless sensor network	24.10.13	Dr.M.Uttara Kumari	Y	Y	N
9	1RV13PEN18	Sahana B	Development of adaptive resource allocation algorithms for discrete multitone transmission in underwater acoustic	12.12.13	Dr.Abhay Deshpande	Y	Y	N
10	1RV13PEN08	Bhavya R	Development of fast algorithms for feature processing and modelling in motion tracking	24.10.13	Dr.K.S.Geetha	Y	Y	N
11	1RV15PEJ15	Roopa J	Design and development of thin film sensors for wearable electronics	13.3.15	Dr.K.S.Geetha	Y	Y	N
12	1RV15PEJ08	Hemath Kumar C	Design, optimization and implementation of SRAM cells for high density memory application	12.12.14	Dr.Kariyappa B.S	Y	Y	N
13	1RV15PEJ14	Praveen kumar Y	Design, optimization, testing and comparative analysis of vedic multipliers using reversible logic	12.12.14	Dr.Kariyappa B.S	Y	Y	N
14	1RV15PEJ22	Binu	Design and development of an artificial inspired search algorithm for fault diagnosis of electronic circuits and its applications	12.12.14	Dr.Kariyappa B.S	Y	Y	N
15	1RV15PEJ03	Mohana	Design analysis of a smart intelligent low power visual surveillance system for security applications	12.12.14	Dr.H.V.Ravish Arad	Y	Y	N
16	1RV15PEJ04	Muralidharan V	Development and characterisation of sensors for vibration survey on aero engines	12.12.14	Dr.Suresh (Chemical)	Y	Y	N
17	1RV15PEJ12	Vivekanand GS	Model based verification and validation of electronic engine controller performance for a developmental aero engine	12.12.14	Dr.Suresh (Chemical)	Y	Y	N
18	1RV16PEJ09	Smitha Gayathri I	Mitigation of impulse noise and load effects in broad band PLC	08.12.15	Dr.Usha Rani K.R	N	N	N
19	1RV16PEJ16	Arun Kumar P C	Performance improvement of sigma-delta ADC for wide band applications	10.12.15	Dr.H.V.Ravish Arad	Y	Y	N
20	1RV16PEJ06	Kumar P	FPGA based low complexity, high performance architecture for FMBC transmitter	10.12.15	Dr.Usha Rani K.R	N	Y	N
21	1RV16PEJ20	Naganna S Sollap	An efficient VLSI implementation of enhanced edge preserving impulse noise removal techniques	27.01.16	Dr.H.V.Ravish Arad	N	N	N
22	1RV17PEA33	Anitha N	Low power high speed digital circuit design based on carbon nano tube technology	02.02.17	Dr.Srividya	Y	Y	N
23	1RV17PEA30	Latha S	Low power high speed digital circuit design based on carbon nano tube technology	02.02.17	Dr.Srividya	Y	N	N
24	1RV17PEA32	Govind M R	Modeling and Design aspects of FinFETs for Low Power SRAM Circuit Design	14.03.17	Dr.H.V.Ravish Arad	N	N	N
25	1RV17PEA31	Padmavati	Compression and cancellation of artefact of ECG signals using modified non linear filter	02.02.17	Dr.Veenadevi S.V	Y	Y	N
26	1RV18PEC03	Sudha Karbari	Design and development of artificial epithelium membrane for biological application	31.12.17	Dr.M.Uttara Kumari	Y	N	N
27	1RV15PEJ41	Manjunath Mana	Design and classification of electronic nose system	12.12.14	Dr.Abhay Deshpande	Y	Y	N
28	1RV18PEC01	Praveena.N	Design and analysis of high speed low power memory devices	31.12.17	Dr.Shylashree N	N	N	N

29	1RV18PEC02	Mr.Ravikant G B	Optimized hardware architecture for machine learning applications	21.04.18	Dr.B.V.Uma	N	N	N
30	1RV19PEC08	Mrs.Sameera P	Design of automatic drone system to identify pomegranate diseases	23.03.18	Dr.Abhay Deshpande	N	N	N
31	1RV19PEC06	V Vijayalakshmi	Modeling and Simulation of MEMS Based Toxic Gas Sensors	22.03.19	Dr.K.S.Geetha	N	N	N
32	1RV19PEC04	Rajith Kumar BK	Development of Novel Algorithm to recognize stone inscripted Kannada words based on convolution Neural network.	19.06.19	Dr.B.V.Uma	N	N	N
33	1RV19PEC07	Sowmya Nag	Development of Automated Detection and grading of CashewKernels	20.06.19	Dr.Veenadevi S.V	N	N	N
34	1RV19PEC01	Soumya R Nadgi	Device Modeling and Performance evaluation of GaN HEMT.	22.03.19	Dr.Srividya	N	N	N
35	1RV19PEC05	Keka Mukhopad	Device Modeling and Simulation of thin film transistors	19.06.19	Dr.Srividya	N	N	N
36	1RV19PEC02	Neetu S	Design and Analysis of Performance Enhancement Algorithms for Software Defined Networks.	04.07.18	Dr.H.V.Ravish Arad	N	N	N
37	1RV19PEC03	Sushant G	A Network Simulator for Wireless Testing of Mobile Applications and Prototyping Wireless Applications	04.07.18	Dr.H.V.Ravish Arad	N	N	N