



**RV College of
Engineering®**



Electronics & Communication Engineering

Bachelor of Engineering (B.E)

Scheme And Syllabus Of VII & VIII Semester
(2021 Scheme)

B.E. Programs : AI, AS, BT, CH, CS, CV, EC, EE, EI, ET, IM, IS, ME.

M. Tech (13) MCA, M.Sc. (Engg.)

Ph.D. Programs : All Departments are recognized as Research Centres by VTU Except AI & AS

2024

99TH
NIRF RANKING
IN ENGINEERING
(2024)

TIMES HIGHER EDUCATION WORLD UNIVERSITY
RANKINGS-2023

1501+
TIMES HIGHER EDUCATION WORLD UNIVERSITY
RANKINGS-2023 (ASIA)
501-600

EDUFUTURE EXCELLENCE AWARD

BEST PRIVATE ENGINEERING
UNIVERSITY (SOUTH)

BY ZEE DIGITAL

1001+
SUBJECT RANKING
(ENGINEERING)

801+
SUBJECT RANKING
(COMPUTER SCIENCE)

IIRF 2023
ENGINEERING RANKING INDIA

NATIONAL RANK-10
STATE RANK - 2
ZONE RANK - 5



QS-IQUAGE
DIAMOND UNIVERSITY
RATING (2021-2024)

17
Centers of
Excellence

11
Centers of
Competence

212
Publications On
Web Of Science

669
Publications Scopus
(2023 - 24)

1093
Citations

70
Patents Filed

39
Patents Granted

11
Skill Based
Laboratories
Across Four Semesters

61
Published Patents

CURRICULUM STRUCTURE

61 CREDITS
PROFESSIONAL
CORES (PC)

23 CREDITS
BASIC SCIENCE

22 CREDITS
ENGINEERING
SCIENCE

18 CREDITS
PROJECT WORK /
INTERNSHIP

12 CREDITS*
OTHER ELECTIVES
& AEC

12 CREDITS
PROFESSIONAL
ELECTIVES

12 CREDITS
HUMANITIES &
SOCIAL SCIENCE

160
CREDITS
TOTAL

*ABILITY ENHANCEMENT COURSES (AEC),
UNIVERSAL HUMAN VALUES (UHV),
INDIAN KNOWLEDGE SYSTEM (IKS), YOGA.

MOUS: 90+ WITH
INDUSTRIES / ACADEMIC
INSTITUTIONS IN INDIA & ABROAD

EXECUTED MORE THAN
RS.40 CRORES WORTH
SPONSORED
RESEARCH PROJECTS &
CONSULTANCY WORKS
SINCE 3 YEARS



**RV College of
Engineering®**



Electronics & Communication Engineering

Bachelor of Engineering (B.E)

Scheme And Syllabus Of VII & VIII Semester
(2021 Scheme)

B.E. Programs : AI, AS, BT, CH, CS, CV, EC, EE, EI, ET, IM, IS, ME.

M. Tech (13) MCA, M.Sc. (Engg.)

Ph.D. Programs : All Departments are recognized as Research Centres by VTU Except AI & AS

2024



VISION

Leadership in Quality Technical Education, Interdisciplinary Research & Innovation, with a Focus on Sustainable and Inclusive Technology.

MISSION

- 1.To deliver outcome-based Quality education, emphasizing on experiential learning with the state-of-the-art infrastructure.
- 2.To create a conducive environment for interdisciplinary research and innovation.
- 3.To develop professionals through holistic education focusing on individual growth, discipline, integrity, ethics, and social sensitivity.
- 4.To nurture industry-institution collaboration leading to competency enhancement and entrepreneurship.
- 5.To focus on technologies that are sustainable and inclusive, benefiting all sections of the society.

QUALITY POLICY

Achieving Excellence in Technical Education, Research and Consulting through an Outcome Based Curriculum focusing on Continuous Improvement and Innovation by Benchmarking against the global Best Practices.

CORE VALUES

Professionalism, Commitment, Integrity, Team Work, Innovation



**RV College of
Engineering®**

Mysore Road, RV Vidyaniketan Post,
Bengaluru - 560059, Karnataka, India

Go, change the world®

ELECTRONICS & COMMUNICATION ENGINEERING

DEPARTMENT VISION

Imparting quality technical education through interdisciplinary research, innovation and teamwork for developing inclusive & sustainable technology in the area of Electronics and Communication Engineering.

DEPARTMENT MISSION

1. To impart quality technical education to produce industry-ready engineers with a research outlook.
2. To train the Electronics & Communication Engineering graduates to meet future global challenges by inculcating a quest for modern technologies in the emerging areas.
3. To create centers of excellence in the field of Electronics & Communication Engineering with industrial and university collaborations.
4. To develop entrepreneurial skills among the graduates to create new employment opportunities.



PROGRAM EDUCATIONAL OBJECTIVES

- PEO1:** To apply concepts of mathematics, science and computing to Electronics and Communication Engineering
- PEO2:** To design and develop interdisciplinary and innovative systems.
- PEO3:** To inculcate effective communication skills, team work, ethics, leadership in preparation for a successful career in industry and R & D organizations.

PROGRAM SPECIFIC OUTCOMES

- PSO1:** Should be able to clearly understand the concepts and applications in the field of Communication/networking, signal processing, embedded systems, and semiconductor technology.
- PSO2:** Should be able to associate the learning from the courses related to Microelectronics, Signal processing, Microcomputers, Embedded and Communication Systems to arrive at solutions to real world problems.
- PSO3:** Should have the capability to comprehend the technological advancements in the usage of modern design tools to analyze and design subsystems/processes for a variety of applications.
- PSO4:** Should possess the skills to communicate in both oral and written forms, the work already done and the future plans with necessary road maps, demonstrating the practice of professional ethics and the concerns for societal and environmental wellbeing.

LEAD SOCIETY

Institute of Electrical and Electronics Engineers (IEEE)



RV College of Engineering®

Mysore Road, RV Vidyaniketan Post,
Bengaluru - 560059, Karnataka, India

Go, change the world®

ABBREVIATIONS

Sl. No.	Abbreviation	Meaning
1.	VTU	Visvesvaraya Technological University
2.	BS	Basic Sciences
3.	CIE	Continuous Internal Evaluation
4.	SEE	Semester End Examination
5.	PE	Professional Core Elective
6.	GE	Global Elective
7.	HSS	Humanities and Social Sciences
8.	PY	Physics
9.	CY	Chemistry
10.	MA	Mathematics
11.	AS	Aerospace Engineering
12.	AI & ML	Artificial Intelligence & Machine Learning
13.	BT	Biotechnology
14.	CH	Chemical Engineering
15.	CS	Computer Science & Engineering
16.	CV	Civil Engineering
17.	EC	Electronics & Communication Engineering
18.	EE	Electrical & Electronics Engineering
19.	EI	Electronics & Instrumentation Engineering
20.	ET	Electronics & Telecommunication Engineering
21.	IM	Industrial Engineering & Management
22.	IS	Information Science & Engineering
23.	ME	Mechanical Engineering



INDEX

VII Semester			
Sl No	Course Code	Course Title	Page No
1	21HS71	Constitution of India and Professional Ethics	1
2	21EC72	Microwaves and Radiating Systems	3
3	21EC73GX	Professional Core Elective-III (Group – G)	6-17
4	21EC74HX	Professional Core Elective-IV (Group- H)	18-29
5	21XX75IX	Institutional Electives – II (Group I)	30-61
6	21EC76I	Summer Internship	62-63
7	21EC77P	Minor Project	64-65

VIII Semester			
Sl No	Course Code	Course Title	Page No
1	21EC81P	Major Project	66-68



Bachelor of Engineering in ELECTRONICS AND COMMUNICATION ENGINEERING

VII SEMESTER													
Sl. No.	Course Code	Course Title	Credit Allocation				BoS	Category	Max Marks CIE		SEE Duration	Max Marks SEE	
			L	T	P	Total			Theory	Lab		Theory	Lab
1	21HS71	Constitution of India and Professional Ethics	3	0	0	3	HSS	Theory	100	----	3	100	----
2	21EC72	Microwaves and Radiating Systems (Theory and Practice)	3	0	1	4	EC	Theory + Lab	100	50	3	100	50
3	21EC73GX	Professional Core Elective-III (Group - G)	3	1	0	4	EC	Theory	100	----	3	100	----
4	21EC74HX	Professional Core Elective-IV (Group- H)	3	0	0	3	EC	Theory	100	----	3	100	----
5	21XX75IX	Institutional Electives – II (Group-I)	3	0	0	3	Resp	Theory	100	----	3	100	----
6	21EC76I	Summer Internship- III	0	0	2	2	EC	Internship	----	50	2	----	50
7	21EC77	Minor Project	0	0	2	2	EC	Project	----	50	2	----	50
		Total				21							

VIII SEMESTER													
Sl. No.	Course Code	Course Title	Credit Allocation				BoS	Category	Max Marks CIE		SEE Duration	Max Marks SEE	
			L	T	P	Total			Theory	Lab		Theory	Lab
1	21EC81P	Major Project	0	0	12	12	EC	Project	----	100	03	----	100
		Total				12							



GROUP-G			
Sl. No.	Course Code	Course Title	Credits
1	21EC73G1	High Performance Computing	4
2	21EC73G2	SystemVerilog	4
3	21EC73G3	Optoelectronics and Networks	4
4	21EC73G4	Radar Systems Engineering	4
5	21EC73G5	Algorithms for VLSI Design Automation	4
6	21EC73G6	Automotive Embedded Systems	4

GROUP-H			
Sl. No.	Course Code	Course Title	Credits
1	21EC74H1	ARM Programming & Optimization	3
2	21EC74H2	Design for Testing and Testability	3
3	21EC74H3	Evolution of Communications: 4G to 6G	3
4	21EC74H4	Multimedia Communication	3
5	21EC74H5	System on Chip Design	3
6	21EC74H6	Computer Architecture	3



Group I				
Sl. No.	Course Code	Course Title	BoS	Credits
1	21AS75IA	Unmanned Aerial Vehicles	AS	3
2	21BT75IB	Healthcare Analytics	BT	3
3	21CH75IC	Sustainability and Life Cycle Analysis	CH	3
4	21CM75ID	Advances in Corrosion Science and Management	CM	3
5	21CS75IE	Prompt Engineering	CS	3
6	21CV75IF	Integrated Health Monitoring of Structures	CV	3
7	21EC75IG	Wearable Electronics	EC	3
8	21EE75IH	E-Mobility	EE	3
9	21EI75IJ	Programmable Logic Controllers and applications	EI	3
10	21ET75IK	Space Technology and Applications	ET	3
11	21IS75IL	Mobile Applications Development	IS	3
12	21IM75IM	Project Management	IM	3
13	21IM75IN	Supply Chain Analytics	IM	3
14	21ME75IO	Nuclear Engineering	ME	3
15	21HS75IQ	Cognitive Psychology	HS	3
16	21HS75IR	Principle and Practices of Cyber Law	HS	3



Semester: VII						
CONSTITUTION OF INDIA AND PROFESSIONAL ETHICS						
PROFESSIONAL CORE COURSE						
(Theory)						
Course Code	:	21HS71		CIE	:	100 Marks
Credits: L:T:P	:	3:0:0		SEE	:	100 Marks
Total Hours	:	39L		SEE Duration	:	3 Hours

Unit-I		10 Hrs
Salient features of Indian Constitution; Preamble to the Constitution of India; Provisions Relating to Citizenship in India-Modes of Acquisition and Termination of Citizenship of India. Scope & Extent of Fundamental Rights-Articles 14-32 with case studies; Right to Information Act, 2005 with Case studies.		
Unit – II		10 Hrs
Significance of Directive Principles of State Policy; Fundamental Duties in the Constitution of India; Union Executive- President and State Executive- Governor; Parliament & State Legislature; Council of Ministers; Union and State Judiciary; Emergency provisions; Elections commission . Human Rights & Human Rights Commission.		
Unit –III		05 Hrs
Consumer Protection Law - Definition and Need of Consumer Protection; Consumer Rights under the Consumer Protection Act, 2019; Unfair Trade Practice, Defect in goods, Deficiency in services; Product liability and Penal Consequences, False and Misleading Advertisement, E-Commerce, Alternate dispute Redress mechanism; Redresses Mechanisms under the Consumer Protection Act, 2019.		
Unit –IV		07 Hrs
Introduction to Labour and Industrial Law, Theory and Concept of Industrial Relations, Industrial Relations Code 2020, Code on Social Security 2020, Code on Occupational Safety, Health and Working Conditions 2020, Code on Wages 2020, Industrial Disputes Act. The Factories Act, 1948 ,Analysis of Recent Amendments made in Labour Laws.		
Unit –V		07 Hrs
Scope and aims of engineering ethics (NSPE Code of Ethics), Responsibility of Engineers, Impediments to responsibility. Honesty, Integrity and reliability, Risks, Safety and Liability in Engineering. Corporate Social Responsibility, Statutory Provision regarding prohibition and prevention of Ragging, The Sexual Harassment of Women at Workplace (Prevention, Prohibition and Redressal) Act, 2013.		

Course Outcomes: After completing the course, the students will be able to: -	
CO1	Equips with a comprehensive understanding of the legal and political framework of India, preparing them to engage with complex legal, social, and political issues both as professionals and responsible citizens.
CO2	Effectively advocate for consumer rights, navigate regulatory frameworks, and address emerging challenges in the marketplace & empowers them with the legal knowledge and practical skills necessary to protect consumers and promote fair business practices.
CO3	Equipping with the knowledge and skills to navigate legal, ethical, and social issues in their professional and personal lives & Cultivate a sense of professional integrity and responsibility, emphasizing the importance of ethical behavior in engineering.
CO4	Apply the knowledge to solve practical problems with regard to personal issues & business enterprises

Reference Books	
1.	Dr. J. N Pandey, Constitutional Law of India, Central Law Agency, 2023 Edition



2.	Avtar Singh: Law of Consumer Protection: Principles and Practice, Eastern Book Company, 5 th Edition, 2015, ISBN: 9789351452461.	
3.	S.C. Srivastava: Industrial Relation and Labour Laws, Vikas Publishing House, 8th Kindle Edition 2023, ASIN : B0C5CCJX63	
RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks, adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar / presentation / demonstration (20) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B		
(Maximum of TWO Sub-divisions only) * (Small case lets and case example in one subdivision)case example in one subdivision)		
2	Unit 1: (Compulsory)	16
3&4	Unit 2: Question 3 or 4	16
5&6	Unit 3: Question 5 or 6	16
7&8	Unit 4: Question 7 or 8	16
9&1	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
MICROWAVES AND RADIATING SYSTEMS			
PROFESSIONAL CORE COURSE			
(Theory & Practice)			
Course Code	: 21EC72	CIE	: 100+50
Credits: L: T: P	: 3:0:1	SEE	: 100+50
Total Hours	: 36 L+13P	SEE Duration	: 03 Hours
Unit-I			07 Hrs
Microwave Waveguides			
Introduction, TE, TM waves rectangular waveguides (qualitative analysis TE, TM modes), circular waveguides (quantitative analysis), dominant modes, group velocity phase velocity, and wave impedance, Microwave cavities (quantitative analysis), resonant frequency.			
S-parameters: Introduction, properties of S matrix (qualitative analysis)			
Unit – II			07 Hrs
Microwave Passive Devices			
Waveguide Tee's, Directional couplers, circulators, Power divider, Isolators (Faraday isolator), phase shifters (Rotatory type), Attenuators (Rotatory type), (s-parameters of all devices).			
Microwave Sources			
Multicavity Klystron amplifier, Reflex klystron oscillator, Helix Travelling Wave Tubes- Slow wave Structure, Amplification Process, Introduction to Gallium Nitride devices.			
Unit –III			07 Hrs
Antenna Basics			
Introduction, antenna radiation mechanism, basic Antenna parameters, patterns, beam area, radiation intensity, beam efficiency, diversity and gain, antenna apertures, effective height, bandwidth, radiation, efficiency, antenna temperature and antenna field zones.			
Wire Antennas			
Electric dipoles: Introduction, short electric dipole (Qualitative Analysis) (fields, power density, power radiated, directivity, radiation resistance), Half wave dipoles (field: quantitative analysis power density, power radiated, directivity, radiation resistance).			
Unit –IV			08 Hrs
Antenna Arrays			
Introduction, pattern multiplication, Array of two isotropic point sources, N element linear array with uniform spacing and phase (Array factor), Broadside and end fire array (Directivity, location of beam width, Beam width, etc).			
Antenna Types			
Folded dipole, Yagi-Uda array, parabolic reflectors, log periodic antenna, Rectangular patch antenna, horn antenna. (Qualitative analysis only: Construction, working).			
Unit –V			07 Hrs
Mobile and Wireless Technologies- Overview			
GSM, CDMA, WCDMA, 4G LTE, 5G Architectures, 3GPP and IEEE standards (1G-5G), Integrated Sensing and Communication Architecture. WLAN Architecture (WiFi 6 and 7).			

Course Outcomes (CO):	
After completing the course, the students will be able to:-	
CO 1	Apply microwave circuit analysis techniques to solve problems related to impedance matching, transmission line behavior, and microwave device performance.
CO 2	Analyse wave propagation in Waveguides and characterize the passive microwave components and Antennas.
CO 3	Analyze the radiation characteristics of different antenna types and their suitability for various communication systems.



CO 4	Apply knowledge of wireless communication standards to analyze and compare the performance of different mobile and wireless technologies in various scenarios
-------------	---

Reference Books	
1.	Microwave Engineering: Theory and Techniques, David M. Pozar, 4 th edition, An Indian Adaptation, 2020, ISBN: 978-9388991087
2.	Antenna Theory and Design, C A Balanis, John Wiley & sons, Inc. publication, 4 th Edition, 2016, ISBN: 978-1-118-64206-1
3.	Foundations of Microwave Engineering, R E Collin, IEEE Press on Electromagnetic and Wave Theory, 2 nd Edition, 2007, ISBN-13: 978-0-7803-6031-0
4.	Wireless Communication: Principles and Practice, Theodore S. Rappaport, 2 nd Edition, 2010, ISBN-978-8131731864

Laboratory Experiments	
1.	Study of Mode Curves of Reflex Klystron Source(X-band)
2.	Radiation Characteristics of Pyramidal Horn Antenna (X-band)
3.	Characterization of Microwave Magic Tee, Circulator and Directional Coupler (Waveguide type, X-band)
4.	Design and Simulation of Waveguide Hybrid Ring using HFSS
5.	Design and Simulation of Patch Antenna (coaxial feed), Dipole and Horn antenna using HFSS
6.	Design of Antenna for Mobile Communication Application (Inverted F Antenna) using HFSS
7.	Radiation characteristics of Microstrip Patch and Printed Dipole Antenna(X-band)
8.	Antenna Array Design using Antenna toolbox of MATLAB
9.	Characterization of Lowpass, bandpass and band stop filters (C-Band)
10.	Target Detection at X – Band range using a reflector

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. Each quiz is evaluated for 10 marks adding up to 20 MARKS	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom’s Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Designing & Modeling (10) ADDING UPTO 40 MARKS.	40
4.	LAB: Conduction of laboratory exercises, lab report, observation, and analysis (20 Marks),lab test (10 Marks) and Innovative Experiment/ Concept Design and Implementation (20 Marks) adding up to 50 Marks. THE FINAL MARKS WILL BE 50 MARKS	50
MAXIMUM MARKS FOR THE CIE (THEORY+LAB)		150



RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q.NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100

RUBRIC FOR SEMESTER END EXAMINATION (LAB)		
Q.NO.	CONTENTS	MARKS
1	Write Up	10
2	Conduction of the Experiments	20
3	Viva	20
TOTAL		50



Semester: VII			
HIGH PERFORMANCE COMPUTING			
Professional core Elective (Group- G)			
(Theory)			
Course Code	:	21EC73G1	CIE
Credits: L:T:P	:	3:1:0	SEE
Total Hours	:	36	SEE Duration
			: 100
			: 100
			: 03 Hours

Unit-I	07 Hrs
Introduction to Parallel Computing Motivating Parallelism, Scope of Parallel Computing, Implicit Parallelism: Trends in Microprocessor Architectures, Limitations of Memory System Performance, Dichotomy of Parallel Computing Platforms, Physical Organization of Parallel Platforms, Communication Costs in Parallel Machines, Routing Mechanisms for Interconnection Networks, Impact of Process-Processor Mapping and Mapping Techniques.	
Unit – II	07 Hrs
Principles of Parallel Algorithm Design Preliminaries, Decomposition Techniques, Characteristics of Tasks and Interactions, Mapping Techniques for Load Balancing, Methods for containing Interaction Overheads, Parallel Algorithms Models.	
Unit –III	07 Hrs
Analytical Modeling of Parallel Programs Sources of Overhead in Parallel Programs, Performance Metrics for Parallel Systems, The Effect of Granularity on Performance, Scalability of Parallel Systems, Minimum Execution Time and Minimum Cost-Optimal Execution Time, Asymptotic Analysis of Parallel Programs, Other Scalability Metrics	
Unit –IV	07 Hrs
Programming using the Message Passing Paradigm Principles of Message Passing Programming, Building Blocks, MPI, Topologies and Embedding, Overlapping Communication with computation, Collective Communication and computation operations, Groups and Communicators.	
Unit –V	08 Hrs
CUDA Programming Introduction to CUDA architecture for parallel processing, CUDA Parallelism Model, Foundations of Shared Memory, Introduction to CUDA-C, Parallel programming in CUDA-C, Thread Cooperation and Execution Efficiency, Constants memory and events, memory management, CUDA C on multiple GPUs, Hashing and Natural Parallelism, Scheduling and Work Distribution, Atomics, Barriers and Progress, Transactional Memory.	

Course Outcomes (CO):	
After completing the course, the students will be able to:-	
CO 1	Understand of parallel computing concepts, including parallel architectures, parallel programming models, and algorithms suitable for parallel execution.
CO 2	Design and implement parallel algorithms for solving scientific and engineering problems efficiently on parallel architectures.
CO 3	Use programming languages and frameworks commonly used in high performance computing.
CO 4	Evaluate and compare different high performance computing solutions based on performance, scalability, and efficiency metrics.

Reference Books	
1	Introduction to Parallel Computing, Ananth Grama, Anshul Gupta, George Karypis, Vipin Kumar, 2 nd Edition, 2013, Pearson Education, ISBN 13: 9788131708071.
2	Parallel Programming with Open ACC, Rob Farber, 1 st Edition, 2016, Morgan Kaufmann (MK) Publication, ISBN :9780124103979.
3	Advanced Computer Architecture, Kai Hwang, Naresh Jotwani, 3 rd Edition, Mc Graw Hill Education.



4	CUDA Programming: A Developers Guide to Parallel Computing with GPUs, Shane Cook, 1 st Edition, 2013, Morgan Kaufmann, ISBN:9780124159334.
---	---

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
SYSTEMVERILOG			
Professional core Elective (Group- G)			
(Theory)			
Course Code	: 21EC73G2	CIE	: 100
Credits: L:T:P	: 3:1:0	SEE	: 100
Total Hours	: 36 L	SEE Duration	: 03 Hrs

Unit-I	07 Hrs
<p>Introduction to SystemVerilog Evolution of SystemVerilog (Contributors to SystemVerilog, Key SystemVerilog enhancements for hardware design and verification. Data types, Built-In Data Types, Fixed-Size Arrays, Dynamic Arrays, Queues, Associative Arrays, Linked Lists) Array Methods, choosing a Storage Type, Creating New Types with typedef, Creating User-Defined Structures, Type conversion, Enumerated Types, Constants, Strings, Procedural Statements, Interface overview, Tasks, Functions, and Void Functions.</p>	
Unit – II	07 Hrs
<p>Classes, Interfaces, Clocking Class basics, class declaration, class members and methods, class handles, class object construction, super and <i>this</i> keyword, object handles, user defined constructors, class extension and inheritance, chaining new() constructors, overriding class methods, extending class methods, local and protected keywords. Generic interfaces, interfaces Vs modports, how interfaces work, requirements of good interface, interface constructs, interface modports. Clocking blocks, clocking skews, clocking block scheduling.</p>	
Unit –III	08 Hrs
<p>Program block, Randomization and Constrained randomization Fundamental test bench construction, program blocks, program block interaction with modules, final blocks. constrained random variables, directed vs random testing, rand and randc class data types, randomize randomizing class variables, randcase, built-in-randomization methods, randsequence. Randomization constraints, Constraint Blocks, Constraint Inheritance, Distribution Constraint, Constraint inside, Constraint implication, fork-join processes. Structured flow to develop a Layered SystemVerilog Testbench.</p>	
Unit –IV	07 Hrs
<p>SystemVerilog Coverage and Assertions Cover groups, cover points, cover point bins and labels, cross coverage, cover group options, coverage capabilities. Assertion definition, assertion benefits, SystemVerilog assertion types, immediate assertions, concurrent assertions, assert and cover properties and labels, overlapping and non-overlapping implications, assertion and coverage examples.</p>	
Unit –V	07 Hrs
<p>Universal Verification Methodology Introduction to Universal Verification Methodology, Overview of UVM Base Classes and Simulation Phases in UVM and UVM macros. UVM environment structure, Connecting DUT and Testbench. Examples on UVM.</p>	

Course Outcomes: After completing the course, the students will be able to	
CO1:	Analyze the behavior of different digital blocks using SystemVerilog.
CO2:	Apply the SystemVerilog verification features for effective and efficient verification.
CO3:	Analyze the system through Coverage and Assertion based verification.
CO4:	Develop Verification Environment for Digital System.



Reference Books	
1	SystemVerilog for Design - A Guide to Using SystemVerilog for Hardware Design and Modeling, Stuart Sutherland, Simon Davidmann and Peter Flake, 2E, Springer Science, 2006, ISBN-13: 978-0387-3339-91.
2	SystemVerilog for Verification-A Guide to Learning the Testbench Language Features, C Spear, Springer Science, IEEE press, 2006, ISBN-13: 978-0387-2703-64.
3	IEEE Standard for SystemVerilog-Unified Hardware Design, Specification and Verification IEEE Computer Society, IEEE Press, 2009, ISBN: 978-0-7381-6129-7
4	Step-by-Step Functional Verification with SystemVerilog and OVM, Sasan Iman Si, CA Spring 2008, ISBN: 978-0981656212
5	System Verilog Primer, J Bhaskar, 2010, Star Galaxy Publishing, ISBN 13: 9780965039116.

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII				
OPTOELECTRONICS AND NETWORKS				
Professional core Elective (Group- G)				
(Theory)				
Course Code	:	21EC73G3	CIE	: 100 Marks
Credits: L:T:P	:	3:1:0	SEE	: 100 Marks
Total Hours	:	36L	SEE Duration	: 03 Hours

Unit-I	07 Hrs
<p>Ray Optics Postulates, Simple optical components: Mirrors, Planar Boundaries, Spherical Boundaries and Lenses, Matrix Optics: The-Ray Transfer Matrix, Matrices of Simple and Cascaded Optical Components.</p> <p>Wave Optics Postulates, Monochromatic Waves: Complex Representation and Helmholtz equation, Elementary Waves, Paraxial Waves, Simple Optical Components: Reflection and Refraction, Transmission through Optical Components, Interference: Two Waves and Multiple-Wave Interference.</p>	
Unit – II	07 Hrs
<p>Beam Optics The Gaussian beam: Complex Amplitude, Properties, Power, Beam Width, Beam Divergence, Depth of focus, Beam Quality.</p> <p>Statistical Optics Statistical Properties of Random Light: Optical Intensity, Temporal Coherence and Spectrum, Spatial and Longitudinal Coherence, Interference of Partially Coherent Light.</p>	
Unit –III	07 Hrs
<p>Photon Optics The Photon, Photon Streams, Interactions of Photons with Atoms.</p> <p>LASER Amplifiers Theory, Amplifier Pumping, Common LASER amplifiers, Theory of LASER oscillation, Characteristics of LASER Output.</p> <p>Semiconductor Photon Sources Light-Emitting Diodes, Semiconductor Optical Amplifiers, Laser Diodes.</p> <p>Semiconductor Photon Detectors Photodetectors, Photoconductors, Photodiodes, Avalanche Photodiodes, Noise in Photodetectors.</p>	
Unit –V	07 Hrs
<p>Optical interconnects Guided wave interconnects, optical interconnects in microelectronics.</p> <p>Passive optical routers Wavelength-based routers, polarization, phase and intensity based routers.</p> <p>Photonic Switches Architecture of space switches, Implementations of optical space switches, all optics space switches, Wavelength domain switches, time domain switches and packet switches.</p>	

Course Outcomes (CO):	
After completing the course, the students will be able to:-	
CO 1	Apply mathematical principles to various optical components and analyze their performance.
CO 2	Explain the basic properties of light: Reflection, Refraction, Interference, Diffraction and Coherence.
CO 3	Design circuits involving optical sources and detectors based on given design parameters.
CO 4	Illustrate the networking aspect of optical switches and describe the various concepts associated with it.
Reference Books	
1	Fundamentals of Photonics, B.E.A. Saleh, M.C.Teich, Wiley, 2nd Edition, 2007, ISBN: 978-0-471-35832-9.



2	Optical Fiber Communications: Principles and Practice, John M. Senior, Pearson Prentice Hall, 3rd Edition, 2009, ISBN: 978-0-13-032681-2.
3	Optical Fiber Communications, Gerd Keiser, Pearson Education, 3rd Edition, 2010, ISBN: 978-8131732663.
4	Optical Networks: A Practical Perspective, Rajiv Ramaswami, Kumar N. Sivarajan and Galen H. Sasaki, 3rd Edition, 2010, ISBN: 978-0-12-374092-2.

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII					
Radar Systems Engineering					
Professional core Elective (Group- G)					
(Theory)					
Course Code	:	21EC73G4	CIE	:	100
Credits: L:T:P	:	3:1:0	SEE	:	100
Total Hours	:	36	SEE Duration	:	03 Hours

Unit-I		06Hrs
<p>Fundamentals for Radar Signals & Signal processing: Radar range equation; RCS statistics; Data cube; Sampling and Quantisation; Review Fourier, Analysis and the Z-Transform; Digital Filtering and Random Signals and signal integration; Correlation and Matched Filters</p> <p>Threshold Detection of Radar Targets: Detections strategies and optimal detectors; Statistical models for noise and target RCS, threshold detection.</p>		
Unit – II		08 Hrs
<p>constant False Alarm Rate Detectors: CFAR Detectors, including cell Averaging; Robust cFAR and comparisons</p> <p>Doppler Processing: Doppler and the Pulsed Radar; Moving Target Indication, pulse doppler; Clutter mapping, pulse pair processing</p> <p>Radar Measurements: Radar signal model and accuracy of measurements; Parameter Estimation: Range; parameter estimation: phase, doppler and range rate.; RCS estimation and angle measurements, coordinate systems.</p>		
Unit –III		07 Hrs
<p>Radar Tracking Algorithms: Basic tracking, kinematic motion; Measurement models and radar track filtering.; Measurement-to-track data association and track performance assessment.</p> <p>Fundamentals of Pulse Compression Waveforms: Matched filters for pulse compression and range resolution; Straddle loss; pulse compression waveforms, compression gain, LFM; Matched filter implementation, range sidelobe reduction Ambiguity Functions and LFM Summary; Phase-coded waveforms, biphasic; polyphase codes.</p>		
Unit –IV		07 Hrs
<p>Overview of Radar Imaging: General imaging considerations and resolution vs sampling; Data collection and image formation. Image phenomenology.</p> <p>Introduction to Synthetic Aperture Imaging : Fundamental SAR Concepts and Relations, Stripmap SAR Data Characteristics, Stripmap SAR Image Formation Algorithms, Spotlight SAR Data Characteristics, The Polar Format Image Formation Algorithm for Spotlight SAR, Backprojection, Interferometric SAR</p>		
Unit –V		08Hrs
<p>ADAS, Autonomous Driving, Image Formation, Calibration, Object detection, Classification, Multi-object tracking, Camera in ADAS and Autonomous Driving Application.</p>		

Course Outcomes (CO):	
After completing the course, the students will be able to:-	
CO 1	comprehensive understanding of the fundamental principles, techniques, and strategies involved in radar signal processing and threshold detection of radar targets.
CO 2	Analyze, and optimize radar signal processing algorithms, perform accurate radar measurements, and effectively detect and track targets in cluttered and dynamic environments.
CO 3	Test and validate in interpreting SAR images, identifying targets, and extracting useful information for various applications such as reconnaissance, surveillance, and environmental monitoring.
CO 4	Design, implement, and evaluate practical projects related to ADAS and autonomous driving, including prototype development, simulation studies, and real-world testing scenarios.

Reference Books	
1	Principles of Modern Radar", Richards, Scheer and Holm, Scitech Publishing, 2 nd Edition, 2010, SciTech Publishing Inc, ISBN-13 978-1891121524



2	Fundamentals of Radar Signal Processing, Mark A. Richards, 3rd Edition, 2022, Publication Date & Copyright: 2022 McGraw Hill, ISBN: 9781260468717
3	Introduction to radar systems, Skolnik, 2nd Edition, 2007, McGraw Hill, ISBN 9780070634411
4	Radar Principles, Technology, Byron Edde, 1st Edition, 2012, Pearson Education Limited, ISBN: 139788131713839

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII				
ALGORITHMS FOR VLSI DESIGN AUTOMATION				
Professional core Elective (Group- G)				
(Theory)				
Course Code	:	21EC73G5	CIE	: 100 Marks
Credits: L:T:P	:	3:1:0	SEE	: 100 Marks
Total Hours	:	36L	SEE Duration	: 03 Hours
Unit-I				07 Hrs
Architectural level synthesis and Scheduling Algorithms				
Introduction, Circuit specification for architectural level synthesis, A model for scheduling problems, Scheduling without and with resource constraints, Scheduling algorithms for extended sequencing models, Scheduling pipelined circuits, Resource sharing and binding.				
Unit – II				07 Hrs
Data Structure and Basic Algorithms				
Basic Terminology, Graph Search Algorithms, Computational Geometry Algorithms, Basic Data structures.				
Partitioning				
Problem Formulation, Classification of Partitioning Algorithms, Group migration Algorithms, Simulated Annealing and evolution algorithm, other partitioning algorithms				
Unit –III				07Hrs
Floor Planning and Pin Assignment				
Problem formulation, classification, Constraint based, Integer programming based, rectangular Dualization, simulated evolution floor planning algorithms.				
Placement				
Problem formulation, Classification, Simulation based, Partitioning based Placement Algorithms				
Unit –IV				08 Hrs
Global Routing				
Problem formulation, Classification, Maze routing Algorithms, Line Probe Algorithms, shortest path-based Algorithms, Steiner tree-based Algorithms				
Detailed Routing				
Problem formulation, Classification single Layer routing, General river routing, Single row routing				
Unit –V				07 Hrs
Channel, Clock and Power Routing				
Two-layer channel routing Algorithms, Design considerations for the clocking system, delay calculation for clock trees, Problem formulation, Clock routing Algorithms, H-tree based Algorithms, MMM Algorithms, Geometric matching based Algorithms, Introduction to compaction, shadow propagation algorithm.				

Course Outcomes: After completing the course, the students will be able to	
CO1:	Analyze each stage of VLSI design flow to develop a CAD tool for physical design.
CO2:	Apply design knowledge to develop algorithms for VLSI design automation.
CO3:	Evaluate the algorithms for optimizing VLSI design with respect to speed, power and area.
CO4:	Create an optimized VLSI IC design technique using various algorithms.



Reference Books	
1	Synthesis and Optimization of Digital Circuit, 1994, Giovanni De Micheli, McGraw- Hill, ISBN: 10-0070163332
2	Algorithms for VLSI Physical Design Automation, N.A. Sherwani, 2002, Kluwar Academic Publishers, ISBN: 0-7923-8393-1
3	An Introduction to VLSI Physical Design, M Sarraf Zadeh, C K Wong, 1996, McGraw Hill, ISBN:0070571945
4	Algorithms for VLSI Design Automation , S.H. Gerez, 1998, John Wiley & Sons, ISBN: 978-0-471-98489-4

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
AUTOMOTIVE EMBEDDED SYSTEMS			
Professional core Elective (Group- G)			
(Theory)			
Course Code	: 21EC73G6	CIE	: 100 Marks
Credits: L:T:P	: 3:1:0	SEE	: 100 Marks
Total Hours	: 36 Hrs	SEE Duration	: 3 Hours

Unit-I	7 Hrs
Automotive Systems Overview Automotive Vehicle Technology, Overview of Vehicle Categories, Various Vehicle Sub Systems like Chassis, Body, Driveline, Engine technology, Fuelling technology, vehicle Emission, Brakes, Suspension, Emission, Doors, Dashboard instruments, Wiring Harness, Safety & Security, Comfort & Infotainment, Communication & Lighting, Future Trends in Automotive Embedded Systems: Hybrid Vehicles, Electric Vehicles.	
Unit – II	7 Hrs
Automotive Sensors and Actuators Automotive Control System Applications of Sensors and Actuators. Sensors: Air Flow Sensor, Engine Crankshaft Angular Position Sensor, Throttle Angle Sensor, Temperature Sensor, Sensors for Feedback Control, Sensors for Driver Assistance System: Radar, Lidar, Video Technology. Actuators: Solenoids, Piezo Electric Force Generators, Fluid mechanical Actuators, Electric Motors and Switches.	
Unit –III	8 Hrs
Automotive Control System Design-I Digital Engine Control, Features, Control Modes for Fuel Control, Discrete Time Idle Speed Control, EGR Control, Variable Valve Timing Control, Electronic Ignition Control, Integrated Engine Control System.	
Unit –IV	7 Hrs
Automotive Control System Design-II Cruise Control System, Cruise Control Electronics, Anti-locking Braking System, Electronic Suspension System, Electronic Steering Control, Four-Wheel Steering, ADAS Systems, Autonomous Vehicles, Application of IoT in automobiles	
Unit –V	7 Hrs
Automotive Protocols LIN, MOST, Flex Ray, Test, Calibration and Diagnostics tools for networking of electronic systems like ECU Software and Testing Tools, ECU Calibration Tools, Vehicle Network Simulation, Advanced Trends in Automotive Electronics: AUTOSAR Architecture.	

Course Outcomes (CO):	
After completing the course, the students will be able to:-	
CO 1	Understand the fundamentals of different Automotive Systems.
CO 2	Integrate various sensors and actuators into automotive systems, and understanding their functionalities in vehicle control and monitoring.
CO 3	Design control systems specifically applied to automotive engineering, including engine control, transmission control, chassis control, and vehicle dynamics control.
CO 4	Provide technical embedded solutions for the development of automotive Systems.

Reference Books	
1.	Understanding Automotive Electronics-An Engineering Perspective, William B. Ribbens, 7 th Edition, Butterworth-Heinemann Publications.
2.	Automotive Electronics Handbook, Robert Bosch, 2004, John Wiley and Sons, ISBN-0471288357



3.	Automotive Control Systems for Engine Driveline and Vehicle, Kiencke, Uwe, Nielsen, Lars, 2 nd Edition, Springer Publication.
4.	Vehicle Safety Communications: Protocols, Security and Privacy, Tao Zhang, Luca Delgrossi, Wiley Publication.
5.	Automobile Electrical and Electronic Systems, Tom Denton, 4 th Edition, Routledge, 2012.

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding upto 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving(10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100
RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
ARM PROGRAMMING & OPTIMIZATION			
Professional core Elective (Group- H)			
(Theory)			
Course Code	:	21EC74H1	CIE : 100 Marks
Credits: L:T:P	:	3:0:0	SEE : 100 Marks
Total Hours	:	36 Hrs	SEE Duration : 03 Hours

Unit-I	08 Hrs
ARM Programming in C Overview of C Compilers and optimization, basic C data types, C looping structures, register allocation, function calls, pointer aliasing, structure arrangement, bit fields, unaligned Data and Endianness, division, floating point, inline functions and inline assembly, portability issues.	
Unit – II	07 Hrs
Writing and Optimizing ARM Assembly Code Writing assembly code, profiling and cycle counting, instruction scheduling, register allocation, conditional execution, looping constructs, Bit manipulation, efficient switches. Handling unaligned data	
Unit –III	07 Hrs
Low Power & System Control Features ARM Cortex M4 Low Power Designs, Low power features, Using WFI and WFE instructions in programming, Developing low power applications, SysTick timer, Configuration control register. Auxiliary control register, Co-processor access control register.	
Unit –IV	07 Hrs
Digital Signal Processing on ARM Representing a digital signal, Introduction to DSP on the ARM, FIR filters, Realization of filters on Cortex M3 & M4, IIR Filters, Realization of filters on Cortex M3 & M4, CMSIS DSP Library, Adaptive filters, LMS algorithm, Design and Realization.	
Unit –V	07 Hrs
OS Support Features of ARM Cortex M4 Overview of OS support features, Shadowed stack pointer, SVC exception, PendSV exception, Context switching in action, Exclusive accesses and embedded OS.	

Course Outcomes (CO):	
After completing the course, the students will be able to: -	
CO 1	Analyse different optimization methods, hardware and software constructs to realize signal processing operations and interpret the role of low power and OS support features of ARM architectures.
CO 2	Develop efficient ARM programs for specific tasks, considering factors like memory management and performance optimization.
CO 3	Implement filters on Cortex M3 & M4 using CMSIS DSP Library and solve real-world problems using special features of ARM architectures.
CO 4	Engage in usage tools to formulate, design and analyze different applications realized with embedded processors.

Reference Books	
1.	ARM System Developers Guide, Andrew N Sloss, Dominic Symes, Chris Wright, Elsevier, Morgan Kaufman publishers, 2008, ISBN-13:9788181476463.
2.	Digital Signal Processing on using ARM Cortex M4, Donald S Reay, 2016, John Wiley & Sons, ISBN 978-1-118-85904-9.
3.	The Definitive Guide to the ARM Cortex-M3& M4 Processors, Joseph Yiu, 3 rd Edition, Newness (Elsevier), 2014, ISBN:978-93-5107-175-4.
4.	User guides and reference manuals for ARM software development and modeling tools.



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII				
DESIGN FOR TESTING AND TESTABILITY				
Professional core Elective (Group- H)				
(Theory)				
Course Code	:	21EC74H2	CIE	: 100 Marks
Credits: L:T:P	:	3:0:0	SEE	: 100 Marks
Total Hours	:	36 Hrs	SEE Duration	: 03 Hours
Unit-I				07 Hrs
Introduction to Testing Introduction to Testing, Role of testing VLSI circuits, VLSI trends affecting testing, Economics of Testing, Reliability of testing, Faults in digital circuits.				
Fault Modeling Functional Testing, Structural Testing, Types of Fault Models, Stuck-at Faults, Bridging Faults, cross point faults, Fault Modeling with shrinking of technology node, Fault Equivalence, Fault Dominance.				
Unit – II				07 Hrs
Fault Simulation Fault Simulation algorithm- Serial, Parallel, Deductive and Concurrent Fault Simulation.				
Testability Measure Controllability, Observability, SCOAP measures for combinational and sequential circuits.				
Unit –III				07 Hrs
ATPG for Combinational Circuits Path Sensitization Methods, Roth’s D- Algorithm, Boolean Difference, Complexity of Sequential ATPG, Time Frame Expansion.				
Design for Testability Ad-hoc, Structured DFT- Scan method, Scan Design Rules, Overheads of Scan Design, partial scan methods, multiple chain scan methods. Case Study: Streaming Scan Network, Hierarchical Scanning, Silent Data Corruption.				
Unit –IV				07 Hrs
Self-test And Test Algorithms Built-In self-Test, test pattern generation for BIST, response compaction - Parity checking, Ones counting, Transition Count, Signature analyser (SISR and MISR). Circular BIST, Logic BIST Architectures, Digital Boundary Scan (IEEE 1149.1): TAP controller, Test architecture & operations, On chip Test support with boundary scan, board and system level boundary scan control architecture.				
Unit –V				08 Hrs
Memory Testing Functional Fault Modeling & Testing of SRAM: Functional Testing of RAMs, Fault modeling for the Mealy automaton, Fault classification, Algorithms for detecting SAFs, TFs, AFs and CFs. Reduced functional faults – MARCH & MAT+ algorithm. Test generation for embedded RAMs, MBIST.				
Course Outcomes (CO): After completing the course, the students will be able to:-				
CO 1	Apply the concepts in testing to obtain a better yield in IC design.			
CO 2	Explore various fault simulation techniques and identify the design for testability methods.			
CO 3	Apply various BIST techniques to improve the testability of IC design.			
CO 4	Analyse different fault modelling methods for memory testing.			



Reference Books	
1.	Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits, M. L. Bushnell and V. D. Agrawal, Kluwer Academic Publishers, 2000, ISBN:0-7923-7991-8.
2.	VLSI Test Principles and Architectures, L. T. Wang, C. W. Wu, and X. Wen, Morgan Kaufmann, 2006, ISBN-13: 978-0-12-370597-6
3.	Digital Systems Testing and Testable Design, M. Abramovici, M. A. Breuer, and A. D. Friedman, Computer Science Press, 1990, ISBN: 0-7167-8179-4.
4.	Testing & Testable Design of High-density Random-Access Memories, Pinaki Mazumder, Kanad Chakraborty, The University of Michigan, Kluwer Academy Publishers, ISBN-13: 978- 1-4612-8632-5 e-ISBN-13: 978-1-4613-1 451 -6 001: 10.1007/978-1-4613-1451-6.

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100
RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII					
5G and Beyond Technologies					
Professional core Elective (Group- H)					
(Theory)					
Course Code	:	21EC74H3	CIE	:	100
Credits: L:T:P	:	3:0:0	SEE	:	100
Total Hours	:	36	SEE Duration	:	03 Hours

Unit-I		07 Hrs
<p>Review of - Statistical characterization of multipath channels – Binary signaling over frequency non selective Rayleigh fading channel, and Frequency selective fading channel. Fading and Diversity: - Diversity techniques for performance improvement with binary signaling over FNS, Slow fading channels – power combining and Maximal ratio combining; Frequency selective channels – Rake receivers, Performance, Tap weight Synchronization. Channel estimation and synchronization for Single and Multicarrier carrier LTE Capacity of wireless channel: A Review of Differential Entropy. Shannon’s Theorem,</p>		
Unit – II		07 Hrs
<p>Capacity of a Linear time invariant Gaussian channel, Capacity of Colored Noise channels. Multicarrier Signalling: Single carrier vs Multicarrier, Multicarrier Concepts, Types of Multicarrier in AWGN channel, OFDM, DMT, FBMC and OTFS Implementation, Spectral Characteristics, ISI and ICI in Multicarrier, Cyclic Prefix and Suffix, Power and bit allocation algorithms, Capacity of Multicarrier Channel, Peak to Average Power Ratio for Multicarrier, Channel Equalization and Coding Considerations for Multicarrier - COFDM. Modem Initialization, Training, channel estimation and adaptation.</p>		
Unit –III		07 Hrs
<p>MIMO spatial multiplexing and channel modelling: Multiplexing capability of deterministic MIMO channels, Physical modelling of MIMO channels, Modelling of MIMO fading channels. Concept of Massive MIMO with examples. MIMO capacity and multiplexing architectures: Considerations for Receiver architectures – MLD, MMSE, ICD with Spatial Multiplexing and SVD with beamforming. Implementation of beam forming (Analog, Digital and Hybrid examples) and beam Management, Information theoretic optimality.</p>		
Unit –IV		07 Hrs
<p>5G system concept – overview, Extreme mobile broadband, Massive machine-type communication, Ultra-reliable machine-type communication, Dynamic radio access network, Lean system control plane, Localized contents and traffic flows, Spectrum toolbox. The 5G architecture – Introduction, NFV and SDN, Basics about RAN architecture, High-level requirements for the 5G architecture, Functional architecture and 5G flexibility, Functional split criteria, Functional split alternatives, Functional optimization for specific applications, Integration of LTE and new air interface to fulfill 5G requirements, Enhanced Multi-RAT coordination features, Physical architecture and 5G deployment, Deployment enablers, Flexible function placement in 5G deployments</p>		
Unit –V		08 Hrs
<p>Interference management, mobility management, and dynamic reconfiguration - Network deployment types , Ultra-dense network or densification, Moving networks, Heterogeneous networks, Interference management in 5G, Interference management in UDN, Interference management for moving relay nodes, Interference cancelation, Mobility management in 5G, User equipment-controlled versus network-controlled, Mobility management in heterogeneous 5G networks, Context awareness for mobility management, Dynamic network reconfiguration in 5G, Energy savings through control/user plane decoupling, Flexible network deployment based on moving networks</p>		
Course Outcomes (CO):		
After completing the course, the students will be able to:-		
CO 1	Recall the requirements and key functionalities of 4G LTEA/5G NR technology	
CO 2	Compare various channel access technologies and modulation techniques used in 5G wireless systems	
CO 3	Illustrate the architecture of 5G and its NextGen core network.	



CO 4	Apply the 5G concepts to D2D communications. Demonstrate the concept of massive MIMO, V2X & THz
Reference Books	
1.	Saad Z. Asif, “5G Mobile Communications Concepts and Technologies” CRC Press, 2019.
2.	Suvra Sekhar Das and Ramjee Prasad, “Evolution of Air Interface Towards 5G: Radio Access Technology and Performance Analysis”, Gistrup, Denmark: River Publishers series in Communication, 2018.
3.	Wei Xiang, Kan Zheng, Xuemin (Sherman) Shen, “5G Mobile Communications”, Springer publications-2016.
4.	William Stallings “5G Wireless: A Comprehensive Introduction”, Pearson Education, 2021.

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom’s Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII					
MULTIMEDIA COMMUNICATION					
Professional core Elective (Group- H)					
(Theory)					
Course Code	:	21EC74H4	CIE	:	100
Credits: L:T:P	:	3:0:0	SEE	:	100
Total Hours	:	36	SEE Duration	:	03 Hours

Unit-I	07 Hrs
Multimedia Communications Multimedia information representation, multimedia networks-PSTN, Data, Broadcast, ISDN, broad band multiservice, multimedia applications, network QoS and application QoS.	
Unit – II	07 Hrs
Text and image compression, compression principles Lossless and loss, Source encoders and destination decoders, Entropy encoding, Source encoding, Statistical encoding text compression- Run length, static Huffman Coding, Dynamic Huffman coding (Greedy method), Arithmetic coding, Dictionary encoding and decoding –LZ77, LZ78, LZW, Image compression- GIF, TIFF and JPEG.	
Unit –III	07 Hrs
Audio and video compression Introduction, audio compression, DPCM, ADPCM, APC, LPC, video compression principles.	
Unit –IV	07 Hrs
Video compression standards H.261, H.263, H.264, H.265 MPEG, MPEG 1, MPEG 2, MPEG-4, MPEG-7 Multimedia software tools: A Quick Scan, Digital audio, graphics and image editing, Video editing, Animation, Multimedia authoring.	
Unit –V	08 Hrs
Internet IP datagrams, fragmentation, Internet protocol address, ARP and RARP, QoS. Transport Protocol: Introduction, TCP/IP, TCP, UDP, RTP and RTCP	

Course Outcomes (CO):	
After completing the course, the students will be able to:-	
CO 1	Deploy the right multimedia communication models.
CO 2	Apply QoS to multimedia network applications
CO 3	Analyze different audio, image and video compression standards and their advance features.
CO 4	Develop algorithms for protocols like RTP, RTCP for multimedia communication over mobile networks

Reference Books	
1	Multimedia Communications, Fred Halsall, 2 nd Edition, Pearson education, 2001. ISBN: 8131709949, 978-8131709948
2	Multimedia Communication Systems: Techniques, Standards, and Networks, K. R. Rao, Zoran S. Bojkovic, Dragorad A. Milovanovic, , Pearson education, 2002.ISBN: 978-0130313980
3	Multimedia: Computing, Communications and Applications, Raif steinmetz, Klara Nahrstedt, Pearson education, 2002,ISBN: 3540408673, 978-3540408673
4	Multimedia: An Introduction, John Villamil, Louis Molina, PHI, 2002, ISBN: 1575765578, 978-1575765570



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII					
SYSTEM ON CHIP DESIGN					
Professional core Elective (Group- H)					
(Theory)					
Course Code	:	21EC74H5		CIE	: 100
Credits: L:T:P	:	3:0:0		SEE	: 100
Total Hours	:	36L		SEE Duration	: 03

Unit-I	7Hrs
<p>Motivation for SoC Design Introduction to SoC, SoB, SiP, Benefits of system-on-chip integration in terms of cost, power, and performance. Comparison on System-on-Board, System-on-Chip, and System-in-Package. Typical goals in SoC design – cost reduction, power reduction, design effort reduction, performance maximization. Productivity gap issues and the ways to improve the gap. System on Chip Design Process Canonical SoC Design, SoC Design flow - waterfall vs spiral, Top-down vs Bottom-up, Specification requirement, Types of Specification, System Design process, System level design issues- Soft IP vs Hard IP, Design for timing closure.</p>	
Unit – II	7Hrs
<p>SoC Components Simple Microprocessor: Bus Connection and Internals, A canonical D8/A16 Micro-Computer, A Basic Micro-Controller, Switch/LED Interfacing, UART Device, Programmed I/O, I/O Blocks, Common Interface Nets, RAM - on chip memory (Static RAM), Interrupt Wiring: General Structure, GPIO - General Purpose Input/Output Pins, A Keyboard Controller, Counter/Timer Block, Video Controller: Framestore, Arbiter. Basic bus: Multiple Initiators, DMA Controller, Network and Streaming Media Devices, Bus Bridge, Inter-core Interrupter, Remote Debug (JTAG) Access Port, Clock Frequency Multiplier PLL and Clock Tree.</p>	
Unit –III	7Hrs
<p>Macro Design Process, Developing Hard Macros and SoC Verification Overview of IP Design, Key Features, Planning and Specification, Macro design and Verification. Developing Hard Macros, Design Issues for Hard Macros, The Hard Macro Design Process, Productization of Hard Macros. Verification technology options, Verification methodology, Verification languages, Verification IP Reuse, approaches. Verification and Device Test, Verification Plans.</p>	
Unit –IV	7Hrs
<p>Interconnect architectures for SoC Bus architecture and its limitations, Characteristics of Bus-Based Communication Architectures- Bus Signal Types, Physical Structure, Clocking, Decoding and Arbitration. Network on Chip (NOC) Topology, Switching Strategies- Circuit Switching, Packet Switching, Routing Algorithms, Flow Control, Clocking Schemes, QOS, NoC Architectures</p>	
Unit –V	8Hrs
<p>3D IC technology and Multiprocessor SoCs Introduction to 3D IC technology, Architecture of 3D ICs, Benefits and Applications of 3D IC technology, Introduction to MPSoCs, Techniques for designing MPSoCs, Multichip Packages and chipset based design, Performance and flexibility for MPSoCs design Case Study: A Low Power Open Multimedia Application Platform for LTE. High density FPGAs - EDA tools used for SOC design.</p>	

Course Outcomes: After completing the course, the students will be able to	
CO1:	Explore the blocks of System on Chip and its performance.
CO2:	Analyse the design flow and verification of IPs used in system on Chip
CO3:	Acquire the concepts of different memory and interconnection methods in SoC
CO4:	Develop various IPs and Macros for SoC and exposure to the concept of MPSoCs.



Reference Books	
1	Reuse Methodology manual for System-On-A-Chip Designs, Michael Keating, Pierre Bricaud, Kluwer Academic Publishers, 2nd edition,2001
2	System on Chip Design and Modelling University of Cambridge Computer Laboratory Lecture Notes, Dr. David J Greaves (C) 2011 All Rights Reserved DJG. Part II Computer Science Tripos Easter Term,2011
3	SoC Verification-Methodology and Techniques, Prakash Rashinkar, Peter Paterson and Leena Singh, Kluwer Academic Publishers,2001
4	On-Chip Communication Architectures: System on Chip Interconnect, Sudeep Pasricha and NikilDutt, Morgan Kaufmann Publishers,2008

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
COMPUTER ARCHITECTURE			
Professional core Elective (Group- H)			
(Theory)			
Course Code	:	21EC74H6	CIE Marks : 100
Credits: L:T:P	:	3:0:0	SEE Marks : 100
Total Hours	:	36	SEE Duration : 03 Hours
Unit-I			07 Hrs
Introduction, Pipelining & Hazards			
Introduction to CISC versus RISC, Concept of Load-Store architecture, Architecture versus Microarchitecture, Machine Models, ISA characteristics with RISC-V ISA overview, Pipeline basics, Structural Hazards, Data Hazards, Control Hazards - Jumps, Branches & Others, Reducing Branch Costs with Branch Prediction, Overcoming Data Hazards with Dynamic Scheduling			
Unit – II			07 Hrs
Instruction Level Parallelism - Superscalar, Out-of-Order			
Introduction, Basic Two-way In-order Superscalar, Fetch Logic and Alignment, Baseline Superscalar and Alignment, Introduction to Out-of-Order Processors, Overview of I2O2, I2O1, IO3, IO2I with emphasis on IO2I, RISC-V open source CPU case study, Basic Compiler Techniques for Exposing ILP, Examples and the Algorithm			
Unit –III			07 Hrs
Register Renaming & Thread Level Parallelism			
Register Renaming Introduction, Register Renaming with pointers to IQ & ROB, Register Renaming with values in IQ and ROB, Introduction to hardware multithreading, Multithreading motivation, fine grain multithreading, coarse grain multithreading, simultaneous multithreading			
Unit –IV			07 Hrs
Data Level Parallelism using Vector/SIMD			
Vector Processor Introduction, Vector Parallelism, Vector Hardware Optimisations, Vector Software & Compiler Optimisations, RISC-V Vector Extension detailed Overview			
Unit –V			08 Hrs
Memory Management and Caches			
Memory Management Introduction, Base & Bound Registers, Page Based Memory Systems, Address Translation & Protection, Page Table, TLB, Page Walk Cache overview, Cache Performance, Basic Cache Optimizations, Cache Pipelining, Write Buffers, Multilevel Caches, Victim Caches, Prefetching			

Course Outcomes (CO):	
After completing the course, the students will be able to:-	
CO 1	Describe fundamental principles of computer architecture, the significance of load-store architecture, and the impact of ISA characteristics on processor design.
CO 2	Identify various pipeline hazards and apply appropriate mitigation techniques to enhance pipeline efficiency.
CO 3	Analyze program structures and utilize compiler techniques to maximize parallelism and optimize performance.
CO 4	Evaluate cache performance metrics and implement cache optimization techniques to improve memory hierarchy efficiency and overall system performance.

Reference Books	
1.	Computer Architecture: A Quantitative Approach, J.L. Hennessy, and D.A. Patterson, 5 th Edition, Morgan Kaufman Publication, 2012
2.	Computer Organisation and Design RISC-V Edition: The Hardware Software Interface, J.L. Hennessy, D.A. Patterson, 1 st Edition, The Morgan Kaufmann Series, ISBN:13- 978-0128122754
3.	Advanced Computer Architecture, Kai Hwang, Naresh Jotwani, 3 rd Edition, Mc Graw Hill Education.
4.	Introduction to Parallel Computing, Ananth Grama, Anshul Gupta, George Karypis, Vipin Kumar, 2nd Edition, 2013, Pearson Education, ISBN 13: 9788131708071.



5. Computer Organization and Architecture, William Stallings, 6th Edition, Pearson Education.

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
UNMANNED AERIAL VEHICLES			
Institutional Electives-II (Group I)			
(Theory)			
Course Code	:	21AS75IA	CIE : 100 Marks
Credits: L:T:P	:	3:0:0	SEE : 100 Marks
Total Hours	:	45L	SEE Duration : 3.00 Hours

Unit-I	08Hrs
Introduction to Unmanned Aerial Vehicles (UAVs): History of UAVs, Need of unmanned aerial systems, Overview of UAV Systems-System Composition, Classes and Missions of UAVs-Classification of UAVs based on size, range and endurance, Applications, Examples of UAVs	
Unit – II	11Hrs
Aerodynamics & Propulsion aspects of UAVs: Basic Aerodynamic Equations, Air foils, lift, drag, moments, Aircraft Polar, The Real Wing and Airplane, Induced Drag, Total Air-Vehicle Drag, Flapping Wings, Rotary wings. Propulsion: Thrust Generation and basic thrust equation, Sources of Power for UAVs- Piston, Rotary, Gas turbine engines, electric or battery powered UAVs.	
Unit –III	08Hrs
Airframe of UAVs: Mechanic loading, basics of types of load calculation and structural engineering, Material used for UAV (general introduction), FRP and methods of usage in UAV, Testing of FRP specimens for UAV, selection criteria for structure, Types of structural elements used in UAV their significance and characteristics, Methods of manufacturing UAV structure.	
Unit –IV	10Hrs
Payloads for UAVs: Barometers, Accelerometer, Magnetometer, RADAR and range finder, Non-dispensable and dispensable Payloads-Optical, electrical, weapon, imaging payloads.	
Unit –V	08Hrs
Mission Planning and Control: Air Vehicle and Payload Control, Reconnaissance/Surveillance Payloads, Weapon Payloads, Other Payloads, Data-Link Functions and Attributes, Data-Link Margin, Data-Rate Reduction, Launch Systems, Recovery Systems, Launch and Recovery Trade-offs.	

Course Outcomes: At the end of this course the student will be able to :	
CO1:	Understand the role of UAVs in the current generation for diverse applications ranging from commercial to military purposes
CO2:	Apply the fundamental concepts of Aerospace Engineering to Design a UAV for a particular Mission and application
CO3:	Evaluate the performance of UAV with a perspective of Aerodynamics, Propulsion, Structures for a given Mission
CO4:	Critically appraise and optimize the performance of the UAV for a given Mission profile

Reference Books	
1	Unmanned Aircraft Systems UAV design, development and deployment, Reg Austin, 1 st Edition, 2010, Wiley, ISBN 9780470058190.
2	Flight Stability and Automatic Control, Robert C. Nelson, 2 nd Edition, October 1, 1997, McGraw-Hill, Inc, ISBN 978-0070462731.
3	Advances in Unmanned Aerial Vehicles: State of the Art and the Road to Autonomy, Kimon P. Valavanis, 1 st Edition,2007, Springer ISBN 9781402061141
4	Introduction to UAV Systems, Paul G Fahlstrom, Thomas J Gleason, 4 th Edition, 2012, Wiley, ISBN: 978-1-119-97866-4
5	Design of Unmanned Air Vehicle Systems, Dr. Armand J. Chaput, 3 rd Edition, 2001, Lockheed Martin Aeronautics Company, ISBN: 978-1-60086-843-6



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks adding up to 20 Marks. THE SUM OF TWO QUIZZES WILL BE CONSIDERED AS FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test consisting of descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO TESTS will be conducted. Each test will be evaluated for 50 Marks, adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Phase I (20) & Phase II (20) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100
RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII						
Healthcare Analytics						
Institutional Electives-II (Group I)						
(Theory)						
Course Code	:	21BT75IB		CIE	:	100 Marks
Credits: L:T:P	:	3:0:0		SEE	:	100 Marks
Total Hours	:	42 Hrs		SEE Duration	:	3 Hours
Unit-I					09 Hrs	
Introduction to tools and databases: Introduction to Bioinformatics, Goals, Scope, Applications, Sequence databases, Structure databases, Special databases, Applications of these databases, Database similarity search: Unique requirements of database searching, Heuristic Database Searching, Basic Local Alignment Search Tool (BLAST), FASTA, Comparison of FASTA and BLAST, Database Searching with Smith-Waterman Method						
Unit – II					09 Hrs	
Sequence Analysis: Types of Sequence alignment -Pairwise and Multiple sequence alignment, Alignment algorithms, Scoring matrices, Statistical significance of sequence alignment. Multiple Sequence Alignment: Scoring function, Exhaustive algorithms, Heuristic algorithms, Profiles and Hidden Markov Models: Position-Specific scoring matrices, Profiles, Markov Model and Hidden Markov Model, Scoring matrices – BLOSSUM and PAM						
Molecular Phylogenetics: Introduction, Terminology, Forms of Tree Representation. Phylogenetic Tree Construction Methods - Distance-Based, Character-Based Methods and Phylogenetic Tree evaluation.						
Unit –III					09 Hrs	
Introduction to Next-Generation Sequencing (NGS) analysis: Sanger sequencing principles - history and landmarks, of Sequencing Technology Platforms, A survey of next-generation sequencing technologies, A review of DNA enrichment technologies, Base calling algorithms, Base quality, phred values, Reads quality checks, Interpretations from quality checks. Adapter and primer contamination. Processing reads using clipping of reads-Advantages and disadvantages of processing of reads						
Unit –IV					09 Hrs	
Structural analysis & Systems Biology: Gene prediction programs – ab initio and homology-based approaches.. Detection of functional sites and codon bias in the DNA. Predicting RNA secondary structure, Protein structure basics, structure visualization, comparison and classification. Protein structure predictive methods using protein sequence, Protein identity based on composition, Prediction of secondary structure. Scope, Applications. Concepts, implementation of systems biology, Mass spectrometry and Systems biology.						
Unit –V					09 Hrs	
Drug Screening: Introduction to Computer-aided drug discovery, target selection, ligand preparation and enumeration, molecular docking, post-docking processing, molecular dynamics simulations, applications and test cases.						

Course Outcomes: After completing the course, the students will be able to:-	
CO1	Comprehend Bioinformatics Tools: Understand and effectively utilize various bioinformatics tools and databases for sequence and structure analysis.
CO2	Investigate and apply innovative sequencing technologies and analytical methods to solve complex biological questions and advance research in genomics and molecular biology.
CO3	Analyze Next-Generation Sequencing: Proficiency in NGS technologies, including data quality assessment and read processing techniques and handle big data.
CO4	Apply bioinformatics tools to model and simulate various biological processes, leveraging gene prediction programs including both ab initio and homology-based approaches.



Reference Books	
1.	Xiong J. Essential bioinformatics. Cambridge University Press; 2006 Mar 13.
2.	Buehler LK, Rashidi HH, editors. Bioinformatics basics: applications in biological science and medicine. CRC Press; 2005 Jun 23.
3.	Ghosh Z, Mallick BM. Bioinformatics principles and Applications. Oxford University Press; 2018 Jun 13.
4.	Low L, Tammi MT. Introduction to next generation sequencing technologies. Bioinformatics. WORLD SCIENTIFIC. 2017 Jul 26:1-21.
5.	Bioinformatics: Sequence and Genome Analysis; D W Mount; 2014; CSHL Press; 2nd edn; ISBN: 9780879697129.
6.	Computational Systems Biology; A Kriete and R Eils; 2006; Academic Press; Illustrated edn; ISBN: 978-01-208-87866.

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B		
(Maximum of TWO Sub-divisions only; wherein one sub division will be a caselet in the related topics)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester VII						
Sustainability and Life Cycle Analysis						
Institutional Electives-II (Group I)						
(Theory)						
Course Code	:	21CH75IC		CIE	:	100 Marks
Credits: L:T:P	:	3:0:0		SEE	:	100 Marks
Total Hours	:	45L		SEE Duration	:	3Hours
Unit-I					09Hrs	
Introduction to sustainability:						
Introduction to Sustainability Concepts and Life Cycle Analysis, Material flow and waste management, Chemicals and Health Effects, Character of Environmental Problems						
Unit – II					09 Hrs	
Environmental Data Collection and LCA Methodology:						
Environmental Data Collection Issues, Statistical Analysis of Environmental Data, Common Analytical Instruments, Overview of LCA Methodology. – Goal, Definition.						
Unit –III					09 Hrs	
Life Cycle Assessment:						
Life Cycle Impact Assessment, Life Cycle Interpretation, LCA Benefits and Drawbacks.						
Wet Biomass Gasifiers:						
Introduction, Classification of feedstock for biogas generation, Biomass conversion technologies: Photosynthesis, Biogas generation, Factors affecting bio-digestion, Classification of biogas plants, Floating drum plant and fixed dome plant their advantages and disadvantages.						
Unit –IV					09 Hrs	
Design for Sustainability:						
Green Sustainable Materials, Environmental Design for Sustainability.						
Dry Biomass Gasifiers:						
Biomass energy conversion routes, Thermal gasification of biomass, Classification of gasifiers, Fixed bed systems:						
Unit –V					09Hrs	
Case Studies:						
Odor Removal for Organics Treatment Plant, Bio-methanation, Bioethanol production. Bio fuel from water hyacinth.						

Course Outcomes: After completing the course, the students will be able to:-	
CO1	Understand the sustainability challenges facing the current generation, and systems-based approaches required to create sustainable solutions for society.
CO2	Identify problems in sustainability and formulate appropriate solutions based on scientific research, applied science, social and economic issues.
CO3	Apply scientific method to a systems-based, trans-disciplinary approach to sustainability
CO4	Formulate appropriate solutions based on scientific research, applied science, social and economic issues.

Reference Books	
1.	Sustainable Engineering Principles and Practice, Bavik R Bhakshi, 2019, Cambridge University Press, ISBN - 9781108333726.
2.	Environmental Life Cycle Assessment , Olivier Jolliet, Myriam Saade-Sbeih, Shanna Shaked, Alexandre Jolliet, Pierre Crettaz , 1 st Edition, CRC Press, ISBN: 9781439887660 .
3.	Sustainable Engineering: Drivers, Metrics, Tools, and Applications, Krishna R. Reddy, Claudio Cameselle, Jeffrey A. Adams, 2019, John Wiley & Sons , ISBN-9781119493938



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). THREE tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 150 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII						
ADVANCES IN CORROSION SCIENCE AND MANAGEMENT						
Institutional Electives-II (Group I)						
(Theory)						
Course Code	:	21CM75ID		CIE	:	100 Marks
Credits: L:T:P	:	3:0:0		SEE	:	100 Marks
Total Hours	:	42		SEE Duration	:	03 Hours

Unit-I		08 Hrs
Basics of corrosion: Introduction: Galvanic series, Pilling-Bedworth ratio, Types: Galvanic corrosion, crevice corrosion, pitting corrosion, intergranular corrosion, erosion corrosion, stress corrosion, season cracking, hydrogen embrittlement, bacterial corrosion. Corrosion in different engineering materials: Concrete structures, duplex, stainless steels, ceramics, composites.		
Unit-II		08 Hrs
Corrosion mechanism: Electrochemical theory of corrosion, Crevice corrosion-mechanism of differential aeration corrosion, mixed potential theory for understanding common corrosion of metals and alloys. Thermodynamics of Corrosion: Pourbaix diagram and its importance in metal corrosion and its calculation for Al, Cu, Ni and Fe.		
Unit – III		08 Hrs
Effects of corrosion: The direct and indirect effects of corrosion, economic losses, Indirect losses -Shutdown, contamination, loss of product, loss of efficiency, environmental damage, Importance of corrosion prevention in various industries, corrosion auditing in industries, corrosion map of India. Corrosion issues in specific industries-power generation, chemical processing industries, oil and gas Industries, corrosion effect in electronic industry.		
Unit –IV		09 Hrs
Corrosion Testing and monitoring: Introduction, classification. Purpose of corrosion testing, materials, specimen. Surface preparation, measuring and weighing. Types of testing, lab, pilot plant and field tests. Measurement of corrosion rate, weight loss method, CPR numericals, Electrochemical methods, Tafel extrapolation. Linear polarization method.		
Unit –V		09 Hrs
Corrosion Control: Principles of corrosion prevention, material selection, design considerations, control of environment- decrease in velocity, passivity, removal oxidizer, Inhibitors and passivators, coatings- organic, electroplating of Copper, Nickel and Chromium, physical vapor deposition-sputtering, Electroless plating of Nickel.		

Course Outcomes: After completing the course, the students will be able to	
CO1:	Understand the causes and mechanism of various types of corrosion
CO2:	Apply the knowledge of chemistry in solving issues related to corrosion.
CO3:	Analyse and interpret corrosion with respect to practical situations.
CO4:	Develop practical solutions for problems related to corrosion.
Reference Books	
1	Corrosion Engineering, M.G, Fontana, 3rd Edition, 2005, Tata McGraw Hill, ISBN: 978-0070214637.
2	Principles and Prevention of Corrosion, D. A Jones, 2nd Edition, 1996, Prentice Hall, ISBN: 978-0133599930.
3	Design and corrosion prevention, Pludek, 1978, McMillan, ISBN: 978-1349027897
4	Introduction to metal corrosion, Raj Narain, 1983, Oxford &IBH, ISBN: 8120402995.



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). THREE tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 150 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
PROMPT ENGINEERING			
Institutional Electives-II (Group I)			
(Theory)			
Course Code	:	21CS75IE	CIE : 100 Marks
Credits: L:T:P	:	3:0:0	SEE : 100 Marks
Total Hours	:	40L	SEE Duration : 03 Hours

Unit-I	08Hrs
Introduction to Prompt Engineering	
Raise of Context Learning, Prompts, Prompt Engineering, LLM Settings, Basics of prompting, Elements of a Prompt, Settings for Prompting Language Model, General Tips for Designing Prompts, Designing Prompts for Different Tasks: few examples of common tasks using different prompts- Text Summarization, Information Extraction, Question Answering, Text Classification, Conversation/Role Playing, Code Generation, Reasoning	
Unit – II	08 Hrs
Techniques for Effective Prompts	
Techniques designed to improve performance on complex tasks - Zero-Shot Prompting, Few-shot prompting, Chain-of-thought (CoT) prompting, Zero-Shot CoT, Self-Consistency, Knowledge Generation Prompting, Program-aided Language Model (PAL), ReAct, Directional Stimulus Prompting	
Unit –III	07 Hrs
Best Practices in Prompt Engineering	
Tools & IDEs: Capabilities include: Developing and experimenting with prompts, Evaluating prompts. Versioning and deploying prompts; Advanced prompting techniques: advanced applications with LLMs LLMs and external tools/APIs -- LLMs with External Tools; Data-augmented Generation – Steps, External Data, QA with sources, Summarization using sources	
Unit –IV	08 Hrs
Applications of Prompt Engineering:	
LLM Applications: Function Calling with LLMs - Getting Started with Function Calling, Function Calling with GPT-4, Function Calling with Open-Source LLMs	
Function Calling Use Cases: Conversational Agents, Natural Language Understanding, Math Problem Solving, API Integration, Information Extraction	
Unit –V	08 Hrs
Opportunities and Future Directions	
Model safety, Prompt Injection, Prompt Leaking, Jail Breaking; Reinforcement Learning from Human Feedback (RLHF) -- Popular examples: aClaude (Anthropic), ChatGPT (OpenAI)	
Future directions: Augmented LMs, Emergent ability of LMs, Acting / Planning - Reinforcement Learning, Multimodal Prompting, Graph Prompting	

Course Outcomes: After completing the course, the students will be able to	
CO1	Demonstrate an understanding of prompt engineering principles including how prompt structure and phrasing impact the performance of AI models.
CO2	Design and implement effective prompts- to create and apply prompts for various natural language processing (NLP) tasks, such as text generation, summarization, and translation, using AI models.
CO3	Critically evaluate the effectiveness of prompts - assess the quality and performance of prompts in terms of accuracy, coherence, and relevance, identifying areas for improvement.
CO4	Apply prompt engineering techniques in real-world scenarios - use prompt engineering strategies to address practical problems in domains such as education, healthcare, and business, demonstrating the applicability of AI-driven solutions.



Reference Books	
1	Unlocking the Secrets of Prompt Engineering: Master the art of creative language generation to accelerate your journey from novice to pro , Gilbert Mizrahi, Jan 2024, 1st Edition, Packt Publishing, ISBN-13:978-1835083833
2.	Prompt Engineering for Generative AI, James Phoenix, Mike Taylor, May 2024, O'Reilly Media, Inc.,ISBN: 9781098153434
3.	Prompt Engineering for LLMs, John Berryman, Albert Ziegler, O'Reilly Media, Inc. Dec 2024, ISBN: 9781098156152
4.	The Art of Asking ChatGPT for High-Quality Answers_ A Complete Guide to Prompt Engineering, Ibrahim John , Nzunda Technologies Limited, 2023, ISBN-13: 9781234567890
5	Programing Large Language Models with Azure Open AI: Conversational programming and prompt engineering with LLMs, Francesco Esposito, Microsoft Pr, 1 st Edition, April 2024,ISBN-13: 978-0138280376

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problemsolving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
INTEGRATED HEALTH MONITORING OF STRUCTURES			
Institutional Electives-II (Group I)			
(Theory)			
Course Code	:	21CV75IF	CIE : 100 Marks
Credits: L:T:P	:	3:0:0	SEE : 100 Marks
Total Hours	:	42L	SEE Duration : 3Hours
Unit-I			08 Hrs
Structural Health: Factors affecting Health of Structures, Causes of Distress, Regular Maintenance, Importance of maintenance			
Structural Health Monitoring: Concepts, Various Measures, Analysis of behavior of structures using remote structural health monitoring, Structural Safety in Alteration.			
Unit – II			08 Hrs
Materials: Piezo–electric materials and other smart materials, electro–mechanical impedance (EMI) technique, adaptations of EMI technique, Sensor technologies used in SHM			
Structural Audit: Assessment of Health of Structure, Collapse and Investigation, Investigation Management, SHM Procedures, SHM using Artificial Intelligence			
Unit –III			08 Hrs
Static Field Testing: Types of Static Tests, Simulation and Loading Methods, sensor systems and hardware requirements, Static Response Measurement.			
Unit –IV			08 Hrs
Dynamic Field Testing: Types of Dynamic Field Test, Stress History Data, Dynamic Response Methods, Hardware for Remote Data Acquisition Systems, Remote Structural Health Monitoring.			
Unit –V			08 Hrs
Remote Structural Health Monitoring: Introduction, Hardware for Remote Data Acquisition Systems, Advantages, Case studies on conventional and Remote structural health monitoring			
Case studies: Structural Health Monitoring of Bridges, Buildings, Dams, Applications of SHM in offshore Structures- Methods used for non-destructive evaluation (NDE) and health monitoring of structural components			

Course Outcomes: After completing the course, the students will be able to:-	
CO1	Diagnose the distress in the structure understanding the causes and factors.
CO2	Understand safety aspects, components and materials used in Structural Health Monitoring.
CO3	Assess the health of structure using static field methods and dynamic field tests.
CO4	Analyse behavior of structures using remote structural health monitoring
Reference Books	
1	Structural Health Monitoring, Daniel Balageas, Claus Peter Fritzen, Alfredo Güemes,2006, John Wiley and Sons, ISBN: 978-1905209019
2	Health Monitoring of Structural Materials and Components Methods with Applications, Douglas E Adams, 2007,John Wiley and Sons, ISBN:9780470033135
3	Structural Health Monitoring and Intelligent Infrastructure, J. P. Ou, H. Li and Z. D. Duan, Vol1,2006,Taylor and Francis Group, London, UK. ISBN: 978-0415396523
4	Structural Health Monitoring with Wafer Active Sensors, Victor Giurgutiu, 2007,Academic Press Inc, ISBN: 9780128101612



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). THREE tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 150 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII					
Wearable Electronics Institutional Electives-II (Group I) (Theory)					
Course Code	:	21EC75IG		CIE	: 100 Marks
Credits: L:T:P	:	3:0:0		SEE	: 100 Marks
Total Hours	:	39L		SEE Duration	: 03 Hours

Unit-I		07 Hrs
Introduction: world of wearable (WOW), Role of wearable, The Emerging Concept of Big Data, The Ecosystem Enabling Digital Life, Smart Mobile Communication Devices, Attributes of Wearables, Taxonomy for Wearables, Advancements in Wearables, Textiles and Clothing, Applications of Wearables. [Ref 1: Chapter 1.1]		
Unit – II		08 Hrs
Wearable Bio and Chemical Sensors: Introduction, System Design, Microneedle Technology, Sampling Gases, Types of Sensors, Challenges in Chemical Biochemical Sensing, Sensor Stability, Interface with the Body, Textile Integration, Power Requirements, Applications: Personal Health, Sports Performance, Safety and Security, Case studies. [Ref 1: Chapter 2.1]		
Unit –III		07 Hrs
Wearable Textile: Conductive fibres for electronic textiles: an overview, Types of conductive fibre, Applications of conductive fibres, Bulk conductive polymer yarn, Bulk conductive polymer yarn, Techniques for processing CPYs, Wet-spinning technique, Electrospinning technique, case studies, Hands on project in wearable textile: Solar Backpack, LED Matrix wallet. [Ref 2: Chapter 1,2] & [Ref 3: Chapter 6,9]		
Unit –IV		08 Hrs
Energy Harvesting Systems: Introduction, Energy Harvesting from Temperature Gradient, Thermoelectric Generators, Dc-Dc Converter Topologies, Dc-Dc Converter Design for Ultra-Low Input Voltages, Energy Harvesting from Foot Motion, Ac-Dc Converters, Wireless Energy Transmission, Energy Harvesting from Light, Case studies. [Ref 1: Chapter 4.1]		
Unit –V		08 Hrs
Wearable antennas for communication systems: Introduction, Background of textile antennas, Design rules for embroidered antennas, Integration of embroidered textile surfaces onto polymer substrates, Characterizations of embroidered conductive, textiles at radio frequencies, RF performance of embroidered textile antennas, Applications of embroidered antennas. [Ref 2: Chapter 10]		

Course Outcomes: After completing the course, the students will be able to	
CO1:	Describe the different types and wearable sensors, textile, energy harvesting systems and antenna
CO2:	Analysis measurable quantity and working of wearable electronic devices.
CO3:	Determine & interpret the outcome of the wearable devices and solve the design challenges
CO4:	Analyse and Evaluate the wearable device output parameter in real time scenario or given problem statement.

Reference Books	
1	Wearable Sensors: Fundamentals, Implementation and Applications, Edward Sazonov, Michael R. Neuman Academic Press, 1 st Edition, 2014, ISBN-13: 978-0124186620.
2	Electronic Textiles: Smart Fabrics and Wearable Technology, Tilak Dias, Woodhead Publishing; 1 edition, ISBN-13: 978-0081002018.
3	Make It, Wear It: Wearable Electronics for Makers, Crafters, and Cosplayers, McGraw-Hill Education, 1st Edition, ISBN-13: 978-1260116151.
4	Flexible and Wearable Electronics for Smart Clothing: Aimed to Smart Clothing, Gang Wang, Chengyi Hou, Hongzhi Wang, Wiley, 1st Edition, ISBN-13: 978-3527345342
5	Printed Batteries: Materials, Technologies and Applications, Senentxu Lanceros-Méndez, Carlos Miguel Costa, Wiley, 1 edition, ISBN-13: 978-1119287421



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding up to 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Real time problem solving (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR THE SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type of questions covering entire syllabus	20
PART B (Maximum of THREE Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: Question 3 or 4	16
5 & 6	Unit 3: Question 5 or 6	16
7 & 8	Unit 4: Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
E-MOBILITY			
Institutional Electives-II (Group I)			
(Theory)			
Course Code	:	21EE75IH	CIE
Credits: L:T:P	:	3:0:0	SEE
Total Hours	:	45 L	SEE Duration
			: 100Marks
			: 100 Marks
			: 3 Hours

Unit-I	06 Hrs
<p>E-Mobility: A Brief History of the Electric Powertrain, Energy Sources for Propulsion and Emissions, The Advent of Regulations, Drive Cycles, BEV Fuel Consumption, Range, Carbon Emissions for Conventional and Electric Powertrains, An Overview of Conventional, Battery, Hybrid, and Fuel Cell Electric Systems, A Comparison of Automotive and Other Transportation Technologies. Vehicle Dynamics: Vehicle Load Forces, Vehicle Acceleration, Simple Drive Cycle for Vehicle Comparisons</p>	
Unit – II	09 Hrs
<p>Batteries: Batteries Types and Battery Pack, Lifetime and Sizing Considerations, Battery Charging, Protection, and Management Systems, Battery Models, Determining the Cell/Pack Voltage for a Given Output/Input Power, Cell Energy and Discharge Rate.</p> <p>Battery Charging: Basic Requirements for Charging System, Charger Architectures, Grid Voltages, Frequencies, and Wiring, Charging Standards and Technologies, SAE J1772, Wireless Charging, The Boost Converter for Power Factor Correction.</p>	
Unit –III	09 Hrs
<p>Battery Management System: BMS Definition, Li-Ion Cells, Li-Ion BMSs, Li-Ion Batteries, BMS Options: Functionality, CCCV Chargers, Regulators, Balancers, Protectors, Functionality Comparison, Technology, Topology. Measurement: Voltage, Temperature, Current, Management: Protection, Thermal Management, Balancing, Distributed Charging, Evaluation, External Communication: Dedicated analog and digital wires.</p>	
Unit –IV	09 Hrs
<p>Electric Drive train: Overview of Electric Machines, classification of electric machines used in automobile drivetrains, modelling of electric machines, Power Electronics, controlling electric machines, electric machine and power electronics integration Constraints.</p> <p>Energy Management Strategies: Introduction to energy management strategies used in hybrid and electric vehicles, Classification of different energy management strategies, Comparison of different energy management strategies and implementation issues of energy management strategies.</p>	
Unit –V	09 Hrs
<p>Charger Classification and standards: classification based on charging, levels (region-wise), modes, plug types, standards related to: connectors, communication, supply equipments, EMI/EMC.</p> <p>Sizing the drive system: Matching the electric machine and the internal combustion engine (ICE), Sizing the propulsion motor, sizing the power electronics, selecting the energy storage technology, Communications, supporting subsystems</p> <p>Communications, Supporting Subsystems: In vehicle networks- CAN</p>	

Course Outcomes: After completing the course, the students will be able to: -	
CO 1	Explain the basics of electric and hybrid electric vehicles, their architecture, technologies and modelling.
CO 2	Discuss and implement different energy storage technologies used for electric vehicles and their management system.
CO 3	Analyze various electric drives and its integration techniques with Power electronic circuits suitable for electric vehicles.
CO 4	Design EV Simulator for performance evaluation and system optimization and understand the requirement for suitable EV infrastructure.



Reference Books	
	Electric Powertrain: Energy Systems, Power Electronics and Drives for Hybrid, Electric and Fuel Cell Vehicles, John G. Hayes, G. Abas Goodarzi, 1st Edition, 2018, Wiley, ISBN 9781119063667.
2.	Battery Management system for large Lithium Battery Packs, Davide Andrea, 1st Edition, 2010, ARTECH HOUSE, ISBN-13 978-1-60807-104-3.
3.	Hybrid Vehicles from Components to System, F. BADIN, Ed, 1st Edition, 2013, Editions Technip, Paris, ISBN 978-2-7108-0994-4.
4.	Modern Electric Vehicle Technology C.C. Chan and K.T. Chau, 1st Edition, 2001, Oxford university press, ISBN 0 19 850416 0.

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) Phase 2 will be done in the exhibition mode (Demo/Prototype/any outcome). ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
PROGRAMMABLE LOGIC CONTROLLER'S AND APPLICATIONS			
Institutional Electives-II (Group I)			
(Theory)			
Course Code	:	21EI75IJ	CIE : 100Marks
Credits: L:T:P	:	3:0:0	SEE : 100 Marks
Total Hours	:	45 L	SEE Duration : 3 Hours

Unit-I		06 Hrs
Introduction: Introduction to Industrial Automation, Historical background, Different parts and types of Industrial automation, Block diagram of PLC, PLC Versus Other types of Controls, PLC Product Application Ranges, Fixed and Modular I/O Hardware PLC Operation: Binary Data representation, Input and output status files for modular PLC, Addressing concept.		
UNIT II		
PLC Hardware: The I/O section, Discrete I/O Modules, Analog I/O Modules, Special I/O Modules, I/O specifications Input and Output modules: Brief overview of Discrete and Analog input modules, Discrete and TTL/Relay output modules		
Unit –III		09 Hrs
Basics of PLC Programming: Processor memory organization, Program scan, PLC programming languages, Basic Relay Instruction, Bit or relay instructions, NO, NC, One Shot, Output latching software, negated Output and Internal Bit Type instructions, mode of operations		
Unit –IV		
Special programming Instructions: Timer and Counter Instructions: On delay and Off delay and retentive timer instructions, PLC Counter up and down instructions, combining counters and timers. Program Control &Data manipulation Instructions: Data handling instructions, Sequencer instructions, Programming sequence output instructions.		
UNIT V		09 Hrs
SCADA & DCS Building Block of SCADA System, Hardware structure of Remote Terminal Unit, Block diagram of Distributive Control System Case Studies: Bottle filling system, Material Sorter. Elevator, Traffic control, Motor sequencers, Piston extraction and retraction using timers and counters.		
Course Outcomes: After completing the course, the students will be able to: -		
CO1	Understand the basic concepts of PLC's and SCADA techniques.	
CO2	Apply the programming concepts to interface peripheral.	
CO3	Analyze and evaluate the automation techniques for industrial applications.	
CO4	Develop a system for automation application.	

Reference Books	
1.	Programmable Logic controllers, Frank D. Petruzella, Mc Graw hill, 4 th Edition, ISBN:9780073510880, 2017
2.	Introduction to Programmable Logic Controllers, Garry Dunning, CENGAGE Learning, 3rd Edition, 2017, ISBN: 978-8131503027
3.	Industrial Control and Instrumentation, Bolton W, Universities Press, 6th Edition, 2006. ISBN 978-0128029299
4.	Computer Based Industrial control, Krishna Kant, PHI Publishers, 2nd Edition, 2010. ISBN 978-8120339880.



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) Phase 2 will be done in the exhibition mode (Demo/Prototype/any outcome). ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII						
Space Technology and Applications						
Institutional Electives-II (Group I)						
(Theory)						
Course Code	:	21ET75IK		CIE	:	100 Marks
Credits: L:T:P	:	3:0:0		SEE	:	100 Marks
Total Hours	:	45 L		SEE Duration	:	3 Hours

Unit-I	9 Hrs
Earth's environment: Atmosphere, ionosphere, Magnetosphere, VanAllen Radiation belts, Interplanetary medium, Solar wind, Solar- Earth Weather Relations. Launch Vehicles: Rocketry, Propellants, Propulsion, Combustion, Solid, Liquid and Cryogenic engines, Control and Guidance system, Ion propulsion and Nuclear Propulsion.	
Unit- II	9Hrs
Satellite Technology: Structural, Mechanical, Thermal, Power control, Telemetry, Telecomm and Quality and Reliability, Payloads, Classification of satellites. Satellite structure: Satellite Communications, Transponders, Satellite antennas.	
Unit-III	9Hrs
Satellite Communications: LEO, MEO and GEO orbits, Altitude and orbit controls, Multiple Access Techniques. Space applications: Telephony, V-SAT, DBS system, Satellite Radio and TV, Tele-Education, Telemedicine, Satellite navigation, GPS.	
Unit-IV	9Hrs
Remote Sensing: Visual bands, Agricultural, Crop vegetation, Forestry, water Resources, Land use, Land mapping, geology, Urban development resource Management, and image processing techniques. Metrology: Weather forecast(Long term and Short term),weather modelling,Cyclonepredictions,Disasterandfloodwarning,rainfallpredictionsusing	
Unit-V	9 Hrs
Space Missions: Technology missions, deep space planetary missions, Lunar missions, zero gravity experiments, space biology and International space Missions. Advanced space systems: Remote sensing cameras, planetary payloads, space shuttle, space station, Interspace communication systems.	

Course Outcomes: After completing the course, the students will be able to	
CO1	Explain various Orbital Parameters, Satellite Link Parameters, Propagation considerations and Radar systems.
CO2	Apply the concepts to determine the parameters of satellite, performance of radar and navigation systems.
CO3	Analyze the design issues of satellite and its sub-systems ,radars and navigation systems.
CO4	Evaluatetheperformanceofthesatellitesystemsanditsparameters,radarandnavigation systems



Reference Books	
1.	Atmosphere, weather and climate, RGB arry, Routledge publications,2009, ISBN- 10:0415465702.
2.	Fundamentals of Satellite Communication, KN Raja Rao, PHI, 2012, ISBN: 978-8120324015
3.	SatelliteCommunication,Timothypratt,JohnWiley,1986ISBN: 978-0-471-37007 -9, ISBN10: 047137007X
4	Remote sensing and applications, B C Panda, VIVAbooksPvt.Ltd.,2009, ISBN: 108176496308.

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
MOBILE APPLICATION DEVELOPMENT			
Institutional Electives-II (Group I)			
(Theory)			
Course Code	:	21IS75IL	CIE : 100 Marks
Credits: L:T:P	:	3:0:0	SEE : 100 Marks
Total Hours	:	45L	SEE Duration : 03 Hours

Prerequisite: - Programming in Java.

Unit-I		09 Hrs
Introduction: Smart phone operating systems and smart phones applications. Introduction to Android, Installing Android Studio, creating an Android app project, deploying the app to the emulator and a device. UI Design: Building a layout with UI elements, Layouts, Views and Resources, Text and Scrolling Views. Activities and Intents, The Activity Lifecycle, Managing State, Activities and Implicit Intents, The Android Studio Debugger, Testing the Android app, The Android Support Library.		
Unit-II		09 Hrs
User experience: User interaction, User Input Controls, Menus, Screen Navigation, Recycler View, Delightful user experience, Drawables, Styles, and Themes, Material Design, Testing app UI, Testing the User Interface		
Unit-III		09 Hrs
Working in the background: Async Task and Async Task Loader, Connect to the Internet, Broadcast Receivers and Services. Scheduling and optimizing background tasks – Notifications, Scheduling Alarms, and Transferring Data Efficiently		
Unit-IV		09 Hrs
All about data: Preferences and Settings, Storing Data, Shared Preferences. Storing data using SQLite, SQLite Database. Sharing data with content providers. Advanced Android Programming: Internet, Entertainment and Services. Displaying web pages and maps, communicating with SMS and emails, Sensors.		
Unit-V		09 Hrs
Hardware Support & devices: Permissions and Libraries, Performance and Security. Fire base and AdMob, Publish and Polish, Multiple Form Factors, Using Google Services.		
Course Outcomes: After completing the course, the students will be able to		
CO1:	Comprehend the basic features of android platform and the application development process. Acquire familiarity with basic building blocks of Android application and its architecture.	
CO2:	Apply and explore the basic framework, usage of SDK to build Android applications incorporating Android features in developing mobile applications.	
CO3:	Demonstrate proficiency in coding on a mobile programming platform using advanced Android technologies, handle security issues, rich graphics interfaces, using debugging and troubleshooting tools.	
CO4:	Create innovative applications, understand the economics and features of the app marketplace by offering the applications for download.	

Reference Books	
1	Android Programming, Phillips, Stewart, Hardy and Marsicano, Big Nerd Ranch Guide, 2 nd Edition, 2015, ISBN-13 978-0134171494



2	AndroidStudioDevelopmentEssentials-Android6, NeilSmyth,2015, Create space Independent Publishing Platform, ISBN:9781519722089
3	Android Programming–Pushing the limits, EricHellman,2013, Wiley, ISBN-13:978-1118717370
4	Professional Android2ApplicationDevelopment, RetoMeier, Wiley India Pvt. Ltd, 1 st Edition, 2012, ISBN-13:9788126525898
5	BeginningAndroid3, Mark Murphy, A press Springer India Pvt Ltd, 1 st Edition,2011, ISBN-13:978-1-4302-3297-1
6	AndroidDeveloperTraining- https://developers.google.com/training/android/ AndroidTestingSupportLibrary- https://google.github.io/android-testing-support-library/

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. Each quiz is evaluated for 10 marks adding up to 20 MARKS	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom’s Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks , adding upto 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (10) Designing & Modeling (10) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q.NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII					
PROJECT MANAGEMENT					
Institutional Electives-II (Group I)					
(Theory)					
Course Code	:	21IM75IM		CIE	: 100Marks
Credits: L:T:P	:	3:0:0		SEE	: 100 Marks
Total Hours	:	45 L		SEE Duration	: 3 Hours

Unit-I		06 Hrs
<p>Introduction: Project, Project management, relationships among portfolio management, program management, project management, and organizational project management, relationship between project management, operations management and organizational strategy, business value, role of the project manager, project management body of knowledge.</p> <p>Generation and Screening of Project Ideas: Generation of ideas, monitoring the environment, corporate appraisal, scouting for project ideas, preliminary screening, project rating index, sources of positive net present value.</p>		
Unit – II		09 Hrs
<p>Project Scope Management: Project scope management, collect requirements define scope, create WBS, validate scope, control scope.</p> <p>Organizational influences & Project life cycle: Organizational influences on project management, project state holders & governance, project team, project life cycle.</p>		
Unit –III		09 Hrs
<p>Project Integration Management: Develop project charter, develop project management plan, direct & manage project work, monitor & control project work, perform integrated change control, close project or phase.</p> <p>Project Quality management: Plan quality management, perform quality assurance, control quality.</p>		
Unit –IV		09 Hrs
<p>Project Risk Management: Plan risk management, identify risks, perform qualitative risk analysis, perform quantitative risk analysis, plan risk resources, control risk.</p> <p>Project Scheduling: Project implementation scheduling, Effective time management, Different scheduling techniques, Resources allocation method, PLM concepts. Project life cycle costing.</p>		
Unit –V		09 Hrs
<p>Tools & Techniques of Project Management: Bar (GANTT) chart, bar chart for combined activities, logic diagrams and networks, Project evaluation and review Techniques (PERT) Planning, Computerized project management.</p>		

Course Outcomes: After completing the course, the students will be able to: -	
CO 1	Understand the fundamental concepts of project management and its relationship with organizational strategy, operations management, and business value.
CO 2	Apply techniques for generating, screening, and evaluating project ideas, considering factors such as net present value and project rating index.
CO 3	Create Work Breakdown Structures (WBS), utilization of PERT/CPM for developing project schedule, alongside requirement collection, scope definition, scope validation, and scope control.
CO 4	Develop skills in project integration, quality, risk management, and scheduling, enabling effective project planning, execution, monitoring, and control.

Reference Books	
1.	Project Management Institute, “A Guide to the Project Management Body of Knowledge (PMBOK Guide)”, 5 th Edition, 2013, ISBN: 978-1-935589-67-9
2.	Harold Kerzner, Project Management A System approach to Planning Scheduling & Controlling, John Wiley & Sons Inc., 11 th Edition, 2013, ISBN 978-1-118-02227-6.



3.	Prasanna Chandra, Project Planning Analysis Selection Financing Implementation & Review, Tata McGraw Hill Publication, 7 th Edition, 2010, ISBN 0-07-007793-2.
4.	Rory Burke, “Project Management – Planning and Controlling Techniques”, John Wiley & Sons, 4 th Edition, 2004, ISBN: 9812-53-121-1

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom’s Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). TWO tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 100 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) Phase 2 will be done in the exhibition mode (Demo/Prototype/any outcome). ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII			
SUPPLY CHAIN ANALYTICS			
Institutional Electives-II (Group I)			
(Theory)			
Course Code	:	211M75IN	CIE : 100 Marks
Credits: L:T:P	:	3:0:0	SEE : 100 Marks
Total Hours	:	42L	SEE Duration : 03 Hours
Unit-I			06 Hrs
Introduction: Supply Chain, Supply Chain Management, Business Analytics, Supply Chain Analytics. Data-Driven Supply Chains: Data and its value in SCM, Data Source in Supply Chains, Big Data, Introduction to Python (Concepts only).			
Unit – II			08 Hrs
Data Manipulation: Data Manipulation, Data Loading and Writing, Data Indexing and Selection, Data Merging and Combination, Data Cleaning and Preparation, Data Computation and Aggregation, Working with Text and Datetime Data (Concepts only).			
Unit –III			08 Hrs
Customer Management: Customers in Supply Chains, Understanding Customers, Building a Customer-Centric SC, Cohort Analysis, RFM Analysis, Clustering Algorithms (Concepts only). Supply Management: Procurement in Supply Chains, Supplier Selection, Supplier Evaluation, Supplier Relationship Management, Supply Risk Management, Regression Algorithms (Concepts only).			
Unit –IV			08 Hrs
Warehouse and Inventory Management: Warehouse Management, Inventory Management, Warehouse Optimization, Classification Algorithms (Concepts only). Demand Management: Demand Management, Demand Forecasting, Time Series Forecasting, Machine Learning Methods (Concepts only).			
Unit –V			06 Hrs
Logistics Management: Logistics Management, Modes of Transport in Logistics, Logistics Service Providers, Global Logistics Management, Logistics Network Design, Route Optimization (Concepts only).			
Experiential Learning: Data Visualization: Data Visualization in Python, Creating a Figure in Python, Formatting a Figure, Plotting Simple Charts, Plotting with Seaborn, Geographic Mapping with Basemap, Visualizing Starbucks Locations. Python programming for various algorithms applied to supply chain processes and modelling included in the five units of the syllabus.			

Course Outcomes: After completing the course, the students will be able to know	
CO1:	Understand supply chain concepts, systemic and strategic role of SCM in global competitive environment.
CO2:	Evaluate alternative supply and distribution network structures using optimization models.
CO3:	Develop optimal sourcing and inventory policies in the supply chain context.
CO4:	Select appropriate information technology frameworks for managing supply chain processes.

Reference Books	
1.	Kurt Y. Liu, Supply Chain Analytics - Concepts, Techniques and Applications, Palgrave – Macmillan, Springer Nature Switzerland AG, 2022, ISBN 978-3-030-92224-5 (eBook)
2.	Işık Biçer, Supply Chain Analytics - An Uncertainty Modeling Approach, 2023, Springer Texts in Business and Economics, Springer Nature Switzerland AG, e-ISSN 2192-4341, e-ISBN 978-3-031-30347-0
3.	Supply Chain Management – Strategy, Planning & Operation, Sunil Chopra, Peter Meindl & D V Kalra, 6 th Edition, 2016, Pearson Education Asia; ISBN: 978-0-13-274395-2.
4.	Supply Chain Management – Creating Linkages for Faster Business Turnaround, Sarika Kulkarni & Ashok Sharma, 1 st Edition, 2004, TATA Mc Graw Hill, ISBN: 0-07-058135–5



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). THREE tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 150 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII						
NUCLEAR ENGINEERING						
Institutional Electives-II (Group I)						
(Theory)						
Course Code	:	21ME7510		CIE	:	100 Marks
Credits: L:T:P	:	3:0:0		SEE	:	100 Marks
Total Hours	:	45		SEE Duration	:	3 Hours
Prerequisites: Basic knowledge of Physics and Mathematics at the college level						
Unit-I					09 hrs	
Introduction to Nuclear Engineering Historical Development of Nuclear Engineering, Overview of Nuclear Energy Applications, Nuclear Physics Fundamentals: Atomic Structure and Nuclear Models: Nuclear Forces and Interactions, Nuclear Reactions and Cross-sections, Types of Nuclear Reactions: Fission and Fusion Reactions, Neutron-Induced Reactions, Applications in Power Generation and Industry, Nuclear Power Generation: Basic Principles of Nuclear Reactors, Types of Nuclear Reactors, Radiation Basics, Types of Radiation (Alpha, Beta, Gamma), Radioactive Decay and Decay Chains, Units of Radioactivity and Radiation Measurement						
Unit-2					10 hrs	
Nuclear Reactors Types of Nuclear Reactors, Reactor Components and Their Functions, Nuclear Reactor Kinetics and Control, Neutron Interactions and Transport, Neutron Moderation and Absorption, Reactor Kinetics and Dynamics, Specific Types of Nuclear Reactor, Light Water Reactors: Pressurized Water Reactor (PWR) and Boiling Water Reactor (BWR), Heavy Water Reactors: Canada Deuterium Uranium (CANDU), Gas-Cooled Reactors: Gas-Cooled Reactor and Fast Breeder Reactor (and HTGR), Liquid Metal-Cooled Reactors (LMFR).						
Unit - 3					10 hrs	
Nuclear Fuel Cycle Introduction to the Nuclear Fuel Cycle: Importance of Fuel Cycle Management, Uranium Mining and Ore Processing, Types of Uranium Deposits, Mining Methods and Processing Techniques, Environmental and Health Considerations, Uranium Enrichment and Fuel Fabrication: Enrichment Technologies (Centrifugation, Gaseous Diffusion), Fuel Fabrication Processes, Quality Control and Safety Measures, Nuclear Reactors and Fuel Utilization: Fuel Assembly Design and Composition.						
Unit-4					08 hrs	
Radiation Protection and Safety: Basics of Ionizing Radiation, Types of Ionizing Radiation, Interaction of Radiation with Matter, Units of Radiation Measurement, Biological Effects of Radiation, Deterministic and Stochastic Effects, Acute and Chronic Radiation Effects, Risk Assessment and Dose, Response Relationships, Radiation Dose Assessment: External and Internal Dosimetry, Radiation Monitoring Devices, Occupational and Public Dose Limits, Radiation Safety Measures:, Emergency Response and Contingency Planning: Emergency Procedures and Drills, Communication Strategies During Radiation Incidents						
Unit-5					08 hrs	
Environmental and Societal Aspects Environmental Impact Assessment: Life Cycle Analysis of Nuclear Energy, Impact of Uranium Mining and Fuel Cycle Operations, Radioactive Waste Management and Environmental Considerations, Societal Perceptions and Attitudes, Factors Influencing Public Perception, Ethical Considerations: Principles of Ethics in Nuclear Engineering, Nuclear Energy and Social Justice, Ethical Dilemmas in Nuclear Technology, Nuclear Energy and Climate Change: Carbon Footprint of Nuclear Power.						

Course Outcomes:	
CO1	Understand nuclear physics: grasp atomic structure, nuclear models, and the forces driving nuclear interactions
CO2	Evaluate various reactor types and advanced concepts, applying kinetics and controls to ensure safe and efficient nuclear reactor analysis and design.



CO3	Examine the nuclear fuel cycle from mining to recycling, assess environmental impact and safety, and promote responsible, sustainable practices throughout.
CO4	Apply ionizing radiation principles for safety measures; integrate communication and regulatory compliance into emergency response plans effectively.

Reference Books	
1	Bodansky, D. (2007). "Nuclear Energy: Principles, Practices, and Prospects." Springer. ISBN-13: 978-0387261994.
2	Lamarsh, J. R., & Baratta, A. J. (2001). "Introduction to Nuclear Engineering." Prentice Hall. ISBN-13: 978-0201824988.
3	Duderstadt, J. J., & Hamilton, L. J. (1976). "Nuclear Reactor Analysis." John Wiley & Sons. ISBN-13: 978-0471223634.
4	Knoll, G. F. (2008). "Radiation Detection and Measurement." John Wiley & Sons. ISBN-13: 978-0470131480

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1: (Compulsory)	16
3 & 4	Unit 2: (Internal Choice)	16
5 & 6	Unit 3: (Internal Choice)	16
7 & 8	Unit 4: (Internal Choice)	16
9 & 10	Unit 5: (Internal Choice)	16
TOTAL		100



Semester: VII

**Cognitive Psychology
Institutional Electives-II (Group I)
(Theory)**

Course Code	:	21HS75IQ	CIE	:	100
Credits: L:T:P	:	03	SEE	:	100
Total Hours	:	42 Hrs	SEE Duration	:	3 Hours

Unit-I

09 Hrs

Fundamentals & current trends in cognitive psychology: Definition, Emergence of cognitive psychology, Cognitive development theories and perspectives; Current status and trends in cognitive Psychology. Research methods in cognitive psychology- goals of research. Distinctive research method. Current areas of research in cognitive psychology, (Educational application, marketing and advertisement).

Unit – II

08 Hrs

Basic cognitive processes: Sensation and Perception: Sensory receptors and Brain, The constancies, pattern recognition, Modularity, Imagery: Characteristics of Imagery, Cognitive maps. Attention and Information processing: Nature and Types, Theories and models of attention. Neuropsychological studies of Attention. Consciousness: – meaning, Modern Theories and Contemporary Research of Consciousness.

Unit –III

08 Hrs

Reasoning, Creativity and Problem- Solving: Reasoning definition, types, influencing factors. Creativity- definition, steps involved in creative process, obstacles involved in creativity, enhancing techniques of creativity. Meta cognition: Problem solving, steps in problem solving, types, methods, obstacles and aids of problem Solving.

Unit –IV

08 Hrs

Psycholinguistics: Definition, characteristics of language, theories - Chomsky. Structure of Language (Properties), Stages in Language Development, Neurological Language. Comprehension and Production. Bilingualism, Multilingualism and Learning disability.

Unit –V

09 Hrs

Cognitive Neuroscience: Definition and emergence of cognitive neuroscience, Scope of Neuroscience, structure and functions of Brain, Brain Plasticity, Intelligence and Neuroscience. Meta-cognitive strategies. Artificial intelligence, Robotics, Models on Information Processing.

Course Outcomes: After completing the course, the students will be able to: -

CO1	Describe the basic theories, principles, and concepts of cognitive psychology as they relate to behaviors and mental processes.
CO2	Define learning and compare and contrast the factors that cognitive, behavioural, and Humanistic theorists believe influence the learning process.
CO3	Develop understanding of psychological attributes such as reasoning, problem-solving creativity, resulting in their enhancement and apply effective strategies for self-management and self-improvement.
CO4	Apply the theories to their own and others' lives to better understand their personalities and experiences.

Reference Books

1	Sterberg R.J and Sternberg Karin(2012) Cognitive Psychology 6 th Edition Woods worth Cengage Learning
2	Psychology-themes and variations , Wayne Weiten, IV edition, Brooks / Cole Publishing Co.
3	Psychology Robert A. Baron, III edition (1995) Prentice Hall India.
4	Understanding Psychology Feldman R. S, IV edition, (1996) McGraw Hill India



RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom's Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). THREE tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 150 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII					
PRINCIPLES AND PRACTICES OF CYBER LAW					
Institutional Electives-II (Group I)					
(Theory)					
Course Code	:	21HS75IS	CIE	:	100
Credits: L:T:P	:	3:0:0	SEE	:	100
Total Hours	:	39	SEE Duration	:	3 Hours

Unit-I	08Hrs
<p>Introduction - Origin and meaning of Cyberspace; Introduction to Indian Cyber Law, Distinction between Cyber Crime and Conventional Crime, Cyber Criminals and their Objectives, Kinds of Cyber Crime & Cyber Threats, challenges of cybercrimes, Overview of General Laws and Procedures in India.</p> <p>Cyber Jurisdiction - Concept of Jurisdiction, Jurisdiction in Cyberspace, Issues and concerns of Cyberspace Jurisdiction in India, International position of Cyberspace Jurisdiction, Judicial interpretation of Cyberspace Jurisdiction. Activities: Case Studies and Practical Applications</p>	
Unit- II	08Hrs
<p>Information Technology Act: A brief overview of Information Technology Act 2000, IT Act2000vs.ITAmendmentAct2008, Relevant provisions from Indian Penal Code, Indian Evidence Act, Bankers Book Evidence Act, Reserve Bank of India Act, etc.</p> <p>Electronic Signature and Digital Signature - Meaning & Concept of Relevance of Signature, Handwritten signature vs Digital Signature, Technological Advancement and development of signature, Digital Signature: IT Act, 2000, Cryptography, Public Key and Private Key, Public Key Infrastructure Electronic Signature vs. Digital Signature, E- Commerce under IT Act 2000, Issues and challenges of E-Commerce. Activities: Case Studies and Practical Applications</p>	
Unit-III	08Hrs
<p>Data Protection and Privacy Concerns in Cyberspace - Need to protect data in cyberspace, Types of data, Legal framework of data protection, Data protection bill -an overview, GDPR, Concept of privacy, Privacy concerns of cyberspace, Constitutional framework of privacy, Judicial interpretation of privacy in India.</p> <p>Data Privacy and Data Security- Defining data, meta-data, big data, non- personal data. Data protection, Data privacy and data security, Data protection regulations of other countries- General Data Protection Regulations (GDPR),2016 Personal Information Protection and Electronic Documents Act (PIPEDA)., Social media- data privacy and security issues. Activities: Case Studies and Practical Applications</p>	
Unit-IV	08 Hrs
<p>IP Protection Issues in Cyberspace</p> <p>Copyright Issues in Cyberspace- Copyright infringement in digital environment. Indian legal protection of copyright in cyberspace.</p> <p>Trademark Issues in Cyberspace - Domain Name Vs Trademark, Domain Name dispute and Related Laws, Different Form of Domain in Cyberspace.</p> <p>PatentIssuesinCyberspace-LegalpositiononComputerrelatedPatents-IndianPosition on Patents.</p> <p>Activities: Case Studies and Practical Applications</p>	
Unit-V	07 Hrs
<p>Digital Forensics- Computer Forensics, Mobile Forensics, Forensic Tools, Anti-Forensics</p> <p>Cyber Crime &Criminal Justice Agencies -Cyber Crime Cells, Cyber Crime Appellate- Cyber Crime Investigation, Investigation Procedure - FIR - Charge Sheet</p>	

Course Outcomes: After completing the course, the students will be able to:-	
CO1	Understand the importance of professional practice, Lawand Ethics in their personal lives and professional careers.
CO2	Build in Depth Knowledge of Information Technology Act and Legal Frame Work of Right to Privacy, Data Security and Data Protection.



CO3	Identify the bone of contentions of cyber crime investigation techniques, evaluate problem- solving strategies, and develop science-based solutions.
CO4	Develop an Understanding of the Relationship Between E-Commerce and Cyberspace.

Reference Books	
1.	Cyber Law by Dr. Pavan Duggal Publisher: Lexis Nexis, ISBN-10:8196241070, ISBN-13:978- 8196241070
2.	Introduction to Information Security and Cyber Laws by Surya Prakash Tripathi, Ritendra Goel, Praveen Kumar Shukla ASIN:9351194736, Publisher: Dreamtech Press, ISBN-10:9789351194736, ISBN-13: 978-9351194736.
3.	Cyber Forensics in India: A Legal Perspective by Nishesh Sharma, 1 st Edition, ISBN: 9788131250709.
4.	Cyber Laws, Justice Yatindra Singh, 6 th Edition, Vol.1, ISBN: 9789351437338

RUBRIC FOR THE CONTINUOUS INTERNAL EVALUATION (THEORY)		
#	COMPONENTS	MARKS
1.	QUIZZES: Quizzes will be conducted in online/offline mode. TWO QUIZZES will be conducted & Each Quiz will be evaluated for 10 Marks. THE SUM OF TWO QUIZZES WILL BE THE FINAL QUIZ MARKS.	20
2.	TESTS: Students will be evaluated in test, descriptive questions with different complexity levels (Revised Bloom’s Taxonomy Levels: Remembering, Understanding, Applying, Analyzing, Evaluating, and Creating). THREE tests will be conducted. Each test will be evaluated for 50 Marks, adding upto 150 Marks. FINAL TEST MARKS WILL BE REDUCED TO 40 MARKS.	40
3.	EXPERIENTIAL LEARNING: Students will be evaluated for their creativity and practical implementation of the problem. Case study-based teaching learning (10), Program specific requirements (10), Video based seminar/presentation/demonstration (20) ADDING UPTO 40 MARKS.	40
MAXIMUM MARKS FOR THE CIE THEORY		100

RUBRIC FOR SEMESTER END EXAMINATION (THEORY)		
Q. NO.	CONTENTS	MARKS
PART A		
1	Objective type questions covering entire syllabus	20
PART B (Maximum of TWO Sub-divisions only)		
2	Unit 1 : (Compulsory)	16
3 & 4	Unit 2 : Question 3 or 4	16
5 & 6	Unit 3 : Question 5 or 6	16
7 & 8	Unit 4 : Question 7 or 8	16
9 & 10	Unit 5: Question 9 or 10	16
TOTAL		100



Semester: VII					
SUMMER INTERNSHIP					
Course Code	:	21CS76I		CIE	: 50 Marks
Credits: L:T:P	:	0:0:2		SEE	: 50 Marks
Hours/Week	:	04		SEE Duration	: 2 Hours
GUIDELINES					
<ol style="list-style-type: none"> 1. The duration of the internship shall be for a period of 6/8 weeks on full time basis after VI semester final exams and before the commencement of VII semester. 2. The student must submit letters from the industry clearly specifying his / her name and the duration of the internship on the company letter head with authorized signature. 3. Internship must be related to the field of specialization of the respective UG programme in which the student has enrolled. 4. Students undergoing internship training are advised to report their progress and submit periodic progress reports to their respective guides. 5. Students have to present the internship activities carried out to the departmental committee and only upon approval by the committee, the student can proceed to prepare and submit the hard copy of the final internship report. However, interim or periodic reports as required by the industry / organization can be submitted as per the format acceptable to the respective industry / organizations. 6. The reports shall be printed on A4 size with 1.5 spacing and Times New Roman with font size 12, outer cover of the report (wrapper) has to be Ivory color for UG circuit Programs and Light Blue for Non-Circuit Programs. 7. The broad format of the internship final report shall be as follows <ul style="list-style-type: none"> • Cover Page • Certificate from College • Certificate from Industry / Organization • Acknowledgement • Synopsis • Table of Contents • Chapter 1 - Profile of the Organization: Organizational structure, Products, Services, Business Partners, Financials, Manpower, Societal Concerns, Professional Practices, • Chapter 2 - Activities of the Department • Chapter 3 - Tasks Performed: summary of the tasks performed during 8-week period • Chapter 4 – Reflections: Highlight specific technical and soft skills acquired during internship • References & Annexure 					
<p>Course Outcomes: After going through the internship the student will be able to: CO1: Apply Engineering and Management principles CO2: Analyze real-time problems and suggest alternate solutions CO3: Communicate effectively and work in teams CO4: Imbibe the practice of professional ethics and need for lifelong learning.</p>					
<p>Scheme of Continuous Internal Evaluation (CIE): The evaluation committee shall consist of Guide, Professor/Associate Professor and Assistant Professor. The committee shall assess the presentation and the progress reports in two reviews. The evaluation criteria shall be as per the rubrics given below:</p>					
Reviews	Activity			Weightage	



Review-I	Explanation of the application of engineering knowledge in industries, ability to comprehend the functioning of the organization/ departments.	25 Marks
Review - II	Importance of resource management, environment and sustainability, presentation skills and report writing	25 Marks

Scheme for Semester End Evaluation (SEE):

The SEE examination shall be conducted by an external examiner (domain expert) and an internal examiner. Evaluation shall be done in batches, not exceeding 6 students per batch.

Scheme of Evaluation for SEE	
Particulars	%Marks
Project Synopsis (Initial Writeup)	10%
Project Demo/Presentation	30%
Methodology and Results Discussion	30%
Project Work Report	10%
Viva-voce	20%
Total	100



Semester: VII						
MINOR PROJECT						
Course Code	:	21CS77		CIE	:	50 Marks
Credits: L:T:P	:	0:0:2		SEE	:	50 Marks
Hours/Week	:	04		SEE Duration	:	2 Hours
GUIDELINES						
<p>1. The minor project is to be carried out individually or by a group of students. (maximum of 4 members and minimum of 3 students).</p> <p>2. Each student in a team must contribute equally in the tasks mentioned below.</p> <p>3. Each group has to select a current topic that will use the technical knowledge of their program of study after detailed literature survey.</p> <p>4. The project should result in system/module which can be demonstrated, using the available resources in the college.</p> <p>5. The CIE evaluation will be done by the committee constituted by the department. The committee shall consist of respective guide & two senior faculty members as examiners. The evaluation will be done for each student separately.</p> <p>6. The final copy of the report should be submitted after incorporation of any modifications suggested by the evaluation committee.</p>						
<u>The minor-project tasks would involve:</u>						
<ol style="list-style-type: none"> 1. Carrying out the Literature Survey of the topic chosen. 2. Understand the requirements specification of the minor-project. 3. Detail the design concepts as applicable through appropriate functional block diagrams. 4. Commence implementation of the methodology after approval by the faculty. 5. Conduct thorough testing of all the modules developed and carry out integration testing. 6. Demonstrate the functioning of the minor project along with presentations of the same. 7. Prepare a project report covering all the above phases with proper inference to the results obtained. 8. Conclusion and Future Enhancements must also be included in the report. 						
The students are required to submit the report in the prescribed format provided by the department.						
Course Outcomes:						
After going through the minor project the student will be able to:						
CO1: Interpreting and implementing the project in the chosen domain by applying the concepts learnt.						
CO2: The course will facilitate effective participation by the student in team work and development of communication and presentation skills essential for being part of any of the domains in his / her future career.						
CO3: Applying project life cycle effectively to develop an efficient product.						
CO4: Produce students who would be equipped to pursue higher studies in a specialized area or carry out research work in an industrial environment.						
Scheme of Continuous Internal Evaluation (CIE):						
The evaluation committee shall consist of Guide, Professor/Associate Professor and Assistant Professor. The committee shall assess the presentation and the progress reports in three review phases. The evaluation criteria shall be as per the rubrics given below:						
ReviewPhase	Activity				Weightage	
Phase-I	Synopsis submission, approval of the selected topic, Problem definition, Literature review, formulation of objectives, methodology				10 Marks	



Phase - II	Mid-term evaluation to review the progress of implementation, design, testing and result analysis along with documentation	15 Marks
Phase -III	Submission of report, Final presentation and demonstration	25 Marks

Scheme for Semester End Evaluation (SEE):

The SEE examination shall be conducted by an external examiner (domain expert) and an internal examiner. Evaluation shall be done in batches, not exceeding 6 students per batch.

Scheme of Evaluation for SEE	
Particulars	%Marks
Project Synopsis (Initial Writeup)	10%
Project Demo/Presentation	30%
Methodology and Results Discussion	30%
Project Work Report	10%
Viva-voce	20%
Total	100



Semester: VIII					
MAJOR PROJECT					
Course Code	:	21CS81P		CIE	: 100 Marks
Credits: L:T:P	:	0:0:12		SEE	: 100 Marks
Hours/Week	:	24		SEE Duration	: 03 Hours
GUIDELINES					
<ol style="list-style-type: none">1. The project topic, title and synopsis have to be finalized and submitted to their respective internal guide(s) before the beginning of the 8th semester.2. The detailed Synopsis (approved by the department Project Review Committee) has to be submitted during the 1st week after the commencement of 8th semester.					
<u>Batch Formation:</u>					
<ul style="list-style-type: none">• Students are free to choose their project partners from within the program or any other program.• Each student in the team must contribute towards the successful completion of the project.• The project may be carried out In-house / Industry / R & D Institution. The project work is to be carried out by a team of two to four students, in exceptional cases where a student is placed in a company and offered an internship through the competitive process or student is selected for internship at national or international level through competitive process, the student can work independently.• The students are allowed to do either a project for full 5 days in the industry or full 5 days in the college.• In case the project work is carried out outside Bengaluru, such students must be available during Project Evaluation process scheduled by the respective departments and they must also interact with their guide regularly through Email / Webinar / Skype etc.					
<u>Project Topic Selection:</u>					
<p>The topics of the project work must be in the field of respective program areas or in line with CoE's(Centre of Excellence) identified by the college or List of project areas as given by industry/Faculty. The projects as far as possible should have societal relevance with focus on sustainability.</p> <p>Students can select courses in NPTEL from the discipline of Humanities and Social Sciences, Management, Multidisciplinary and Design Engineering. The course chosen could be either of 4w/8w/12w duration. The students need to enrol for a course, register for the exam and submit the e-certificate to the department, as and when it is released by NPTEL. The same will be considered as one of the components during project evaluation of phase 2 and phase 5.</p>					
<u>Project Evaluation:</u>					
<ul style="list-style-type: none">• Continuous monitoring of project work will be carried out and cumulative evaluation will be done.• The students are required to meet their internal guides once in a week to report their progress in project work.• Weekly Activity Report (WAR) has to be maintained in the form of a diary by the project batch and the same has to be discussed with the Internal Guide regularly.• In case of Industry project, during the course of project work, the internal guides will have continuous interaction with external guides and will visit the industry at least twice during the project period.• For CIE assessment the project groups must give a final seminar with the draft copy of the project report.• The presentation by each group will be for 20-30 minutes and every member of the team needs to justify the contributions to the project.					



- The project team is required to submit Hard copies of the detailed Project Report in the prescribed format to the department.
- For CIE 50% weightage should be given to the project guide and 50% weightage to the project evaluation committee.
- Before the final evaluations the project group is required to produce a No dues certificate from Industry, Central Library and Department.

Course Outcomes:

After going through the major project the student will be able to:

CO1: Apply knowledge of mathematics, science and engineering to solve respective engineering domain problems.

CO2: Design, develop, present and document innovative/multidisciplinary modules for a complete engineering system.

CO3: Use modern engineering tools, software and equipment to solve problem and engage in life-long learning to follow technological developments.

CO4: Function effectively as an individual, or leader in diverse teams, with the understanding of professional ethics and responsibilities.

Scheme of Continuous Internal Evaluation (CIE):

The following are the weightings given for the various stages of the project.

1.Selection of the topic and formulation of objectives	10%
2.Design and Development of Project methodology	25%
3.Execution of Project	25%
4.Presentation,Demonstration and Results Discussion	30%
5.Report Writing & Publication	10%

Scheme for Semester End Evaluation (SEE):

The following are the weightages given during Viva Examination.

1.Written presentation of synopsis	10%
2.Presentation/Demonstration of the project	30%
3.Methodology and Experimental Results &Discussion	30%
4.Report	10%
5.VivaVoce	20%



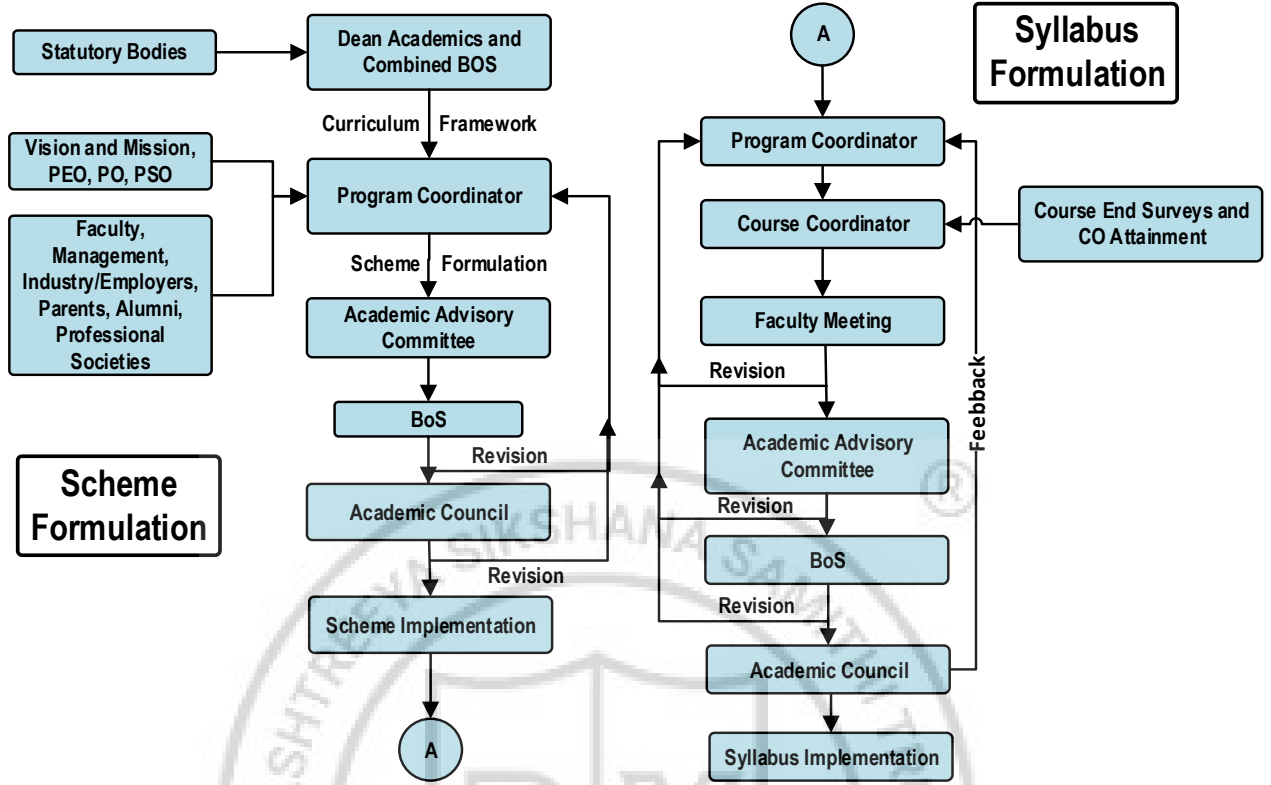
Calendar of Events for the Project Work:

Week	Event
Beginning of 7 th Semester	Formation of group and approval by the department committee.
7 th Semester	Problem selection and literature survey
Last two weeks of 7 th Semester	Finalization of project and guide allotment
II Week of 8 th Semester	Synopsis submission and preliminary seminar
III Week	First visit of the internal guides to industry(In case of project being carried out In industry)
III to VI Week	Design and development of project methodology
VII to IX Week	Implementation of the project
X Week	Submission of draft copy of the project report
XI and XII Week	Second visit by guide to industry for demonstration. Final seminar by Department project Committee and guide for internal assessment. Finalization of CIE.

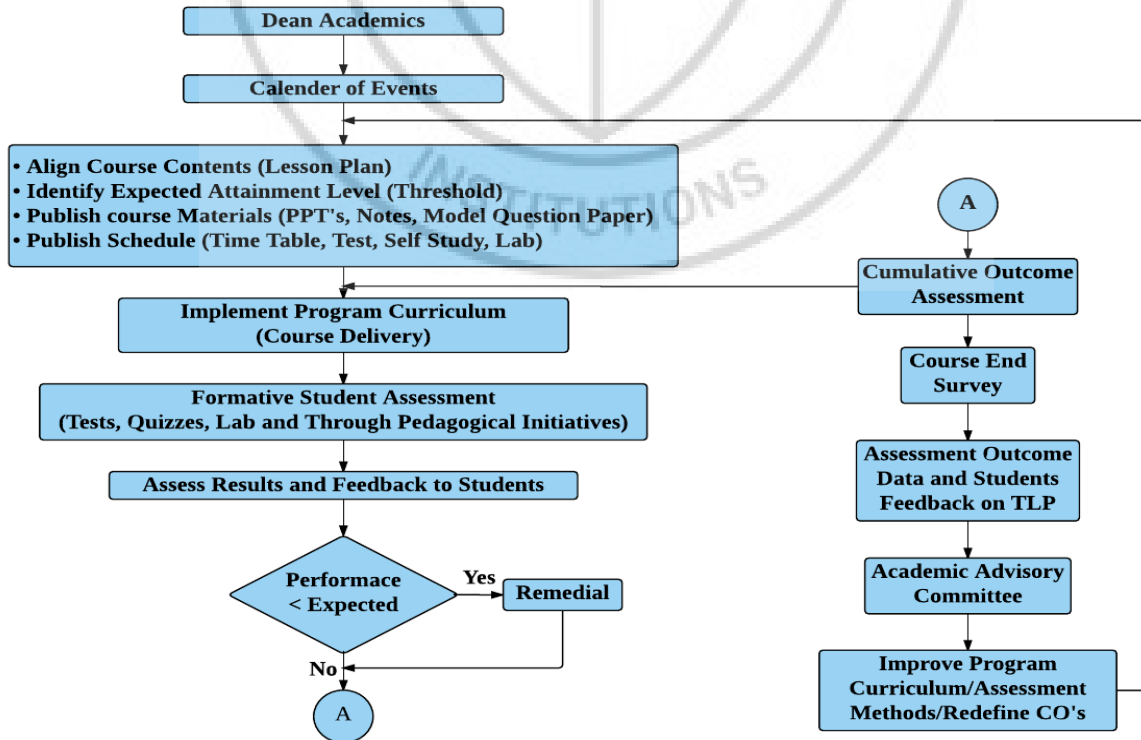
Evaluation & Scheme for CIE and SEE

Scheme of Evaluation for CIE		Scheme of Evaluation for SEE	
Particulars	%Marks	Particulars	%Marks
Project Evaluation I	10%	Project Synopsis(Initial Writeup)	10%
Project Evaluation II	25%	Project Demo/Presentation	30%
Project Evaluation III	25%	Methodology and Results Discussion	30%
Project Evaluation Phase-IV (Submission of Draft Project Report for Verification)	30%	Project Work Report	10%
Project Evaluation Phase-V (Project Final Internal Evaluation)	10%	Viva-voce	20%
Total	100	Total	100

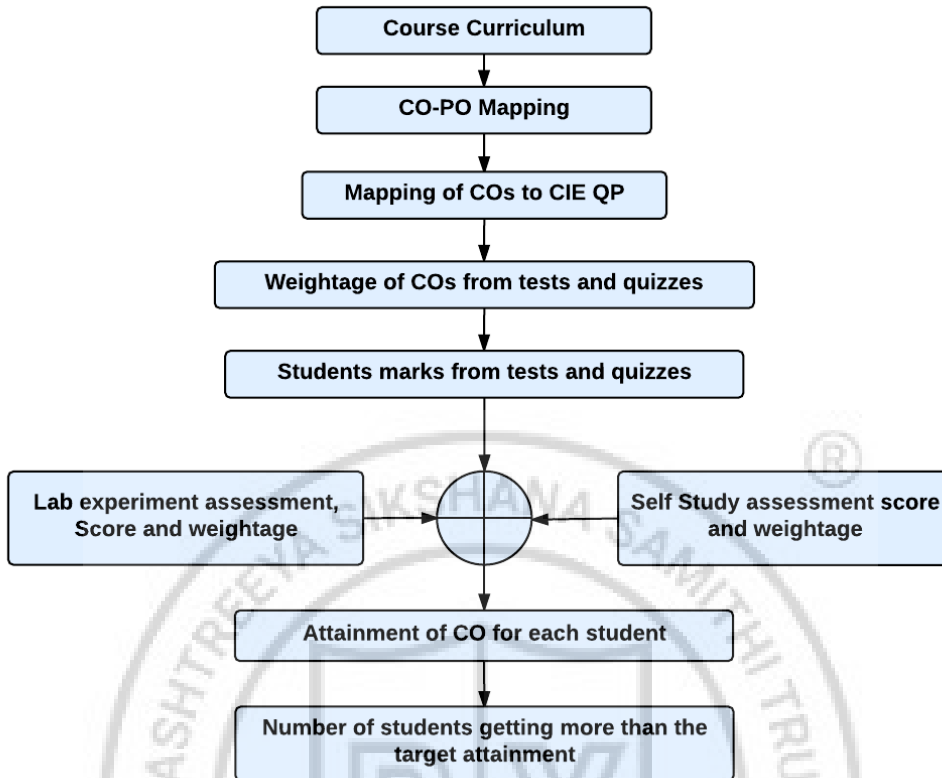
Curriculum Design Process



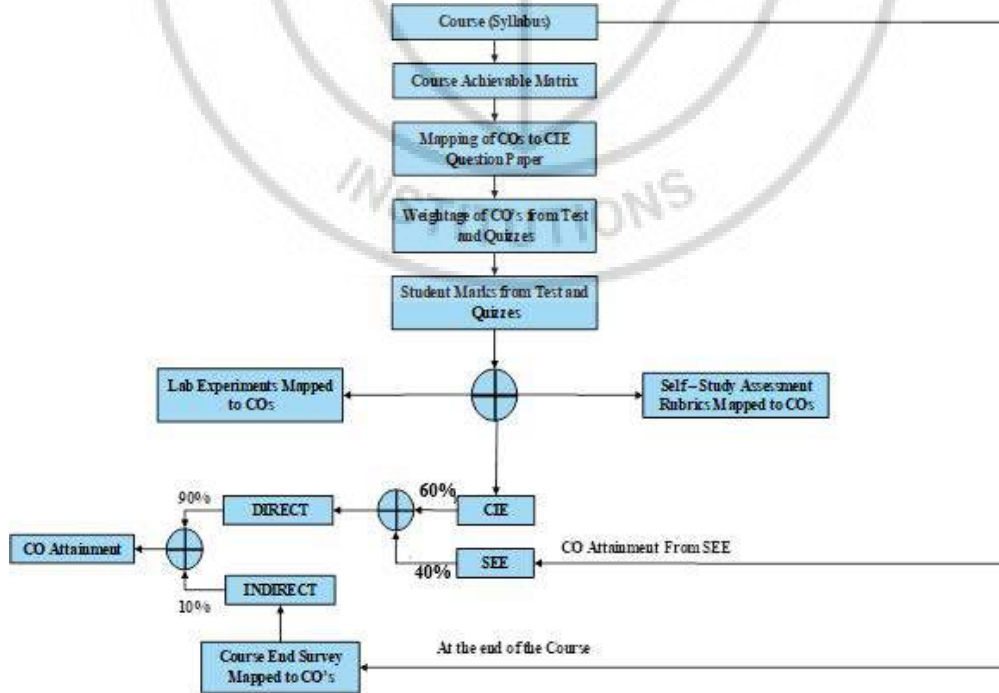
Academic Planning and Implementation



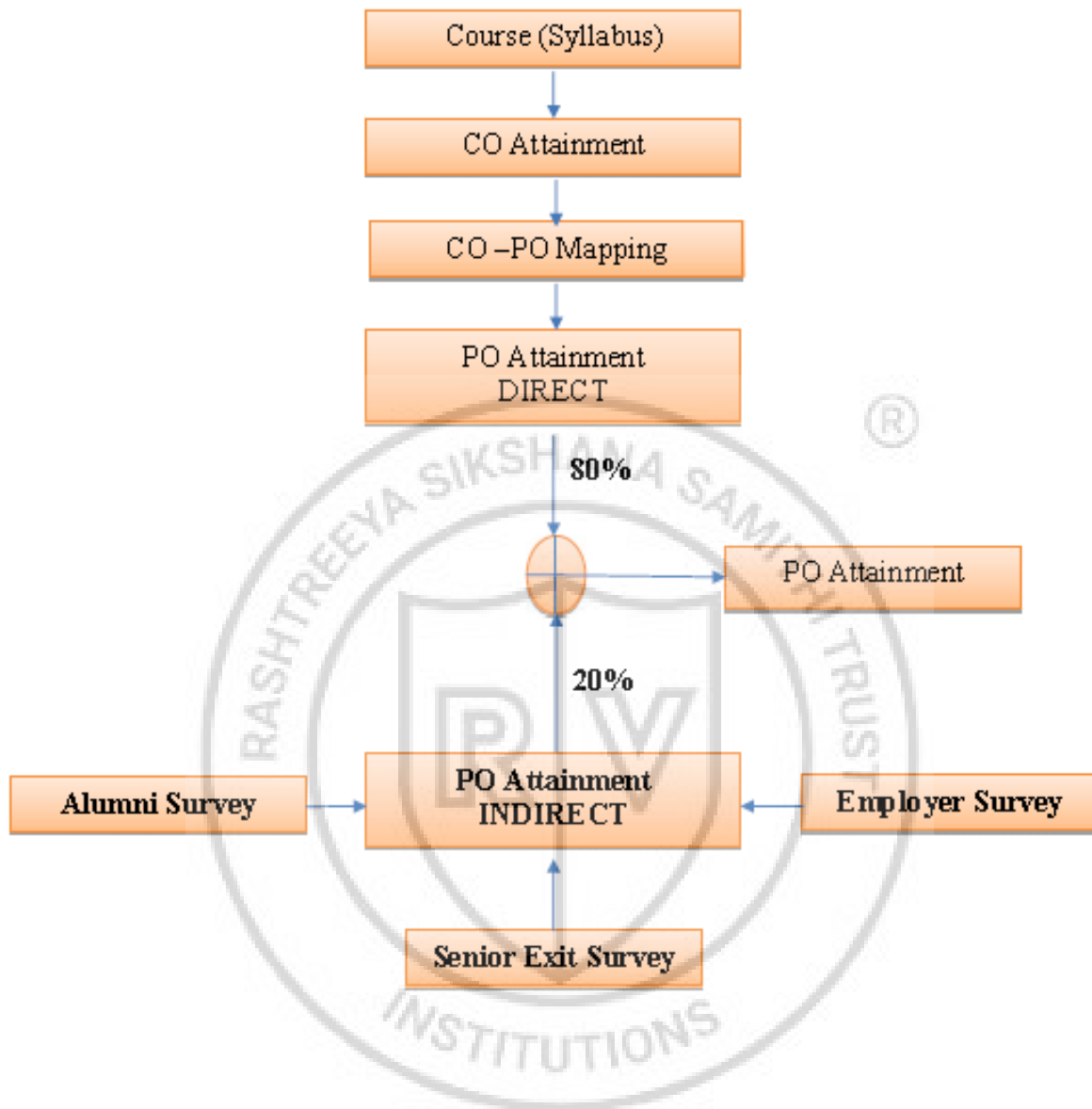
Process For Course Outcome Attainment



Final CO Attainment Process



Program Outcome Attainment Process





KNOWLEDGE & ATTITUDE PROFILE

- **WK1:** A systematic, theory-based understanding of the natural sciences applicable to the discipline and awareness of relevant social sciences.
- **WK2:** Conceptually-based mathematics, numerical analysis, data analysis, statistics and formal aspects of computer and information science to support detailed analysis and modelling applicable to the discipline.
- **WK3:** A systematic, theory-based formulation of engineering fundamentals required in the engineering discipline.
- **WK4:** Engineering specialist knowledge that provides theoretical frameworks and bodies of knowledge for the accepted practice areas in the engineering discipline; much is at the forefront of the discipline.
- **WK5:** Knowledge, including efficient resource use, environmental impacts, whole-life cost, re-use of resources, net zero carbon, and similar concepts, that supports engineering design and operations in a practice area.
- **WK6:** Knowledge of engineering practice (technology) in the practice areas in the engineering discipline.
- **WK7:** Knowledge of the role of engineering in society and identified issues in engineering practice in the discipline, such as the professional responsibility of an engineer to public safety and sustainable development.
- **WK8:** Engagement with selected knowledge in the current research literature of the discipline, awareness of the power of critical thinking and creative approaches to evaluate emerging issues.
- **WK9:** Ethics, inclusive behaviour and conduct. Knowledge of professional ethics, responsibilities, and norms of engineering practice. Awareness of the need for diversity by reason of ethnicity, gender, age, physical ability etc. with mutual understanding and respect, and of inclusive attitudes.



PROGRAM OUTCOMES (POs)

- ❖ **PO1:** Engineering Knowledge: Apply knowledge of mathematics, natural science, computing, engineering fundamentals and an engineering specialization as specified in WK1 to WK4 respectively to develop to the solution of complex engineering problems.
- ❖ **PO2:** Problem Analysis: Identify, formulate, review research literature and analyze complex engineering problems reaching substantiated conclusions with consideration for sustainable development. (WK1 to WK4)
- ❖ **PO3:** Design/Development of Solutions: Design creative solutions for complex engineering problems and design/develop systems/components/processes to meet identified needs with consideration for the public health and safety, whole-life cost, net zero carbon, culture, society and environment as required. (WK5)
- ❖ **PO4:** Conduct Investigations of Complex Problems: Conduct investigations of complex engineering problems using research-based knowledge including design of experiments, modelling, analysis & interpretation of data to provide valid conclusions. (WK8).
- ❖ **PO5:** Engineering Tool Usage: Create, select and apply appropriate techniques, resources and modern engineering & IT tools, including prediction and modelling recognizing their limitations to solve complex engineering problems. (WK2 and WK6)
- ❖ **PO6:** The Engineer and The World: Analyze and evaluate societal and environmental aspects while solving complex engineering problems for its impact on sustainability with reference to economy, health, safety, legal framework, culture and environment. (WK1, WK5, and WK7).
- ❖ **PO7:** Ethics: Apply ethical principles and commit to professional ethics, human values, diversity and inclusion; adhere to national & international laws. (WK9)
- ❖ **PO8:** Individual and Collaborative Team work: Function effectively as an individual, and as a member or leader in diverse/multi-disciplinary teams.
- ❖ **PO9:** Communication: Communicate effectively and inclusively within the community and society at large, such as being able to comprehend and write effective reports and design documentation, make effective presentations considering cultural, language, and learning differences
- ❖ **PO10:** Project Management and Finance: Apply knowledge and understanding of engineering management principles and economic decision-making and apply these to one's own work, as a member and leader in a team, and to manage projects and in multidisciplinary environments.
- ❖ **PO11:** Life-Long Learning: Recognize the need for, and have the preparation and ability for i) independent and life-long learning ii) adaptability to new and emerging technologies and iii) critical thinking in the broadest context of technological change. (WK8)

INNOVATIVE TEAMS OF RVCE

Ashwa Mobility Foundation (AMF): Designs and fabricates Formula-themed race cars and mobility solutions to address urban transportation issues.

Astra Robotics Team: Focuses on designing and building application-specific robots.

Coding Club: Helps students gain coding skills and succeed in competitions like GSoC and ACM-ICPC.

Entrepreneurship Development Cell (E-Cell): Promotes entrepreneurship through workshops, speaker sessions, and mentoring for startups.

Frequency Club Team: Works on software and hardware, emphasizing AI and Machine Learning.

Team Garuda: Develops a supermileage urban concept electric car and E-mobility products.

Team Jatayu: Builds low-cost UAVs with autonomous capabilities for various tasks.

Solar Car Team: Aims to create a solar electric vehicle for sustainable transportation.

Team Antariksh: Focuses on space technology and the development of operational rockets.

Team Chimera: Builds a Formula Electric Car through R&D in E-Mobility.

Helios Racing Team: Designs and tests All-Terrain Vehicles, participating in SAE's BAJA competitions.

Team Hydra: Develops autonomous underwater vehicles for tasks like water purification.

Team Krushi: Creates low-cost farming equipment to assist farmers in cultivation and harvesting.

Team Vyoma: Designs and tests radio-controlled aircraft and UAVs.

Team Dhruva: Engages in astronomy-related activities and collaborates on projects with organizations like ICTS and IIA.

Ham Club: Promotes Amateur Radio and explores technical innovations in communications, especially for disaster response.

Cultural Activity Teams

1. AALAP (Music club)
2. DEBSOC (Debating society)
3. CARV (Dramatics club)
4. FOOTPRINTS (Dance club)
5. QUIZCORP (Quizzing society)
6. ROTARACT (Social welfare club)
7. RAAG (Youth club)
8. EVOKE (Fashion team)
9. f/6.3 (Photography club)
10. CARV ACCESS (Film-making)



NSS of RVCE



NCC of RVCE



VISION

Leadership in Quality Technical Education, Interdisciplinary Research & Innovation, with a Focus on Sustainable and Inclusive Technology



MISSION

- To deliver outcome based Quality education, emphasizing on experiential learning with the state of the art infrastructure.
- To create a conducive environment for interdisciplinary research and innovation.
- To develop professionals through holistic education focusing on individual growth, discipline, integrity, ethics and social sensitivity.
- To nurture industry-institution collaboration leading to competency enhancement and entrepreneurship.
- To focus on technologies that are sustainable and inclusive, benefiting all sections of the society.



QUALITY POLICY

Achieving Excellence in Technical Education, Research and Consulting through an Outcome Based Curriculum focusing on Continuous Improvement and Innovation by Benchmarking against the global Best Practices.



CORE VALUES

Professionalism, Commitment, Integrity, Team Work, Innovation



RV College of
Engineering®

Mysore Road, RV Vidyaniketan Post,
Bengaluru - 560059, Karnataka, India | +91-80-68188110 | www.rvce.edu.in



Scan Here

Go, change the world®