

Scholars who are pursuing PhD in RVCE ECE Research Center (External candidates)

Name of the scholar	Area of research	Name of the Guide
Mr.Vasudeva G	Design and characterization of ADC using TFT's for energy efficient systems	Dr.B.V.Uma
Mr.Premananda B.S	Speech enhancement methodology for next generation mobile technology in noisy environment	Dr.B.V.Uma
Mrs.Roja Reddy	Waveform modeling for MIMO radar with space time adaptive processing	Dr.M.Uttara Kumari
Ms.Bhavya R	Development of fast algorithms for feature processing and modelling in motion tracking	Dr.K.S.Geetha
Mr.Muralidharan V	Development and characterisation of sensors for vibration survey on aero engines	Dr.K.S.Geetha – Co guide
Mr.Praveenkumar Y G	Design, optimization, testing and comparative analysis of vedic multipliers using reversible logic	Dr.Kariyappa B.S
Mr.Hemath Kumar C.S	Design, optimization and implementation of SRAM cells for high density memory application	Dr.Kariyappa B.S
Mr.Binu	Design and development of an artificial inspired search algorithm for fault diagnosis of electronic circuits and its applications	Dr.Kariyappa B.S
Mr.Naganna Shankar Sollapure	An efficient VLSI implementation of enhanced edge preserving impulse noise removal techniques	Dr.H.V.Ravish Aradhya
Mr.Mohana	Design analysis of a smart intelligent low power visual surveillance system for security applications	Dr.H.V.RavishAradhya
Mr.Govind M.R	Modeling and Design aspects of FinFETs for Low Power SRAM Circuit Design	Dr.H.V.Ravish Aradhya
Mr.Vivekanand GS	Model based verification and validation of electronic engine controller performance for developmental aero engine	Dr.M.S.Suma – Co guide
Mr.Chandrashekhar Patil	Through silicon via (TSV) modeling and related thermal solutions for a future generation 3D-Ics	Dr.M.S.Suma
Ms.Poornima M	Fault tolerant reversible 64-bit multiple and accumulate (MAC) unit	Dr.M.S.Suma
Ms.Jisha P	Fabrication of rapid malaria sensor for malaria detection	Dr.M.S.Suma
Mr.Sandeep S	Electronic device for detection and reduction of intussusception	Dr.M.S.Suma
Mr.Kumar P	FPGA based low complexity, high performance architecture for FMBC transmitter	Dr.Usha Rani K.R

Ms.Smitha Gayathri D	Mitigation of impulse noise and load effects in broad band PLC	Dr.Usha Rani K.R
Ms.Sangeetha K.N	Knowledge based image steganographic algorithm selection using artificial intelligence techniques	Dr.Veenadevi S.V
Ms.Padmavathi	Compression and cancellation of artefact of ECG signals using modified non linear filter	Dr.Veenadevi S.V
Ms.Rajine Swetha R	A study of hybrid genetic algorithm based approach to circuit partitioning, mixed-size placement & timing driven placement	Dr. Sumitra Devi
Hemalatha J N	Dynamic modeling & performance evaluation of hybrid renewable energy distributed generation system	Dr.S. A. Hariprasad
Adinath Jain	Design & implementation of efficient controller for unmanned ship navigation	Dr.S. A. Hariprasad
B. Shekhar Babu	Coarsening hierarchical mixed-size placement algorithm with timing constraints estimation	Dr. Sumitra Devi
Chanda V Reddy	Bit loading algorithms with optimized BIT rate & power allocation in wireless communication systems	Dr. K.V. Padmaja