

Scholars who are pursuing PhD in RVCE ECE Research Center (Internal candidates)

Name of the scholar	Area of research	Name of the Guide
Mrs.D.R.Shilpa	Improving the performance of high speed digital system by reducing signal integrity problem	Dr.B.V.Uma
Mr.M.Govinda Raju	Phase noise reduction in OFDM system using wavelet transform	Dr.B.V.Uma
Mr.Prakash Biswagar	Analysis of shaped dielectric lens antennas for mobile broadband systems(MBS)	Dr.S.Ravishankar
Mr.Shushrutha K S	Modal analysis & beam steering of spheroidal dielectric lens array antennas	Dr.S.Ravishankar
Mr. Mahesh A	A newhybrid analytical method for radiation through dielectric lenses	Dr.S.Ravishankar
Mrs.K.Saraswathi	Novel tone loading algorithms with echo cancellation for broadband dynamics spectrum management over twisted pairs	Dr.S.Ravishankar
Mrs. P N Jayanthi	Novel detection algorithms with dynamic spectrum management in MIMO	Dr.S.Ravishankar
Mrs. K.A Nethravathi	Design of an intruder alarm by wireless underground sensor networks	Dr.S.Ravishankar
Mrs.Chethana G	Tone loading algorithms with space time spreading in MIMO performance analysis	Dr.S.Ravishankar
Mrs.Sheela C.S	Performance of lens-array smart antennas in millimeter and sub-millimeter (THz) waveband	Dr.S.Ravishankar
Mrs.Sahana B	Development of adaptive resource allocation algorithms for discrete multitone transmission in underwater acoustic Communication.	Dr.S.Ravishankar
Mrs.Sujatha D Badiger	Fusion of seismic and acoustic signals for classification and detection of multi targetusing signal processing	Dr.M.Uttara Kumari
Mr.Ravishankar Holla	Thin film transistor sensor based wireless sensor network	Dr.M.Uttara Kumari
Ms.Roopa J	Design and development of wireless communicated condition monitoring system using thin film sensors	Dr.K.S.Geetha
Mr.P.Narashimaraja	Novel photonic devices and circuits for photonic Integrated circuits	Dr.Kariyappa B.S
Mr.Arun Kumar P Chavan	Performance improvement of sigma-delta ADC for wide band applications	Dr.H.V.Ravish Aradhya
Ms.Namita Palecha	Low power high fault coverage test patterns generation for memories	Dr.M.S.Suma
Mrs.Rajani Katiyar	A new method of equilizationfor broadband dynamicspectrum management over twiste pairs	Dr. K.V. Padmaja
Sujatha Hiremath	Low power circuit design and testability	Dr.Deepali Koppad