

**Rashtreeya Sikshana Samithi Trust**

**R. V. College of Engineering**

*(Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavi)*



**Department of Electronics & Communication  
Engineering**

**Master of Technology (M. Tech.)**

**VLSI DESIGN  
&  
EMBEDDED SYSTEMS  
(MVE)**

**Scheme and Syllabus of  
Autonomous System w.e.f 2016**

**R. V. College of Engineering, Bengaluru – 59***(An Autonomous Institution affiliated to Visvesvaraya Technological University, Belagavi)***Department of Electronics & Communication Engineering****M. Tech. VLSI Design & Embedded Systems**

<b>FIRST SEMESTER</b>								
<b>Sl. No</b>	<b>Course Code</b>	<b>Course Title</b>	<b>BoS</b>	<b>CREDIT ALLOCATION</b>				<b>Total Credits</b>
				<b>Lecture L</b>	<b>Tutorial T</b>	<b>Practical P</b>	<b>Self Study S</b>	
1	16MEM11R	Research Methodology	IM	3	1	0	0	4
2	16MVE12	Digital IC Design (Theory and Practice)	ECE	4	0	1	0	5
3	16MVE13	Advanced Embedded System Design	ECE	4	0	0	1	5
4	16MVE14	Digital System Design using Verilog	ECE	4	0	0	0	4
5	16MVE15x	Elective 1	ECE	4	0	0	0	4
6	16HSS16	Professional Skill Development	HSS	0	0	2	0	2
		<b>Total</b>		<b>19</b>	<b>1</b>	<b>3</b>	<b>1</b>	<b>24</b>

<b>Elective-1</b>			
16MVE151	Advanced Embedded Processors	16MVE152	ASIC Design

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<b>SECOND SEMESTER</b>								
<b>Sl. No</b>	<b>Course Code</b>	<b>Course Title</b>	<b>BoS</b>	<b>CREDIT ALLOCATION</b>				<b>Total Credits</b>
				<b>Lecture L</b>	<b>Tutorial T</b>	<b>Practical P</b>	<b>Self Study S</b>	
1	16MEM21P	Project Management	IM	3	1	0	0	4
2	16MVE22	Analog IC Design (Theory and Practice)	ECE	4	0	1	0	5
3	16MVE23x	Elective-2	ECE	4	0	0	0	4
4	16MVE24x	Elective-3	ECE	4	0	0	0	4
5	16MVE25x	Elective-4	ECE	4	0	0	0	4
6	16XXX26	Minor Project	Resp	0	0	5	0	5
		<b>Total</b>		<b>19</b>	<b>1</b>	<b>6</b>	<b>0</b>	<b>26</b>

<b>Elective-2</b>			
16MVE231	CAD Tools for VLSI	16MVE232	Real Time Embedded Systems
<b>Elective-3</b>			
16MVE241	Low Power VLSI Design	16MVE242	Advanced Embedded Programming
<b>Elective-4</b>			
16MVE251	High Speed VLSI Design	16MVE252	MEMS and Smart Systems

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<b>THIRD SEMESTER</b>								
<b>Sl. No</b>	<b>Course Code</b>	<b>Course Title</b>	<b>BoS</b>	<b>CREDIT ALLOCATION</b>				<b>Total Credits</b>
				<b>Lecture L</b>	<b>Tutorial T</b>	<b>Practical P</b>	<b>Self Study S</b>	
1	16MVE31	Synthesis & Optimization of Digital Circuits (Theory and Practice)	ECE	4	0	1	0	5
2	16MVE32x	Elective-5	ECE	4	0	0	0	4
3	16MVE33x	Elective-6	ECE	4	0	0	0	4
4	16MVE34x	Elective-7	ECE	4	0	0	0	4
5	16MVE35	Internship/Industrial Training	ECE	0	0	3	0	3
6	16MVE36	Technical Seminar	ECE	0	0	2	0	2
		<b>Total</b>		<b>16</b>	<b>0</b>	<b>6</b>	<b>0</b>	<b>22</b>

<b>Elective-5</b>			
16MVE321	Optimization in Embedded Systems	16MVE322	VLSI testing & Verification
<b>Elective-6</b>			
16MVE331	VLSI for Signal Processing	16MVE332	Mixed Signal IC Design
<b>Elective-7</b>			
16MVE341	Reconfigurable Computing	16MVE342	RFIC Design

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<b>FOURTH SEMESTER</b>								
<b>Sl. No</b>	<b>Course Code</b>	<b>Course Title</b>	<b>BoS</b>	<b>CREDIT ALLOCATION</b>				<b>Total Credits</b>
				<b>Lecture L</b>	<b>Tutorial T</b>	<b>Practical P</b>	<b>Self Study S</b>	
1	16MVE41	Major Project	ECE	0	0	26	0	26
2	16MVE42	Seminar	ECE	0	0	2	0	2
		<b>Total</b>		<b>0</b>	<b>0</b>	<b>28</b>	<b>0</b>	<b>28</b>