

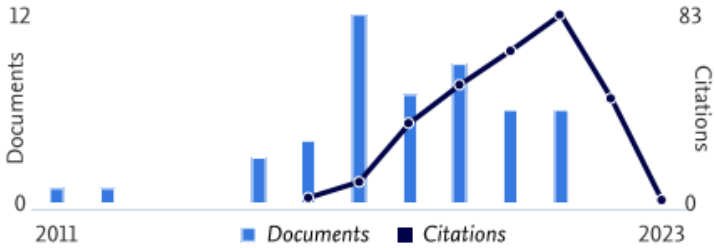






Dr. H V R Aradhya, ECE, Paper Publication Details:

Papers published in Journals/Conferences: Total - - 138

S. No	Dr. HVR – Author Profile/Credentials can be viewed at	Publisher								
1	<p> http://www.scopus.com/authid/detail.url?authorId=55616592800  http://orcid.org/0000-0003-3076-9120 ORCID: 0000-0003-3076-9120; publications: 49; citations: 267; H-index: 09 Aradhya, H. V.Ravish Department of Electrical & Computer Engineering, Bengaluru, India  https://orcid.org/0000-0003-3076-9120 </p> <hr/> <p> Metrics overview 49 Documents by author 295 Citations by 217 documents 10 h-index: View h-graph </p> <p> Document & citation trends  </p> <p>as on 06/06/2022.</p>	Scopus								
2	<p> https://publons.com/dashboard/records/publication/authored/ Researcher ID: 0-6994-2016; publications: 77; citations: 56; H-index: 4 </p> <div style="display: flex; align-items: center;">  <div> <p>Ravish Aradhya H. V</p> <p><i>"Aradhya H V Ravish"</i></p> <p>Web of Science ResearcherID </p> <p>0-6994-2016</p> </div> </div> <p>Faculty - Electronics and Communication Engineering, R V College of Engineering, Bangalore</p> <p>Web of Science Core Collection metrics</p> <table style="width: 100%; border: none;"> <tr> <td style="text-align: center; width: 50%;">4</td> <td style="text-align: center; width: 50%;">27</td> </tr> <tr> <td style="text-align: center;">H-Index</td> <td style="text-align: center;">Publications in Web of Science</td> </tr> <tr> <td style="text-align: center; border-top: 1px solid black;">69</td> <td style="text-align: center; border-top: 1px solid black;">61</td> </tr> <tr> <td style="text-align: center; border-top: 1px solid black;">Sum of Times Cited</td> <td style="text-align: center; border-top: 1px solid black;">Citing Articles</td> </tr> </table> <p style="text-align: right;">as on 06/06/2022.</p>	4	27	H-Index	Publications in Web of Science	69	61	Sum of Times Cited	Citing Articles	WOS
4	27									
H-Index	Publications in Web of Science									
69	61									
Sum of Times Cited	Citing Articles									

3	<p>https://www.semanticscholar.org/author/H.-V.-Ravish-Aradhya/19255649 https://www.semanticscholar.org/author/H.-V.-Ravish-Aradhya/9377770?sort=year</p> <p>ORCID: 0000-0003-3076-9120; publications: 38; citations: 158; H-index: 7</p> <p>RAVISH ARADHYA H V RV College of Engineering, Bengaluru</p> <p> 0000-0003-3076-9120 www.rvce.edu.in</p> <p>Publications 38 h-index 7 Citations 168 Highly Influential Citations 5 as on 06/06/2022.</p>	Semantic Scholar												
4	<p>https://scholar.google.com/citations?hl=en&user=T0QNp0kAAAAJ https://scholar.google.com/citations?hl=en&user=C5X7B-A AAAAJ</p> <p>publications: 138; citations: 446; H-index: 11; i-10 index: 11</p> <p>9/29/22, 9:59 AM Ravish Aradhya H. V. - Google Scholar</p> <div style="display: flex; align-items: flex-start;">  <div> <p>Ravish Aradhya H. V.</p> <p>Professor and Associate Dean, Electronics and Communication Engineering, R. V. College of Low Power VLSI design and Embedded ...</p> </div> <table border="1" style="margin-left: 20px; border-collapse: collapse;"> <thead> <tr> <th></th> <th>All</th> <th>Since 2017</th> </tr> </thead> <tbody> <tr> <td>Citations</td> <td>488</td> <td>411</td> </tr> <tr> <td>h-index</td> <td>11</td> <td>11</td> </tr> <tr> <td>i10-index</td> <td>16</td> <td>12</td> </tr> </tbody> </table> </div> <p>as on 06/06/2022.</p>		All	Since 2017	Citations	488	411	h-index	11	11	i10-index	16	12	Google Scholar
	All	Since 2017												
Citations	488	411												
h-index	11	11												
i10-index	16	12												
Published by		Number												
1.	International Journals	44												
2.	International Conferences	71												
3.	National Conferences	23												
4.	National Journals	00												
5.	PhD Forum	03												
Total Number of Publishes		135												
IEEE, SPRINGER, ELSEVIER PAPERS														
1	SCI Indexed	27												
2	WOS Indexed	77												
3	Scopus Indexed	45												
4	IEEE Conferences	43												
5	SPRINGER Conferences	06												
6	ELSEVIER Conferences	02												
7	Others	60												
BEST PAPER AWARDS - - 04														
1	Journal – Paper No. 16	01												
2	Conference – Paper No. 10	01												
3	Conference – Paper No. 32 (IEEE Conference)	01												
4	PhD forum presentation @ IISc and IIIT–B Conference -Paper No. 31 and Paper No. 55	03												

YEAR WISE PUBLICATIONS		
1.	Year 2022	01
2.	Year 2021	12
3.	Year 2020	12
4.	Year 2019	16
5.	Year 2018	12
6.	Year 2017	09
7.	Year 2016	16
8.	Year 2015	15
9.	Year 2014	04
10.	Year 2013	04
11.	Year 2012	15
12.	Year 2011	07
13.	Year 2010	04
14.	Year 2009	07
15.	Year 2008	01
16.	Year 2007	01
	Total Publications	135
	To be published	02

(i) International Journals: 14

1. Ravish Aradhya H. V, Mohana, **“Object Detection and Classification Algorithms using Deep Learning for Video Surveillance Applications,”** International Journal of Innovative Technology and Exploring Engineering (IJITEE), Vol. 8, Issue 8, Jun 2019, ISSN: 2278-3075, pp. 386-395. (Scopus Indexed) [Download link: <https://www.ijitee.org/wp-content/uploads/papers/v8i8/H6362068819.pdf>]
2. Ravish Aradhya H. V, Mohana, **“Object Detection and Tracking for Video Surveillance Applications,”** International journal of Advanced Computer Science and Applications (IJACSA), Vol. 10, No. 12, ISSN: 2156-5570, Dec-2019, pp: 517-530. (Scopus Indexed) [Download link: https://thesai.org/Downloads/Volume10No12/Paper_69-Object_Detection_and_Tracking_using_Deep_Learning.pdf]
3. Pragna G, Ravish Aradhya H V, **“Review on Skilled Optimization of Voltage Controlled Oscillator,”** International Journal of Research and Analytical Reviews (IJRAR), Vol. 7, Issue. 2, pp. 330-334, ISSN: E-2348-1269, P-2349-5138, Jun 2020 (UGC Approved-Journal No: 43602).
4. Abhishek R. Deshmukh, HV Ravish Aradhya, **“Experimental analysis of performance metrics for configuration of AutoScaling Groups,”** International Research Journal of Engineering and Technology (IRJET), e-ISSN: 2395-0056, p-ISSN: 2395-0072, Vol: 07 Issue: 05, pp. 1-9, May 2020.

5. Ravish Aradhya H. V, Mohana, “**Design of Efficient Algorithms for Object Detection and Tracking using Deep-learning and Artificial Intelligence,**” SN Computer (Springer Nature-IJACSA), Vol. 10, No. 12, ISSN: , Jun-2020, pp: . (Scopus Indexed)
6. Manjunath K M, Muralidhara K M, HV Ravish Aradhya, “**Binary Multiplier (n-Bit Size) Design, Implementation, Analysis and Comparison of Various Logics,**” International Journal of Advanced Science and Technology, Vol. 29, No. 7, (2020), ISSN: 2005-4238, pp. 8206-8223 (Scopus indexed).

[Download link: <http://sersc.org/journals/index.php/IJAST/article/view/24645/13005>]

7. Manjunath K M, Muralidhara K M, Ravish Aradhya H V, “**Incrementer and Decrementer of an N-Bit Input, by a Value 'M' Circuit Design and Analysis for Various Parameters,**” Journal of Critical Reviews, (JCR), Vol. 7, Issue 05, June 2020, ISSN: 2394-5125, pp. 1781-1794. (Scopus indexed). doi: [10.31838/jcr.07.05.302](https://doi.org/10.31838/jcr.07.05.302)

[Download link: <http://www.jcreview.com/fulltext/197-1595575457.pdf?1596341407> or <http://www.jcreview.com/fulltext/197-1595575457.pdf?1596342177>]

8. Kiran G Shetty, H V Ravish Aradhya, “**Comparison of Dynamic Voltage Scaling(DVS) of Core Voltage Using the Onboard Voltage Regulator and External Voltage Regulator via I2C Protocol in Automotive Microcontroller,**” Journal of University of Shanghai for Science and Technology, ISSN: 1007-6735, Volume: 23, Issue: 6, (Scopus Indexed), pp: 794-804, Jun 2021. (WOS and Scopus Indexed-IEEE)

(Download Link: <https://jusst.org/wp-content/uploads/2021/06/Comparison-of-Dynamic-Voltage-ScalingDVS-of-Core-Voltage-Using-the-On-board-Voltage-Regulator-and-External-Voltage-Regulator-via-I2C-Protocol-in-Automotive-Microcontroller.pdf>, <https://jusst.org/comparison-of-dynamic-voltage-scalingdvs-of-core-voltage-using-the-on-board-voltage-regulator-and-external-voltage-regulator-via-i2c-protocol-in-automotive-micro-controller/>)

9. Kiran G Shetty, H V Ravish Aradhya, “**Instruction Based Power Estimation Method in AURIX Microcontroller,**” Journal of University of Shanghai for Science and Technology, ISSN: 1007-6735, Volume: 23, Issue: 6, (Scopus Indexed), pp: 514-520, Jun 2021.

(Download Link: <https://jusst.org/wp-content/uploads/2021/06/Instruction-Based-Power-Estimation-Method-in-AURIX-Microcontroller.pdf>, <https://jusst.org/instruction-based-power-estimation-method-in-aurix-micro-controller/>) (WOS and Scopus Indexed-IEEE)

10. Ankitha, H V Ravish Aradhya, “**A Python Based Design Verification Methodology,**” Journal of University of Shanghai for Science and Technology, ISSN: 1007-6735, Volume: 10, Issue: 6, (Scopus Indexed), pp: 901-911, Jun 2021. (WOS and Scopus Indexed-IEEE)

(Download Link: <https://jusst.org/wp-content/uploads/2021/06/A-Python-based-Design-Verification-Methodology.pdf>, <https://jusst.org/a-python-based-design-verification-methodology/>)

11. Deekshith Nayak, H V Ravish Aradhya, “**Orchestrating a Stateful application using Operator,**” Journal of University of Shanghai for Science and Technology, ISSN: 1007-6735, Volume: 23, Issue: 6, (Scopus Indexed), pp: 514-520, Jun 2021.

(Download Link: <https://jusst.org/wp-content/uploads/2021/06/Orchestrating-a-stateful-application-using-Operator.pdf>, <https://jusst.org/orchestrating-a-stateful-application-using-operator/>)

(WOS and Scopus Indexed-IEEE)

12. Kishan Maladkar, H V Ravish Aradhya, “**Design and Implementation of a 32-bit Floating Point Unit,**” International Journal for Research in Applied Science & Engineering Technology (IJRASET), ISSN: 2321-9653, Volume: 9, Issue: 6, pp: 731-736, Jun 2021.
(Download Link: <https://www.ijraset.com/files/serve.php?FID=35052>)
13. Karthik S Kulkarni, H V Ravish Aradhya, “**MAC Unit Optimization for Area, Power and Timing Constraints,**” International Journal for Research in Applied Science & Engineering Technology (IJRASET), ISSN: 2321-9653, Volume: 9, Issue: 6, pp: 731-736, Jun 2021.
(Download Link: <https://www.ijraset.com/files/serve.php?FID=34753>)
14. H V Ravish Aradhya, Neethu S, “**Detection of DDoS Attacks in SDN,**” ECS Transactions (IOPscience), ISSN: 1938-5862, Vol. 107, No. 1, pp: 18305-18313
<https://doi.org/10.1149/10701.18305ecst>, Jun 2021.

(ii) International Conferences: 16

1. Ravish Aradhya H. V, Arunkumar P Chavan, “**Design and Synthesis of Low Power, High Speed 5TH Order Digital Decimation Converter,**” in the proceedings of International conference on (ICCSP-2020), Dec-2019. (Scopus Indexed)
2. Ravish Aradhya H. V, Mohana, “**Real Time Implementation of Emotion Detection and Classification System for Autism Patients,**” in Easy chair pre-prints series, Paper No. 2207, Dec-2019. (Scopus Indexed and download link: <https://easychair.org/publications/preprint/m54w>)
3. Neethu S and Ravish Aradhya H V, “**Performance Analysis of Various SDN Controllers In Mininet Emulator,**” **SPRINGER**-International Conference On Electronic Systems And Intelligent Computing (ESIC2020), 2-4, March 2020, NIT Arunachal Pradesh, Yupia, India – 791112. (Scopus indexed).
4. Mohana, HV Ravish Aradhya, “**Performance Evaluation of Background Modeling Methods for Object and Tracking,**” 2020 Proceedings of the Fourth IEEE International Conference on Inventive Systems and Control (ICISC 2020), Chennai, India, ISBN: 978-1-7281-2812-2 (Scopus indexed).
5. Ravish Aradhya H. V, Ganesh Kumar M T, Madan H R, Sandeep Malik, “**Performance analysis of 8-bit Multiplier Architectures,**” in the proceedings of **SPRINGER** – International conference on Recent Trends in Science and Technology (ICRTST-2020), ATME College of Engineering, Mysuru – 570 028, 17-18 June 2020. **pp. 1139-1142, ISBN: 978-93-5396-830-4/18/\$31.00 © 2020 IEEE.** [Springer nature’ (Scopus indexed), IETE Journal of research (Scopus indexed)]

6. H V Ravish Aradhya, Manjunath K M, Muralidhara K N, **“N-bit Incrementer and Decrementer by a value 'M' Operation Circuit Design and Analysis for Various Parameters,”** International Web Conference on Smart Engineering Technologies- 2020. (IWCSET20), 26-27 June 2020, ISBN: 978-93-5407-648-0 (Scopus indexed).
7. Ravish Aradhya H. V, Shruthi Goudar, **“Development and Analysis of Parameters to Evaluate Design Partitioning of SOC,”** in the proceedings of **IEEE – 2nd** International Conference on Inventive Research in Computing Applications (ICIRCA-2020), RVS College of Engineering and Technology, Coimbatore, India – 641402, 15-17 July 2020. **pp. 1139-1142, ISBN: 978-1-7281-5374-2/18/\$31.00 © 2020 IEEE.** (Scopus indexed)
8. Ravish Aradhya H. V, Basaveswari Biradar, Robert Chan (Qualcom, USA), **“A Novel Technique to Optimize Regressions in L2 Cache Verification,”** in the proceedings of **IEEE – 2nd** International Conference on Inventive Research in Computing Applications (ICIRCA-2020), RVS College of Engineering and Technology, Coimbatore, India – 641402, 15-17 July 2020. **pp. 1139-1142, ISBN: 978-1-7281-5374-2/18/\$31.00 © 2020 IEEE.** (Scopus indexed)
9. Arunkumar P Chavan, Dr. H V Ravish Aradhya, **“Design and Synthesis of Low Power, High Speed 5th Order Digital Decimation Filter for Sigma- Delta Analog to Digital Converter,”** IEEE-International Conference on Communication and Signal Processing (ICCSP-2020), July 28-30, 2020, **Meghnad Saha Institute of Technology, Kolkata, India.** 978-1-7281-4987-5/20/\$31.00©2020 IEEE, pp. 92-96.
10. Kishan Maladkar, H V Ravish Aradhya, **“Design and Implementation of Automatic Gain Control Unit for Dual-Mode Bluetooth Modem,”** 6th International Conference on Communication and Electronics Systems (ICCES-2021), PPG Institute of Technology, Coimbatore, India, 8-10, July 2021. Paper ID: ICCES205, (Scopus Indexed-IEEE)
(Download Link: <https://www.icces>)
11. Karthik S Kulkarni, H V Ravish Aradhya, **“Dataset development of GPU block using ScanDump for Silicon Debug,”** 6th International Conference on Communication and Electronics Systems (ICCES-2021), PPG Institute of Technology, Coimbatore, India, 8-10, July 2021. Paper ID: ICCES431, (Scopus Indexed-IEEE)
(Download Link: <https://www.icces>)
12. H V Ravish Aradhya, Spoorthi G Gojanur, Juhie Fadnavis, **“Memory Design and Verification of SRAM-based Energy Efficient Ternary Content Addressable Memory,”** 11th IEEE-International Conference on Recent Engineering and Technology (ICRET-2021), CPET, Bengaluru, India, 20, June 2021. (Scopus Indexed-IEEE)
(Download Link: <https://www.icret>)
13. H V Ravish Aradhya, Kevin Mathew, Vivek T M, **“A Review of Next Generation NVM Technologies,”** 11th IEEE-International Conference on Recent Engineering and Technology (ICRET-2021), CPET, Bengaluru, India, 20, June 2021. (Scopus Indexed-IEEE)
(Download Link: <https://www.icret>)

14. H V Ravish Aradhya, Arun Kumar Chavan, Rohan A V, Pranshu Orian, and Preetham Kaka **“Design and Implementation of Automated Industrial Robots in Cylinder Liner Production Lines,”** IEEE- 2021 Asian Conference on Innovation in Technology (ASIANCON), PCCOER, Pune, India, 28 - 29 Aug 2021. (WOS and Scopus Indexed-IEEE)
(Download Link: <https://ieeexplore.ieee.org/document/9545017>)
15. M. R. Govinda, H V Ravish Aradhya, **“Comparison of Power Dissipation for CMOS and FinFET Dependent 6T SRAM at 18 nm Technology,”** 2021 International Conference on Recent Trends on Electronics, Information, Communication & Technology (RTEICT-2021), SVCE, Bengaluru, 27-28 Aug. 2021. (Scopus Indexed) pp. 975-979, doi: 10.1109/RTEICT52294.2021.9573749.
(Download Link: <https://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=9573749>)
16. H.V. Ravish Aradhya, Neethu S., **“Approaches to providing Quality of Service (QoS) in Software-Defined Networking,”** International Conference on Technologies for Smart Green Connected Society 2021 (ICTSGS-1-2021) conference led by Yamagata University Japan, Nov, 29-30, 2021.

PhD Forum – Presentations:

1. Mohana, Ravish Aradhya H. V, **“Elegant and Efficient Algorithms for Real Time Implementation of Object Detection, Classification, Tracking and Counting using FPGA Zynq XC7Z020 for Automated Video Surveillance Applications,”** 10th IEEE Advanced Networks and Telecommunications Systems (ANTS-2016), 6-9 November 2016, J. N. Tata Auditorium, *Indian Institute of Science* (IISc), Bangalore, India.
2. Mohana, Ravish Aradhya H. V, **“Design and Implementation of Object Detection, Classification, Tracking, Counting and Classification Algorithms using Artificial Intelligence for Automated Video Surveillance Applications,”** 24th International Conference on Advanced Computing Communications (ADCOM-2018), 21st – 23rd September 2018, at the IIIT-Bangalore, India.
3. Mohana, Ravish Aradhya H. V, **“Design of Efficient Algorithms for Video Surveillance Applications using Artificial Intelligence,”** 25th IEEE Advanced Computing and Communications Conference (ADCOM 2019), 5-7 September 2019, at International Institute of Information Technology (IIIT-B), Bangalore, India.

Awards and Appreciations:

1. Recipient of **“Best Research Paper Award”** from *International Academy of Science*, Engineering and Technology in International Journal of Electronics and Communication Engineering (IJECE), ISSN: 2278-991X, Dec-2014. IF-3.329, for the paper titled **“Design and Optimization of Reversible carry look ahead Adder Circuit.”** (PhD research outcome).

2. Recipient of “**Best Research Paper Award**” from *IEEE-International Conference on Engineering and Technology* (ICETECH-2015), Mar-2015. IF-3.329, for the paper titled “**Design of square spiral Nano-antenna in infra-red region for Solar Energy Harvesting,**” conducted by Rathinam Institute of Technology, Coimbatore, Tamilnadu, India. IEEE Explorer: 978-1-4799-7678-2/15/\$31.00, ©2015 IEEE, (Conference Record Number: #35961), 20th March 2015, pp:1026-1031.
3. Recipient of “**Best Research Paper Award**” from *IEEE-International Conference on ‘Innovative Mechanisms for Industry Applications’* (ICIMIA-2017), 21-23 Feb 2017, for the paper titled “**Implementation of Highly Efficient Sorting Algorithm for Median Filtering using FPGA Spartan-6,**” conducted by Dayananda Sagar College of Engineering, Bengaluru, India. 978-1-5090-5960-7/17/\$-31.00, ©2017 IEEE, ISBN: 978-1-5090-5959-1, (Conference Record Number: #35961), 21-23 Feb 2017, pp. 265-269.

Best Teacher Awards:

1. Recipient of “Best Teacher Award” from Carrier Launcher (India), Bangalore, May 2006
2. Recipient of “Best Teacher Award” from ISTE-RVCE Chapter, Sep-2019